# NST857BDP6T5G

# **Dual General Purpose Transistor**

The NST857BDP6T5G device is a spin-off of our popular SOT-23/SOT-323/SOT-563 three-leaded device. It is designed for general purpose amplifier applications and is housed in the SOT-963 six-leaded surface mount package. By putting two discrete devices in one package, this device is ideal for low-power surface mount applications where board space is at a premium.

#### **Features**

- h<sub>FE</sub>, 220-475
- Low  $V_{CE(sat)}$ ,  $\leq 0.3 \text{ V}$
- Simplifies Circuit Design
- Reduces Board Space
- Reduces Component Count
- This is a Pb-Free Device

#### **MAXIMUM RATINGS**

Rating		Symbol	Value	Unit
Collector - Emitter Voltage		V <sub>CEO</sub>	-45	Vdc
Collector - Base Voltage		V <sub>CBO</sub>	-50	Vdc
Emitter – Base Voltage		$V_{EBO}$	-6.0	Vdc
Collector Current - Continuous		I <sub>C</sub>	-100	mAdc
Electrostatic Discharge	HBM MM	ESD Class	2 B	

#### THERMAL CHARACTERISTICS

Characteristic (Single Heated)	Symbol	Max	Unit
Total Device Dissipation T <sub>A</sub> = 25°C Derate above 25°C (Note 1)	P <sub>D</sub>	240 1.9	mW mW/°C
Thermal Resistance, Junction-to-Ambient (Note 1)	$R_{\theta JA}$	520	°C/W
Total Device Dissipation T <sub>A</sub> = 25°C Derate above 25°C (Note 2)	P <sub>D</sub>	280 2.2	mW mW/°C
Thermal Resistance, Junction-to-Ambient (Note 2)	$R_{\theta JA}$	446	°C/W
Characteristic (Dual Heated) (Note 3)	Symbol	Max	Unit
Total Device Dissipation T <sub>A</sub> = 25°C Derate above 25°C (Note 1)	P <sub>D</sub>	350 2.8	mW mW/°C
Thermal Resistance, Junction-to-Ambient (Note 1)	$R_{\theta JA}$	357	°C/W
Total Device Dissipation T <sub>A</sub> = 25°C Derate above 25°C (Note 2)	P <sub>D</sub>	420 3.4	mW mW/°C
Thermal Resistance, Junction-to-Ambient (Note 2)	$R_{\theta JA}$	297	°C/W
Junction and Storage Temperature Range	T <sub>J</sub> , T <sub>stg</sub>	-55 to +150	°C

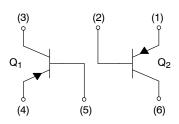
Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

- 1. FR-4 @ 100 mm<sup>2</sup>, 1 oz. copper traces, still air. 2. FR-4 @ 500 mm<sup>2</sup>, 1 oz. copper traces, still air.
- 3. Dual heated values assume total power is sum of two equally powered channels.



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NST857BDP6T5G



SOT-963 CASE 527AD

#### **MARKING DIAGRAM**



K = Device Code = Date Code

#### **ORDERING INFORMATION**

Device	Package	Shipping <sup>†</sup>
NST857BDP6T5G	SOT-963 (Pb-Free)	8000/Tape & Reel

†For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

## NST857BDP6T5G

## **ELECTRICAL CHARACTERISTICS** (T<sub>A</sub> = 25°C unless otherwise noted)

Characteristic	Symbol	Min	Тур	Max	Unit
OFF CHARACTERISTICS	<u>.</u>		-		•
Collector – Emitter Breakdown Voltage (I <sub>C</sub> = -10 mA)	V <sub>(BR)CEO</sub>	-45	_	-	V
Collector – Emitter Breakdown Voltage ( $I_C = -10 \mu A, V_{EB} = 0$ )	V <sub>(BR)CES</sub>	-50	-	-	V
Collector – Base Breakdown Voltage ( $I_C = -10 \mu A$ )	V <sub>(BR)CBO</sub>	-50	-	-	V
Emitter – Base Breakdown Voltage (I <sub>E</sub> = –1.0 μA)	V <sub>(BR)EBO</sub>	-5.0	-	-	V
Collector Cutoff Current ( $V_{CB} = -30 \text{ V}$ ) ( $V_{CB} = -30 \text{ V}$ , $T_A = 150 ^{\circ}\text{C}$ )	I <sub>CBO</sub>	_ _	_ _	-15 -4.0	nA μA
ON CHARACTERISTICS		•		•	1
DC Current Gain $ \begin{pmatrix} I_C = -10 \ \mu\text{A}, \ V_{CE} = -5.0 \ \text{V} \end{pmatrix} $ $ \begin{pmatrix} I_C = -2.0 \ \text{mA}, \ V_{CE} = -5.0 \ \text{V} \end{pmatrix} $	h <sub>FE</sub>	- 220	150 290	- 475	_
Collector – Emitter Saturation Voltage ( $I_C = -10$ mA, $I_B = -0.5$ mA) ( $I_C = -100$ mA, $I_B = -5.0$ mA)	V <sub>CE(sat)</sub>	- -	- -	-0.3 -0.7	V
Base – Emitter Saturation Voltage ( $I_C = -10$ mA, $I_B = -0.5$ mA) ( $I_C = -100$ mA, $I_B = -5.0$ mA)	V <sub>BE(sat)</sub>	- -	-0.7 -0.9	- -	V
Base – Emitter On Voltage ( $I_C = -2.0$ mA, $V_{CE} = -5.0$ V) ( $I_C = -10$ mA, $V_{CE} = -5.0$ V)	V <sub>BE(on)</sub>	-0.6 -	- -	-0.75 -0.82	V
SMALL-SIGNAL CHARACTERISTICS	·				
Current – Gain – Bandwidth Product ( $I_C = -10$ mA, $V_{CE} = -5.0$ Vdc, $f = 100$ MHz)	f <sub>T</sub>	100	-	-	MHz
Output Capacitance (V <sub>CB</sub> = -10 V, f = 1.0 MHz)	C <sub>obo</sub>	-	_	4.5	pF
Input Capacitance ( $V_{EB} = -0.5 \text{ V}, f = 1.0 \text{ MHz}$ )	C <sub>ibo</sub>	_	_	10	pF
Noise Figure (I <sub>C</sub> = $-0.2$ mA, V <sub>CE</sub> = $-5.0$ Vdc, R <sub>S</sub> = $2.0$ k $\Omega$ , f = $1.0$ kHz, BW = $200$ Hz)	NF	-	_	10	dB

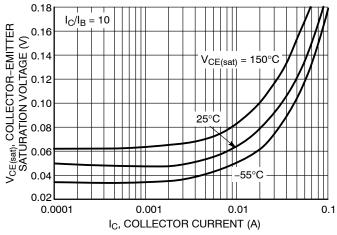


Figure 1. Collector Emitter Saturation Voltage vs.
Collector Current

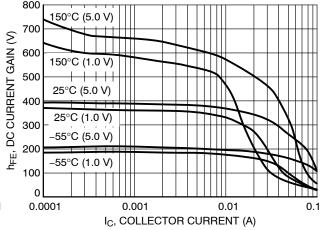


Figure 2. DC Current Gain vs. Collector Current

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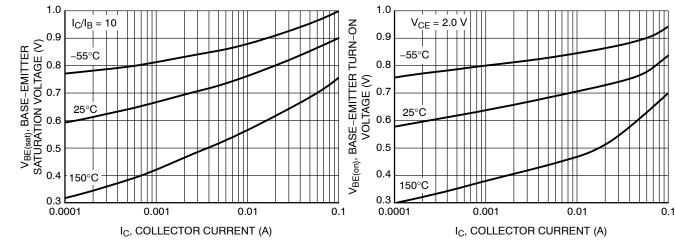


Figure 3. Base Emitter Saturation Voltage vs. **Collector Current** 

Figure 4. Base Emitter Turn-On Voltage vs. **Collector Current** 

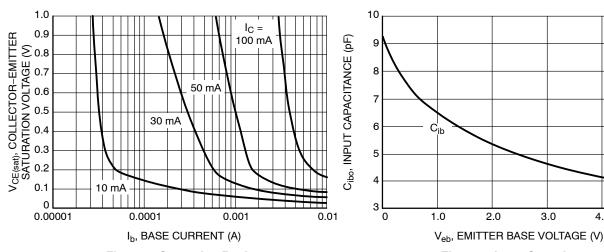


Figure 5. Saturation Region

Figure 6. Input Capacitance

4.0

5.0

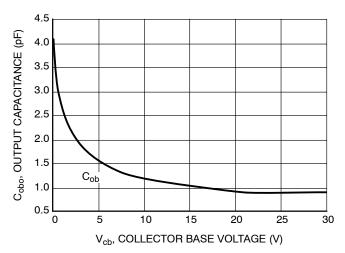


Figure 7. Output Capacitance

## **MECHANICAL CASE OUTLINE**

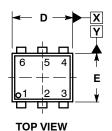


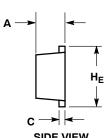


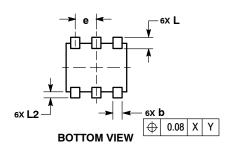
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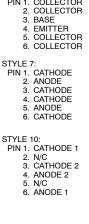




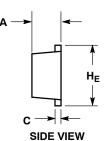




STYLE 1: PIN 1. EMITTER 1 2. BASE 1 3. COLLECTOR 2 4. EMITTER 2 5. BASE 2 6. COLLECTOR 1
STYLE 4: PIN 1. COLLECTOR 2. COLLECTOR 3. BASE 4. EMITTER 5. COLLECTOR 6. COLLECTOR
STYLE 7: PIN 1. CATHODE 2. ANODE 3. CATHODE 4. CATHODE 5. ANODE 6. CATHODE



STYLE 2: PIN 1. EMITTER 1 2. EMITTER2 3. BASE 2 4. COLLECTOR 2 5. BASE 1 6. COLLECTOR 1	STYLE 3: PIN 1. CATHODE 1 2. CATHODE 1 3. ANODE/ANODE 2 4. CATHODE 2 5. CATHODE 2 6. ANODE/ANODE 1
STYLE 5:	STYLE 6:
PIN 1. CATHODE 2. CATHODE	PIN 1. CATHODE 2. ANODE
3. ANODE	3. CATHODE
4. ANODE	4. CATHODE
5. CATHODE 6. CATHODE	5. CATHODE 6. CATHODE
STYLE 8:	STYLE 9:
PIN 1. DRAIN	PIN 1. SOURCE 1
2. DRAIN 3. GATE	2. GATE 1 3. DRAIN 2
4. SOURCE	4. SOURCE 2
5. DRAIN	5. GATE 2
6. DRAIN	6. DRAIN 1



## NOTES:

- DIMENSIONING AND TOLERANCING PER ASME
- DIMENSIONING AND TOLEHANCING PER ASM Y14.5M, 1994.
   CONTROLLING DIMENSION: MILLIMETERS
   MAXIMUM LEAD THICKNESS INCLUDES LEAD FINISH THICKNESS. MINIMUM LEAD THICKNESS IS THE MINIMUM THICKNESS OF BASE MATERIAL
- 4. DIMENSIONS D AND E DO NOT INCLUDE MOLD FLASH, PROTRUSIONS, OR GATE BURRS.

	MILLIMETERS		
DIM	MIN	NOM	MAX
Α	0.34	0.37	0.40
b	0.10	0.15	0.20
С	0.07	0.12	0.17
D	0.95	1.00	1.05
E	0.75	0.80	0.85
е	0.35 BSC		
HE	0.95	1.00	1.05
L	0.19 REF		
L2	0.05	0.10	0.15

#### **GENERIC MARKING DIAGRAM\***

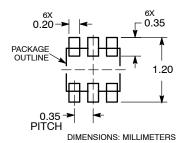


= Specific Device Code = Month Code Μ

\*This information is generic. Please refer to device data sheet for actual part marking.

Pb-Free indicator, "G" or microdot " ■", may or may not be present.

## **RECOMMENDED MOUNTING FOOTPRINT**



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