

SAMXON BRAND ALUMINUM ELECTROLYTIC CAPACITORS

PRODUCT SPECIFICATION 規格書

CUSTOMER: DATE:

(客戶): 志盛翔 (日期): 2017-06-09

CATEGORY (品名) : ALUMINUM ELECTROLYTIC CAPACITORS

DESCRIPTION (型号) : GT 100V220μF(φ12.5X20)

VERSION (版本) : 01

Customer P/N :

SUPPLIER :

SUPPLI	ER
PREPARED (拟定)	CHECKED (审核)
李婷	刘渭清

CUS	TOMER
APPROVAL (批准)	SIGNATURE (签名)

ELECTROLYTIC CAPACITOR SPECIFICATION GT SERIES

		SPECIFICAT			ALTERNA	ATION HIS ECORDS	TORY
		GT SERIE	ES				
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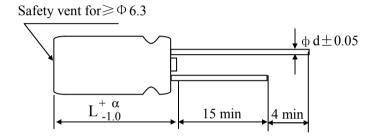
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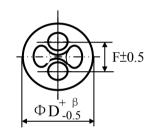
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Table 1 Product Dimensions and Characteristics

Unit: mm





α	L<20 : α=1.5; L≥20 : α=2.0
β	$\Phi D < 20 : \beta = 0.5; \Phi D \ge 20 : \beta = 1.0$

* If it is flat rubber, there is no bulge from the flat rubber surface.

N	SAMXON	WV	Cap.	Com tolomon on	Temp.	tanδ (120Hz,	Leakage	Max Ripple Current	Impedance at 20°C	Load lifetime		nsion nm)		Sleev
ο.	Part No.	(Vdc)	(μF)	Cap. tolerance	range(°C)	(120Hz, 20℃)	Current (µA,2min)	at 105℃ 100kHz (mA rms)	10kHz (Ωmax)	(Hrs)	$D \times L$	F	фd	e
1	EGT227M2AI20RR**P	100	220	-20%~+20%	-40~105	0.08	220	800	0.128	10000	12.5X20	5.0	0.6	PET

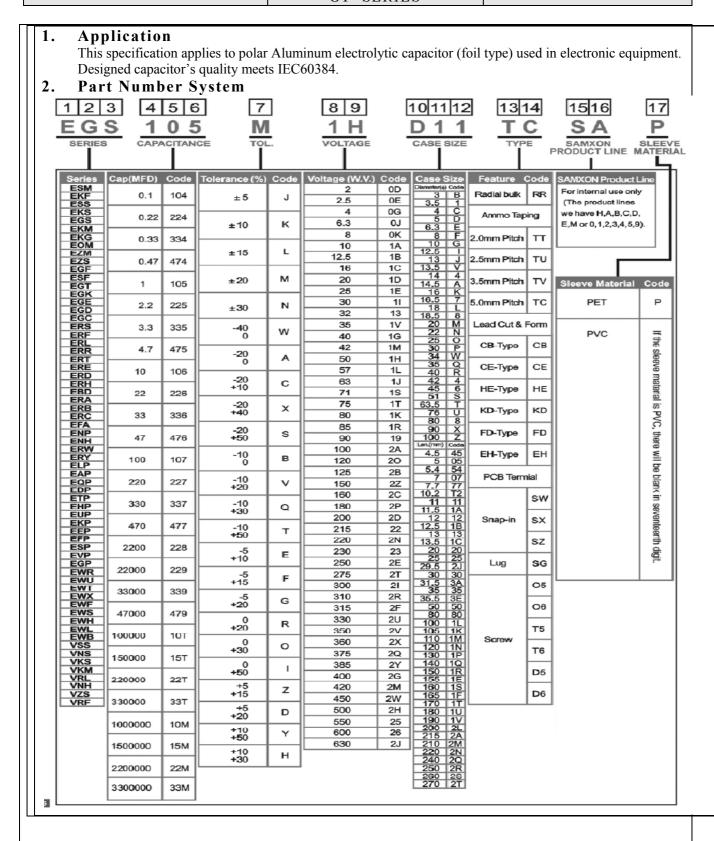
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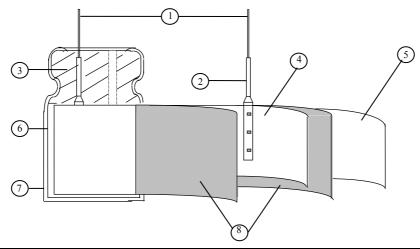


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3. Construction

Single ended type to be produced to fix the terminals to anode and cathode foil, and wind together with paper, and then wound element to be impregnated with electrolyte will be enclosed in an aluminum case. Finally sealed up tightly with end seal rubber, then finished by putting on the vinyl sleeve.



	Component	Material
1	Lead line	Tinned CP wire (Pb Free)
2	Terminal	Aluminum wire
3	Sealing Material	Rubber
4	Al-Foil (+)	Formed aluminum foil
5	Al-Foil (-)	Etched aluminum foil or formed aluminum foil
6	Case	Aluminum case
7	Sleeve	PET
8	Separator	Electrolyte paper

4. Characteristics

Standard atmospheric conditions

Unless otherwise specified, the standard range of atmospheric conditions for making measurements and tests are as follows:

Ambient temperature :15°C to 35°C Relative humidity : 45% to 85% Air Pressure : 86kPa to 106kPa

If there is any doubt about the results, measurement shall be made within the following conditions:

Ambient temperature $: 20^{\circ}\text{C} \pm 2^{\circ}\text{C}$ Relative humidity : 60% to 70%Air Pressure : 86kPa to 106kPa

Operating temperature range

The ambient temperature range at which the capacitor can be operated continuously at rated voltage See table 1 temperature range.

As to the detailed information, please refer to table 2.

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	ITEM				PERFC	RMANC	Έ			
	Rated voltage									
	(WV)	WV (V.DC)	6.3	10	16	25	35	50	63	100
4.1		SV (V.DC)	8	13	20	32	44	63	79	125
	Surge voltage (SV)									
4.2	Nominal capacitance (Tolerance)	Condition> Measuring F Measuring V Measuring T Criteria> Shall be with	requency oltage emperat	: N ture : 20)±2℃	han 0.5V				
4.3	Leakage current	Condition> Connecting t minutes, and Criteria> Refer to Table	he capao then, me				istor (1	lkΩ±10	OΩ) in s	eries for
4.4	tan δ	<condition> See 4.2, Norn <criteria> Refer to Table</criteria></condition>	n Capac	itance, fo	or measui	ring frequ	ency, vo	oltage and	d tempera	nture.
4.5	Terminal strength	0.5r Over 0.	ength of capacitor rength of apacitor, 2~3 second rength of apacitor, 2 mm and 1 mm and 1 mm and 1 mm and 2 mm	f Termina applied f onds, and d wire	Tens	ent the tent it for 9 ile force (kgf) 5 (0.51) 0 (1.0)	rminal (0° to its	1~4 mm toriginal p Bending (kg 2.5 (0)	from the position of the posit	rubber) for within 2~

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		<condition></condition>								
		STEP	Testi	ng Tempe	rature(°C)			Time		
		1		20 ± 2	2	Time	to reach	thermal o	equilibrii	ım
		2		-40(-25)	± 3	Time	to reach	thermal o	equilibrii	ım
		3		20 ± 2	2	Time	to reach	thermal o	equilibrii	ım
		4		105±	2	Time	to reach	thermal o	equilibri	ım
		5		20 ± 2	2	Time	to reach	thermal o	equilibri	ım
		<criteria></criteria>								
		a. tan δ shall be	e with	in the lim	it of Item	4.4The le	eakage cu	ırrent me	easured s	hall not
	T	more than 8 time		-						
	Temperature characteristi	b. In step 5, tar			nin the lin	it of Iter	n 4.4The	leakage	current	shall no
4.6	characteristi	more than the sp								
1.0	C 3	c. At-40°C (-25 table.	(°C), ii	mpedance	(z) ratio s	hall not	exceed th	e value o	of the fol	lowing
		Working Voltage	e (V)	6.3	10	16	25	35	50	63
		Z-25°C/Z+20°	$^{\circ}$	4	3	2	2	2	2	2
		Z-40°C/Z+20°	$^{\circ}$	8	6	4	3	3	3	3
		TT 1' T 1	(7.7)	100	1					·
		Working Voltage Z-25°C/Z+20°C		100						
			_	2						
		Z-40°C/Z+20°	C	3						
		E	1	> 1000 m	C 1110	-	41 1000) E f	7 15/7	20°C
		For capacitance	value	> 1000 µ		-				
		For capacitance Capacitance, tan			Add 1.0	per anot	her 1000	μF for 2		
		Capacitance, tan			Add 1.0	per anot	her 1000	μF for 2		
		-	δ , and	d impedan	Add 1.0	per anote measur	her 1000 ed at 120	μF for Z Hz.	Z-40°C/Z	Z+20°C.
		Capacitance, tan	δ, and	d impedan	Add 1.0 ace shall b	per anote measures, The ca	her 1000 ed at 120 pacitor is	μ F for Z Hz.	Z-40°C/Z	Z+20°C.
		Capacitance, tan Condition> According to IEC 105°C ±2 with DC and ripple p	δ, and C6038 DC bi peak v	d impedan 34-4No.4. as voltage voltage sh	Add 1.0 ace shall b	b per anote measures, The carated ripp	pacitor is le current rated w	F for Z Hz. s stored a t for Tab	at a tempole 1. (The voltage)	Z+20°C. erature of the sum of then the
		Capacitance, tan Condition> According to IEC 105°C ±2 with DC and ripple product should b	δ, and C6038 DC bit peak when tested	d impedan 34-4No.4 as voltage voltage shed after 16	Add 1.0 ace shall be a shall be a shall be a shall not explus the reall not explusive shours recommended.	b per anote measures, The carated ripp	pacitor is le current rated w	F for Z Hz. s stored a t for Tab	at a tempole 1. (The voltage)	Z+20°C. erature of the sum of then the sum of the sum
	Load	Capacitance, tan experience of the condition of the condi	δ, and C6038 DC bit peak when tested	d impedan 34-4No.4 as voltage voltage shed after 16	Add 1.0 ace shall be a shall be a shall be a shall not explus the reall not explusive shours recommended.	b per anote measures, The carated ripp	pacitor is le current rated w	F for Z Hz. s stored a t for Tab	at a tempole 1. (The voltage)	Z+20°C. erature of the sum of the the the sum of the the sum of the the the sum of the the the sum of the the sum of the
4.7	life	Capacitance, tan experience of the condition of the condi	δ, and C6038 DC bi peak voe teste eet the	d impedan 34-4No.4 as voltage voltage shed after 16 following	Add 1.0 ace shall be a shall be a shall be a shall not explus the mall not explain the shours recognized table:	b per anote measures, The carated ripp acceed the covering	pacitor is le curren e rated with a tat	F for Z Hz. s stored a t for Tab	at a tempole 1. (The voltage)	Z+20°C. erature of the sum of then the sum of the sum
4.7		Capacitance, tan Condition> According to IEC 105°C ±2 with DC and ripple product should be result should me Criteria> The characterist	δ, and C6038 DC bi peak voce testo eet the tic sha	d impedan 34-4No.4 as voltage voltage shed after 16 following	Add 1.0 ace shall be a shall be a shall not explus the mall not explusive the shours recognized table:	by per another measures. The capated ripper acced the covering are grequire	pacitor is le current rated whime at at ments.	F F for 2 Hz. s stored a t for Tab yorking y	at a tempole 1. (The voltage)	Z+20°C. erature of the sum of the the the sum of the the sum of the the the sum of the the the sum of the the sum of the
4.7	life	Capacitance, tan Condition> According to IEC 105°C ±2 with DC and ripple product should be result should me Criteria> The characterist Leakage C	δ, and C6038 DC bi peak whose teste the circ sha curren	d impedan 34-4No.4 as voltage voltage shed after 16 following	Add 1.0 ace shall be a shall be a shall not explus the mall not explusive following table: e following Value in	b per anote measures, The caracted ripp acced the covering grequire 4.3 shall	pacitor is le curren e rated with at at ments.	F for A Hz. s stored a t for Tab yorking w mospher	at a tempole 1. (The voltage)	Z+20°C. erature of the sum of the the the sum of the the sum of the the the sum of the the the sum of the the sum of the
4.7	life	Capacitance, tan Condition> According to IEC 105°C ±2 with DC and ripple product should be result should me Criteria> The characterist Leakage of Capacitan	δ, and C6038 DC bi peak whose teste the circ sha curren	d impedan 34-4No.4 as voltage voltage shed after 16 following	Add 1.0 ace shall be 13 method e plus the reall not explusive following table: e following Value in Within	by per another measures, The capated ripper acced the covering are quire 4.3 shall a 25% of	pacitor is le curren e rated writine at at ments.	F for A Hz. s stored a t for Tab yorking y mospher	z-40°C/z	Z+20°C. erature of the sum of the the the sum of the the sum of the the the sum of the the the sum of the the sum of the
4.7	life	Capacitance, tan experience of the characterist tan δ	δ, and C6038 DC bi peak we teste the tic sha currennce Ch	d impedan 34-4No.4 as voltage voltage shed after 16 following	Add 1.0 ace shall be 13 method e plus the reall not explusive following table: e following Value in Within 15 Not more	g require 4.3 shall 25% of than 200	pacitor is le curren e rated with ments. be satisficinitial value of the control of the curren at at the current at	F for 20 PHz. S stored at for Tabyorking with mospher steel alue.	z-40°C/z at a tempole 1. (The voltage) ic condite and value.	Z+20°C. erature of the sum of the the the sum of the the sum of the the the sum of the the the sum of the the sum of the
4.7	life	Capacitance, tan Condition> According to IEC 105°C ±2 with DC and ripple product should be result should me Criteria> The characterist Leakage of Capacitan	δ, and C6038 DC bi peak we teste the tic sha currennce Ch	d impedan 34-4No.4 as voltage voltage shed after 16 following	Add 1.0 ace shall be 13 method e plus the reall not explusive following table: e following Value in Within	g require 4.3 shall 25% of than 200	pacitor is le curren e rated with ments. be satisficinitial value of the control of the curren at at the current at	F for 20 PHz. S stored at for Tabyorking with mospher steel alue.	z-40°C/z at a tempole 1. (The voltage) ic condite and value.	Z+20°C. erature of the sum of then the sum of the sum
4.7	life	Capacitance, tan experience of the condition of the cond	δ, and C6038 DC bi peak we teste the tic sha currennce Ch	d impedan 34-4No.4 as voltage voltage shed after 16 following	Add 1.0 ace shall be 13 method e plus the reall not explusive following table: e following Value in Within 15 Not more	g require 4.3 shall 25% of than 200	pacitor is le curren e rated with ments. be satisficinitial value of the control of the curren at at the current at	F for 20 PHz. S stored at for Tabyorking with mospher steel alue.	z-40°C/z at a tempole 1. (The voltage) ic condite and value.	Z+20°C. erature of the sum of then the sum of the sum
4.7	life	Capacitance, tan experience of the characterist tan \$\delta\$ Condition According to IEC 105°C ±2 with 100 DC and ripple product should be result should me excriteria. The characterist Leakage of Capacitan tan \$\delta\$ Appearan	C6038 DC bi peak voe teste eet the tic sha curren nce Ch	d impedan 84-4No.4. as voltage shed after 16 following Il meet the than ange	Add 1.0 ace shall be 13 method e plus the reall not explusive following table: e following Value in Within Explusive Shall not more the shall not explusive following table:	g require 4.3 shall 25% of than 200 all be no	pacitor is le curren e rated with ments. be satisficial value of the leakage of	s stored at for Tabyorking windsphere	z-40°C/z at a tempole 1. (The voltage) ic condite to the desired value.	erature on the sum of the thions. The
4.7	life	Capacitance, tan experience of the condition of the cond	δ, and C6038 DC bi peak whose teste the circ shall current ce Chance	d impedan 84-4No.4 as voltage shed after 16 following Il meet the than ange	Add 1.0 ace shall be 13 method e plus the reall not explusive following table: e following Value in Within 1 Not more there should be 1.0 method with the s	g require 4.3 shall 25% of than 200 all be no	pacitor is le curren e rated wrime at at ments. be satisficinitial value of the leakage of the data at a term at a t	s stored at for Tabyorking wimospher	z-40°C/z at a tempole 1. (The voltage) ric condite to the distriction of the condite to the con	erature of the sum of the fine the ions. The
4.7	life	Capacitance, tan experience of the capacitance of the capacitans are capacitans	δ, and C6038 DC bi peak whose teste the circ share current ce Chance ce then s. Follows	d impedan 84-4No.4 as voltage woltage shed after 16 following Ill meet the the the the the the the the the	Add 1.0 ace shall be a shall not explus the mall not explus the mall not expluse the following table: The following table: Within I with the mall not expluse in the more shall not expluse the following table: Within I with the mall not more shall no voltage in the more shal	g require 4.3 shall 25% of than 200 all be no	pacitor is le curren e rated wrime at at ments. be satisficinitial value of the leakage of the data at a tertors shall	F for APHZ. S stored at for Table orking with mospher and the specific of electrons and the specific of the s	z-40°C/z at a tempole 1. (The voltage) ic condite to the desired value. and value. and value. and value of the voltage of 105 oved from the voltage of 105 ov	±2°C form the test
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4.7	life test Shelf life	Capacitance, tan experience of the capacitans and be shall be connect applied for 30min.	δ, and C6038 DC bit peak whose tested the current certain cer	d impedants 34-4No.4. as voltage shed after 16 following Ill meet the thange stored wire lowing this yed to stale a series limited as seri	Add 1.0 ace shall be a plus the pall not expected to hours ready table: The following within a subject to hours ready table: Within a subject to hours ready table: There should be a subject to hours ready table: Within a subject to hours ready table.	g require 4.3 shall 25% of than 200 alge application capacitation tensistor (11)	pacitor is le curren e rated wrime at at ments. be satisficinitial various of the leakage of the tors shall apperature to ± 100 Ω	ied alue. e specifie of electro mperatur l be reme e for 4~8) with I	z-40°C/z at a tempole 1. (The voltage) ic condite con	±2°C for the test Next the d voltage
	life test Shelf	Capacitance, tan experience of the capacitans are conditions. Conditions are capacitans are ca	δ, and C6038 DC bit peak whose tested the current certain cer	d impedants 34-4No.4. as voltage shed after 16 following Ill meet the thange stored wire lowing this yed to stale a series limited as seri	Add 1.0 ace shall be a plus the pall not expected to hours ready table: The following within a subject to hours ready table: Within a subject to hours ready table: There should be a subject to hours ready table: Within a subject to hours ready table.	g require 4.3 shall 25% of than 200 alge application capacitation tensistor (11)	pacitor is le curren e rated wrime at at ments. be satisficinitial various of the leakage of the tors shall apperature to ± 100 Ω	ied alue. e specifie of electro mperatur l be reme e for 4~8) with I	z-40°C/z at a tempole 1. (The voltage) ic condite con	±2°C fon the test
	life test Shelf life	Capacitance, tan experience of the capacitans and be shall be connect applied for 30min.	δ, and C6038 DC bit peak whose tested the current certain cer	d impedants 34-4No.4. as voltage shed after 16 following Ill meet the thange stored wire lowing this yed to stale a series limited as seri	Add 1.0 ace shall be a plus the pall not expected to hours ready table: The following within a subject to hours ready table: Within a subject to hours ready table: There should be a subject to hours ready table: Within a subject to hours ready table.	g require 4.3 shall 25% of than 200 alge application capacitation tensistor (11)	pacitor is le curren e rated wrime at at ments. be satisficinitial various of the leakage of the tors shall apperature to ± 100 Ω	ied alue. e specifie of electro mperatur l be reme e for 4~8) with I	z-40°C/z at a tempole 1. (The voltage) ic condite con	±2°C for the test Next the d voltage
	life test Shelf life	Capacitance, tan experience of the capacitans and be shall be connect applied for 30min.	δ, and C6038 DC bit peak whose tested the current certain cer	d impedants 34-4No.4. as voltage shed after 16 following Ill meet the thange stored wire lowing this yed to stale a series limited as seri	Add 1.0 ace shall be a plus the pall not expected to hours ready table: The following within a subject to hours ready table: Within a subject to hours ready table: There should be a subject to hours ready table: Within a subject to hours ready table.	g require 4.3 shall 25% of than 200 alge application capacitation tensistor (11)	pacitor is le curren e rated wrime at at ments. be satisficinitial various of the leakage of the tors shall apperature to ± 100 Ω	ied alue. e specifie of electro mperatur l be reme e for 4~8) with I	z-40°C/z at a tempole 1. (The voltage) ic condite con	±2°C for the test Next the d voltage

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		<criteria></criteria>	1 6 11
		The characteristic shall meet t	
	Shelf	Leakage current	Value in 4.3 shall be satisfied
4.8	life	Capacitance Change	Within $\pm 25\%$ of initial value.
7.0	test	tan δ	Not more than 200% of the specified value.
		Appearance	There shall be no leakage of electrolyte.
		increase. Please apply voltage	stored more than 1 year, the leakage current may e through about 1 k Ω resistor, if necessary.
			e 15~35℃.
4.0	Surge		Not more than the specified value.
4.9	test	Leakage current	•
		Capacitance Change	Within $\pm 15\%$ of initial value.
		tan δ	Not more than the specified value.
		Appearance	There shall be no leakage of electrolyte.
		Attention:	
			ge at abnormal situation only. It is not applicable to suc
		over voltage as often applied	
4.10	Vibration test	perpendicular directions. Vibration frequency ra Peak to peak amplitude Sweep rate Mounting method:	: 1.5mm : $10\text{Hz} \sim 55\text{Hz} \sim 10\text{Hz}$ in about 1 minute treater than 12.5mm or longer than 25mm must be fixe
		Appearance of	To be soldered tems shall be tested: No intermittent contacts, open or short circuiting. No damage of tab terminals or electrodes. No mechanical damage in terminal. No leakage of electrolyte or swelling of the case. The markings shall be legible.

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		<condition></condition>		
		The capacitor shall be tested ur	nder the following	conditions:
		Soldering temperature	: 245±3°C	conditions.
		Dipping depth	: 243±3 C	
	Solderability	Dipping speed	: 25±2.5mm	/a
4.11	test		: 3±0.5s	// S
	icst	Dipping time < Criteria >	. 3±0.38	
		Criteria>	A minimum	n of 95% of the surface being
		Coating quality	immersed	if of 93% of the surface being
			minicised	
		<condition></condition>		
		Terminals of the capacitor shall	l be immersed into	o solder bath at 260±5°C for 1
		1seconds or $400 \pm 10^{\circ}$ C for 3^{+1}_{-0}	seconds to 1.5~2.0	mm from the body of capacito
		Then the capacitor shall be left		
	Resistance to	for 1~2 hours before measurem		competatare and normal narma
4.12	solder heat	<pre><criteria></criteria></pre>		
7.12	test	Leakage current	Not more than t	he specified value.
		Capacitance Change	Within ±10% o	_
		tan δ	Not more than t	he specified value.
		Appearance		o leakage of electrolyte.
				<i>S y</i>
		<condition></condition>		
		Temperature Cycle: According		
		placed in an oven, the condition		
		Temper	ature	Time
		(1)+20°C		≤3 Minutes
	Change of	(2)Rated low temperature	(-40°C) (-25°C)	30 ± 2 Minutes
4.13	temperature	(3)Rated high temperature	(+105°C)	30 ± 2 Minutes
1.15	test	(1) to (3)=1 cycle, total 5 c	evcle	
		<criteria></criteria>	.,,	
		The characteristic shall meet th	e following require	ement
			Not more than the s	
	1		Not more than the s	<u> </u>
				akage of electrolyte.
		**	nere shan be no le	akage of electrolyte.
	1	<condition></condition>		
		Humidity Test:		
		According to IEC60384-4No.4		
		hours in an atmosphere of 90~9		C, the characteristic change sh
		meet the following requiremen	t.	
	1	<criteria></criteria>		
4.14	Damp heat		more than the spec	
4.14	test	1	hin $\pm 20\%$ of initi	
	1	tan δ Not	more than 120% o	of the specified value.
		Appearance The	re shall be no leak	age of electrolyte.

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4.15	Vent test	22.4 or less Over 22.4 <criteria> The vent shall operate with no pieces of the capacitor and/or or other capacitor.</criteria>	ith its poable is aparent (A) 1 10 dangero	plarity revoplied.	versed to	a DC po		Then a
	Maximum	Condition> The maximum permissible r at 120Hz and can be applied Table-1 The combined value of D.C rated voltage and shall not r Frequency Multipliers: Coefficient Freq. (Hz) Cap. (μ F)	d at maxi voltage	mum ope	erating te	mperatur	e	ceed the
	permissible	15~33	0.45	0.55	0.70	0.90	1.00	
4.16	(ripple	39~330	0.60	0.70	0.85	0.95	1.00	
	current)	390~1000	0.65	0.75	0.90	0.98	1.00	
		1200~3900	0.75	0.80	0.95	1.00	1.00	

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5. It refers to the latest document of "Environment-related Substances standard" (WI-HSPM-QA-072).

	Substances
	Cadmium and cadmium compounds
Heavy metals	Lead and lead compounds
Treavy metais	Mercury and mercury compounds
	Hexavalent chromium compounds
	Polychlorinated biphenyls (PCB)
Chloinated	Polychlorinated naphthalenes (PCN)
organic	Polychlorinated terphenyls (PCT)
compounds	Short-chain chlorinated paraffins(SCCP)
	Other chlorinated organic compounds
D : 1	Polybrominated biphenyls (PBB)
Brominated	Polybrominated diphenylethers(PBDE) (including
organic	decabromodiphenyl ether[DecaBDE])
compounds	Other brominated organic compounds
Tributyltin compo	ounds(TBT)
Triphenyltin com	pounds(TPT)
Asbestos	
Specific azo com	pounds
Formaldehyde	
Beryllium oxide	
Beryllium coppe	er
Specific phthalate	es (DEHP,DBP,BBP,DINP,DIDP,DNOP,DNHP)
Hydrofluorocarbo	on (HFC), Perfluorocarbon (PFC)
Perfluorooctane s	ulfonates (PFOS)
Specific Benzotri	azole

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ELECTROLYTIC CAPACITOR SPECIFICATION GT SERIES

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Attachment: Application Guidelines

1.Circuit Design

1.1 Operating Temperature and Frequency

Electrolytic capacitor electrical parameters are normally specified at 20°C temperature and 120Hz frequency. These parameters vary with changes in temperature and frequency. Circuit designers should take these changes into consideration.

- (1) Effects of operating temperature on electrical parameters
 - a) At higher temperatures, leakage current and capacitance increase while equivalent series resistance (ESR) decreases.
 - b) At lower temperatures, leakage current and capacitance decrease while equivalent series resistance (ESR) increases.
- (2) Effects of frequency on electrical parameters
 - a) At higher frequencies capacitance and impedance decrease while tanδ increases.
 - b) At lower frequencies, ripple current generated heat will rise due to an increase in equivalent series resistance (ESR).

1.2 Operating Temperature and Life Expectancy

See the file: Life calculation of aluminum electrolytic capacitor

1.3 Common Application Conditions to Avoid

The following misapplication load conditions will cause rapid deterioration to capacitor electrical parameters. In addition, rapid heating and gas generation within the capacitor can occur causing the pressure relief vent to operate and resultant leakage of electrolyte. Under Leaking electrolyte is combustible and electrically conductive.

(1) Reverse Voltage

DC capacitors have polarity. Verify correct polarity before insertion. For circuits with changing or uncertain polarity, use DC bipolar capacitors. DC bipolar capacitors are not suitable for use in AC circuits.

(2) Charge / Discharge Applications

Standard capacitors are not suitable for use in repeating charge / discharge applications. For charge / discharge applications consult us and advise actual conditions.

(3) Over voltage

Do not apply voltages exceeding the maximum specified rated voltage. Voltages up to the surge voltage rating are acceptable for short periods of time. Ensure that the sum of the DC voltage and the superimposed AC ripple voltage does not exceed the rated voltage.

(4) Ripple Current

Do not apply ripple currents exceeding the maximum specified value. For high ripple current applications, use a capacitor designed for high ripple currents or contact us with your requirements. Ensure that allowable ripple currents superimposed on low DC bias voltages do not cause reverse voltage conditions.

1.4 Using Two or More Capacitors in Series or Parallel

(1) Capacitors Connected in Parallel

The circuit resistance can closely approximate the series resistance of the capacitor causing an imbalance of ripple current loads within the capacitors. Careful design of wiring methods can minimize the possibility of excessive ripple currents applied to a capacitor.

(2) Capacitors Connected in Series

Normal DC leakage current differences among capacitors can cause voltage imbalances. The use of voltage divider shunt resistors with consideration to leakage current can prevent capacitor voltage imbalances.

1.5 Capacitor Mounting Considerations

(1) Double Sided Circuit Boards

Avoid wiring pattern runs, which pass between the mounted capacitor and the circuit board.

When dipping into a solder bath, excess solder may collect under the capacitor by capillary action and short circuit the anode and cathode terminals.

(2) Circuit Board Hole Positioning

The vinyl sleeve of the capacitor can be damaged if solder passes through a lead hole for subsequently processed parts. Special care when locating hole positions in proximity to capacitors is recommended.

(3)Circuit Board Hole Spacing

The circuit board holes spacing should match the capacitor lead wire spacing within the specified tolerances. Incorrect spacing can cause excessive lead wire stress during the insertion process. This may result in premature capacitor failure due to short or open circuit, increased leakage current, or electrolyte leakage.

(4) Clearance for Case Mounted Pressure Relief vents

Capacitors with case mounted pressure relief vents require sufficient clearance to allow for proper vent operation. The minimum clearances are dependent on capacitor diameters as proper vent operation. The minimum clearances are dependent on capacitor diameters as follows.

φ6.3~φ16mm:2mm minimum, φ18~φ35mm:3mm minimum, φ40mm or greater:5mm minimum.

(5) Clearance for Seal Mounted Pressure Relief Vents

A hole in the circuit board directly under the seal vent location is required to allow proper release of pressure.

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(6) Wiring Near the Pressure Relief Vent

Avoid locating high voltage or high current wiring or circuit board paths above the pressure relief vent. Flammable, high temperature gas exceeding 100°C may be released which could dissolve the wire insulation and ignite.

(7) Circuit Board patterns Under the Capacitor

Avoid circuit board runs under the capacitor as electrolyte leakage could cause an electrical short.

(8) Screw Terminal Capacitor Mounting

Do not orient the capacitor with the screw terminal side of the capacitor facing downwards.

Tighten the terminal and mounting bracket screws within the torque range specified in the specification.

1.6 Electrical Isolation of the Capacitor

Completely isolate the capacitor as follows.

- (1) Between the cathode and the case (except for axially leaded B types) and between the anode terminal and other circuit paths
- (2) Between the extra mounting terminals (on T types) and the anode terminal, cathode terminal, and other circuit paths.
- 1.7 The Product endurance should take the sample as the standard.
- 1.8 If conduct the load or shelf life test, must be collect date code within 6 months products of sampling.

1.9 Capacitor Sleeve

The vinyl sleeve or laminate coating is intended for marking and identification purposes and is not meant to electrically insulate the capacitor.

The sleeve may split or crack if immersed into solvents such as toluene or xylene, and then exposed to high temperatures.

CAUTION!

Always consider safety when designing equipment and circuits. Plan for worst case failure modes such as short circuits and open circuits which could occur during use.

- (1) Provide protection circuits and protection devices to allow safe failure modes.
- (2) Design redundant or secondary circuits where possible to assure continued operation in case of main circuit failure.

2. Capacitor Handling Techniques

- 2.1 Considerations Before Using
- (1) Capacitors have a finite life. Do not reuse or recycle capacitors from used equipment.
- (2) Transient recovery voltage may be generated in the capacitor due to dielectric absorption. If required, this voltage can be discharged with a resistor with a value of about 1kΩ.
- (3) Capacitors stored for long periods of time may exhibit an increase in leakage current. This can be corrected by gradually applying rated voltage in series with a resistor of approximately $1k\Omega$.
- (4) If capacitors are dropped, they can be damaged mechanically or electrically. Avoid using dropped capacitors.
- (5) Dented or crushed capacitors should not be used. The seal integrity can be compromised and loss of electrolyte / shortened life can result.

2.2 Capacitor Insertion

- (1) Verify the correct capacitance and rated voltage of the capacitor.
- (2) Verify the correct polarity of the capacitor before inserting.
- (3) Verify the correct hole spacing before insertion (land pattern size on chip type) to avoid stress on the terminals.
- (4) Ensure that the auto insertion equipment lead clinching operation does not stress the capacitor leads where they enter the seal of the capacitor.

For chip type capacitors, excessive mounting pressure can cause high leakage current, short circuit, or disconnection.

2.3 Manual Soldering

- (1) Observe temperature and time soldering specifications or do not exceed temperatures of 400 °C for 3 seconds or less.
- (2) If lead wires must be formed to meet terminal board hole spacing, avoid stress on the lead wire where it enters the capacitor seal.
- (3) If a soldered capacitor must be removed and reinserted, avoid excessive stress to the capacitor leads.
- (4) Avoid touching the tip of the soldering iron to the capacitor, to prevent melting of the vinyl sleeve.

2.4 Flow Soldering

- (1) Do not immerse the capacitor body into the solder bath as excessive internal pressure could result.
- (2) Observe proper soldering conditions (temperature, time, etc.) Do not exceed the specified limits.
- (3) Do not allow other parts or components to touch the capacitor during soldering.

2.5 Other Soldering Considerations

Rapid temperature rises during the preheat operation and resin bonding operation can cause cracking of the capacitor vinyl sleeve. For heat curing, do not exceed 150°C for a maximum time of 2 minutes.

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2.6 Capacitor Handling after Solder

- (1). Avoid movement of the capacitor after soldering to prevent excessive stress on the lead wires where they enter the seal.
- (2). Do not use capacitor as a handle when moving the circuit board assembly.
- (3). Avoid striking the capacitor after assembly to prevent failure due to excessive shock.

2.7 Circuit Board Cleaning

- (1) Circuit boards can be immersed or ultrasonically cleaned using suitable cleaning solvents for up 5 minutes and up to 60°C maximum temperatures. The boards should be thoroughly rinsed and dried. The use of ozone depleting cleaning agents is not recommended in the interest of protecting the environment.
- (2) Avoid using the following solvent groups unless specifically allowed for in the specification;

Halogenated cleaning solvents: except for solvent resistant capacitor types, halogenated solvents can permeate the seal and cause internal capacitor corrosion and failure. For solvent resistant capacitors, carefully follow the temperature and time requirements of the specification. 1-1-1 trichloroethane should never be used on any aluminum electrolytic capacitor.

Alkali solvents : could attack and dissolve the aluminum case.

Petroleum based solvents: deterioration of the rubber seal could result.

Xylene : deterioration of the rubber seal could result.

Acetone : removal of the ink markings on the vinyl sleeve could result.

- (3) A thorough drying after cleaning is required to remove residual cleaning solvents which may be trapped between the capacitor and the circuit board. Avoid drying temperatures, which exceed the maximum rated temperature of the capacitor.
- (4) Monitor the contamination levels of the cleaning solvents during use by electrical conductivity, pH, specific gravity, or water content. Chlorine levels can rise with contamination and adversely affect the performance of the capacitor. Please consult us for additional information about acceptable cleaning solvents or cleaning methods.

2.8 Mounting Adhesives and Coating Agents

When using mounting adhesives or coating agents to control humidity, avoid using materials containing halogenated solvents. Also, avoid the use of chloroprene based polymers. After applying adhesives or coatings, dry thoroughly to prevent residual solvents from being trapped between the capacitor and the circuit board.

3. Precautions for using capacitors

3.1 Environmental Conditions

Capacitors should not be stored or used in the following environments.

- (1) Temperature exposure above the maximum rated or below the minimum rated temperature of the capacitor.
- (2) Direct contact with water, salt water, or oil.
- (3) High humidity conditions where water could condense on the capacitor.
- (4) Exposure to toxic gases such as hydrogen sulfide, sulfuric acid, nitric acid chlorine, or ammonia.
- (5) Exposure to ozone, radiation, or ultraviolet rays.
- (6) Vibration and shock conditions exceeding specified requirements.

3.2 Electrical Precautions

- (1) Avoid touching the terminals of the capacitor as possible electric shock could result. The exposed aluminum case is not insulated and could also cause electric shock if touched.
- (2) Avoid short circuit the area between the capacitor terminals with conductive materials including liquids such as acids or alkaline solutions.

4. Emergency Procedures

- (1) If the pressure relief vent of the capacitor operates, immediately turn off the equipment and disconnect form the power source. This will minimize additional damage caused by the vaporizing electrolyte.
- (2) Avoid contact with the escaping electrolyte gas which can exceed 100℃ temperatures.

If electrolyte or gas enters the eye, immediately flush the eyes with large amounts of water.

If electrolyte or gas is ingested by month, gargle with water.

If electrolyte contacts the skin, wash with soap and water.

5. Long Term Storage

Leakage current of a capacitor increases with long storage times. The aluminum oxide film deteriorates as a function of temperature and time. If used without reconditioning, an abnormally high current will be required to restore the oxide film. This current surge could cause the circuit or the capacitor to fail. After one year, a capacitor should be reconditioned by applying rated voltage in series with a 1000Ω , current limiting resistor for a time period of 30 minutes . If the expired date of products date code is over eighteen months, the products should be return to confirmation.

5.1 Environmental Conditions

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The capacitor shall be not use in the following condition:

- (1) Temperature exposure above the maximum rated or below the minimum rated temperature of the capacitor.
- (2) Direct contact with water, salt water, or oil.
- (3) High humidity conditions where water could condense on the capacitor.
- (4) Exposure to toxic gases such as hydrogen sulfide, sulfuric acid, nitric acid, chlorine, or ammonia.
- (5) Exposure to ozone, radiation, or ultraviolet rays.
- (6) Vibration and shock conditions exceeding specified requirements.

6. Capacitor Disposal

When disposing of capacitors, use one of the following methods.

Incinerate after crushing the capacitor or puncturing the can wall (to prevent explosion due to internal pressure rise). Capacitors should be incinerated at high temperatures to prevent the release of toxic gases such as chlorine from the polyvinyl chloride sleeve, etc.

Dispose of as solid waste.

NOTE: Local laws may have specific disposal requirements, which must be followed.

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