## 3-INPUT VIDEO SWITCH WITH 75』 DRIVER

## ■ GENERAL DESCRIPTION

The NJM2244 is a three input integrated video switch, which selects one video signal from three input signals.
It contains driver circuit for $75 \Omega$ load and is able to connect to TV monitor.
Its operating supply voltage range is 5 to 12 v and bandwidth is 10 MHz .
Crosstalk is 70 dB (at 4.43 MHz ).
NJM2244 contains clamp function and it can be operated while setting DC level fixed in position of the video signal.

## - FEATURES

- Operating Voltage
4.75 to 13 V
- 3 input - 1 Output
- Internal Driver Circuit for $75 \Omega$ Impedance
- Muting Function available
- Internal Clamp Function
- Low power Dissipation
- Cross-talk
- Wide Frequency Range
- Package Outline
16.5 mA

70 dB (at 4.43 MHz )
10 MHz (2V-p Input)
DIP8, DMP8, SIP8

- PACKAGE OUTLINE


NJM2244D


NJM2244M


NJM2244L

## PIN FUNCTION

| 1. $\mathrm{V}_{\mathrm{tn}} 1$ |  |
| :---: | :---: |
| 2 | SW 1 |
| 3 | $\mathrm{V}_{\mathrm{t}} 2$ |
| 4 | SW2 |
| 5 | $\mathrm{V}_{1 \mathrm{n}} 3$ |
| 6 | $\mathrm{V}^{+}$ |
| 7. | $\mathrm{V}_{\text {out }}$ |
|  |  |

- BLOCK DIAGRAM

Pin Connection


■ INPUT CONTROL SIGNAL-OUTPUT SIGNAL

| SW1 | SW2 | OUTPUT SIGNAL |
| :---: | :---: | :---: |
| $L$ | $L$ | $V_{\mathbb{N}} 1$ |
| $H$ | $L$ | $V_{\mathbb{N}} 2$ |
| $L / H$ | $H$ | $V_{\mathbb{N}} 3$ |

note): Input clamp Voltage is about $2 / 5$ of Supply Voltage

|  |  |  |  |
| :--- | :---: | :---: | :---: |
| ABSOLUTE MAXIMUM RATINGS | $\left(\mathrm{Ta}=25^{\circ} \mathrm{C}\right)$ |  |  |
| PARAMETER | SYMBOL | RATINGS | UNIT |
| Supply Voltage | $\mathrm{V}^{+}$ | 15 | V |
| Power Dissipation | PD | (DIP8) 500 | mW |
|  |  | (DMP8) 300 | mW |
| (SIP8) 800 | mW |  |  |
| Operating Temperature Range | $\mathrm{T}_{\text {opr }}$ | -20 to +75 | ${ }^{\circ} \mathrm{C}$ |
| Storage Temperature Range | $\mathrm{T}_{\text {stg }}$ | -40 to +125 | ${ }^{\circ} \mathrm{C}$ |

$\square$ ELECTRICAL CHARACTERISTICS $\quad\left(\mathrm{V}^{+}=5 \mathrm{~V}, \mathrm{Ta}=25^{\circ} \mathrm{C}\right)$

| PARAMETER | SYMBOL | TEST CONDITION | MIN. | TYP. | MAX. | UNIT |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| Recommended Supply Voltage | $\mathrm{V}^{+}$ |  | 4.75 | - | 13.0 | V |
| Operating Current | Icc | $\mathrm{S} 1=\mathrm{S} 2=\mathrm{S} 3=\mathrm{S} 4=\mathrm{S} 5=2$ | 11.5 | 16.5 | 22.0 | mA |
| Voltage Gain | $\mathrm{G}_{V}$ | $\mathrm{Vin}=2.0 \mathrm{~V}_{\text {P-P, }} 100 \mathrm{kHz}, \mathrm{VO} / \mathrm{Vi}, \mathrm{R}_{\mathrm{L}}=150 \Omega$ | -0.8 | -0.3 | +0.2 | dB |
| Frequency Characteristics | $\mathrm{G}_{\mathrm{f}}$ | $\mathrm{Vin}=2.0 \mathrm{~V}_{\text {P-P }}, \mathrm{V}_{\mathrm{O}}(10 \mathrm{MHz}) / \mathrm{V}_{\mathrm{O}}(100 \mathrm{kHz}), \mathrm{R}_{\mathrm{L}}=150 \Omega$ | -1.0 | - | +1.0 | dB |
| Differential Gain | DG | Vin $=2.0 \mathrm{~V}_{\text {P-P, }}$, staircase, $\mathrm{R}_{\mathrm{L}}=150 \Omega$ | - | 0.3 | - | \% |
| Differential Phase | DP | Vin $=2.0 V_{\text {P-P, }}$, staircase, $\mathrm{R}_{\mathrm{L}}=150 \Omega$ | - | 0.3 | - | deg. |
| Output Offset Voltage | $V_{\text {off }}$ | $\mathrm{S} 1=\mathrm{S} 2=\mathrm{S} 3=2, \mathrm{~S} 5=1 \rightarrow 2 \mathrm{Vo}$ : Voltage change | - | 0 | $\pm 30$ | mV |
| Crosstalk | CT | $\mathrm{Vin}=2 \mathrm{~V}_{\mathrm{PP},}, 4.43 \mathrm{MHz}$, Vo $/ \mathrm{Vi}$ | - | -70 | - | dB |
| Switch Change Voltage | $\mathrm{V}_{\text {CH }}$ | All inside Sw : ON | 2.4 | - | - | V |
|  | $\mathrm{V}_{\mathrm{CL}}$ | All inside Sw : OFF | - | - | 0.8 | V |

(note) Unless specified, tested with three mode below.
a) $\mathrm{S} 1=1, \mathrm{~S} 2=\mathrm{S} 3=\mathrm{S} 4=\mathrm{S} 5=2 \mathrm{~b}) \mathrm{S} 2=\mathrm{S} 4=1, \mathrm{~S} 1=\mathrm{S} 3=\mathrm{S} 5=2 \mathrm{c}) \mathrm{S} 1=\mathrm{S} 2=2, \mathrm{~S} 3=\mathrm{S} 5=1, \mathrm{~S} 4=1$ or 2

## - APPLICATION

Oscillation Prevention on light loading conditions
Recommended under circuit


## - TEST CIRCUIT



DC Voltage Each Terminal
Typ. on Test Circuit Ta $=25^{\circ} \mathrm{C}$

| Terminal Name | $\mathrm{V}_{\mathbb{N} 1}$ | SW 1 | $\mathrm{~V}_{\mathbb{N} 2}$ | SW2 | $\mathrm{V}_{\mathbb{N} 3}$ | $\mathrm{~V}^{+}$ | $\mathrm{V}_{\text {out }}$ | GND |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| DC Voltage | $\frac{2}{5} \mathrm{~V}^{+}$ | - | $\frac{2}{5} \mathrm{~V}^{+}$ | - | $\frac{2}{5} \mathrm{~V}^{+}$ | - | $\frac{2}{5} \mathrm{~V}^{+}-0.7$ | - |

## ■ EQUIVALENT CIRCUIT

PIN NO. | PIN |
| :---: |
| FUNCTION |

## ■ APPLICATION

This IC requires $1 \mathrm{M} \Omega$ resistance between INPUT and GND pin for clamp type input since the minute current causes an unstable pin voltage.


This IC requires 0.1 uF capacitor between INPUT and GND, $1 \mathrm{M} \Omega$ resistance between INPUT and GND for clamp type input at mute mode.

[CAUTION]

