

S52 THRU S525

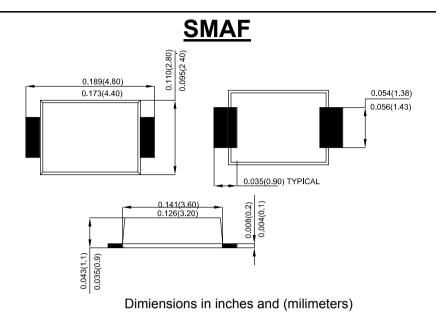
5.0 AMP SURFACE MOUNT SCHOTTKY BARRIER RECTIFIER

Features

- · Schottky Brrier Chip
- · Low Power Loss, High Efficiency
- · Ideally Suited for Automatic Assembly
- · Surge Overload Rating to 100APeak
- Plastic Case Material has UL Flammability Classification Rating 94V-0

Mechanical Data

- · Case: Molded plastic SMAF
- Terminals: Plated leads solderable per MIL-STD-750, Method 2026 guaranteed
- · Polarity: Color band denotes cathode end
- Mounting Position: Any
- · Making: Type Number



Maximum Ratings and Electrical Characteristics

Rating at 25°C ambient temperature unless otherwise specified

Single phase, half wave, 60Hz, resistive or inductive load

For capacitive load derate current by 20%

| Type Number | SYMBOL | S 52 | S 53 | S 54 | S 545 | S 55 | S 56 | S 58 | S 510 | S 515 | S 520 | S 525 | Unit |
|--|------------------|-------------|---------|---------|----------|---------|---------|---------|----------|----------|----------|----------|----------------------|
| Maximum Recurrent Peak Reverse Voltage | V _{RRM} | 20 | 30 | 40 | 45 | 50 | 60 | 80 | 100 | 150 | 200 | 250 | V |
| Maximum RMS Voltage | V _{RMS} | 14 | 21 | 28 | 31 | 35 | 42 | 56 | 70 | 105 | 140 | 175 | V |
| Maximum DC Blocking Voltage | V _{DC} | 20 | 30 | 40 | 45 | 50 | 60 | 80 | 100 | 150 | 200 | 250 | V |
| Average Rectified Output Current @T∟=90°C | I F(AV) | 5.0 | | | | | | | | | | | Α |
| Peak Forward Surge Current 8.3ms Single half sine-wave superimposed on rated load (JEDEC Method) | İfsm | 100 | | | | | | | | | | Α | |
| I ² t Rating for Fusing (t < 8.3ms) | l ² t | 41.500 | | | | | | | | | | | A ² s |
| Forward Voltage @IF=5.0A (Note1) | V _{FM} | 0.55 0.7 | | | | 0.8 | 85 0 | | 92 | 0.95 | V | | |
| Peak Reverse Current @T _A =25°C | | 0.1 0.05 | | | | | | | | | | m A | |
| At Rated DC Blocking Voltage @TA =100°C | l _R | 10 5 | | | | | | | | | -mA | | |
| Typical Junction Capacitance | CJ | 28 | | | | | | | | | | pF | |
| Typical Thermal Resistance per leg (Note 2) | Rеја | 88 | | | | | | | | | | | °C /W |
| Operating Temperature Range | TJ | -55 to+150 | | | | | | | | | | | $^{\circ}$ |
| Storage Temperature Range | Tstg | -55 to +150 | | | | | | | | | | | $^{\circ}\mathbb{C}$ |

Note: 1.Pulse Test with PW=300usec,1%Duty Cycle.

2.Mounted on P.C.Board with 5.0 mm² (0.13mm thick) copper pad areas.



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Fig. 1 Forward Current Derating Curve

Solution

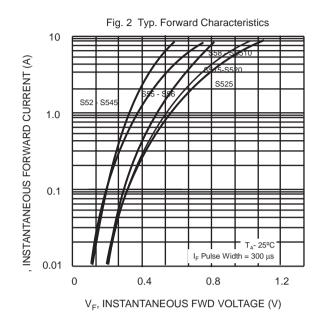
Fig. 1 Forward Current Derating Curve

2.5

Description

2.5

LEAD TEMPERATURE(°C)



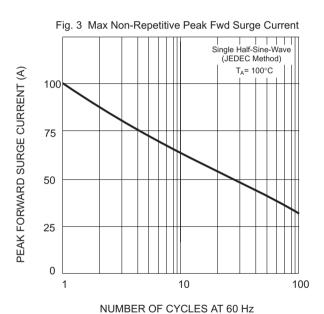


Fig. 4 T ypical Reverse Characteristics (per element)

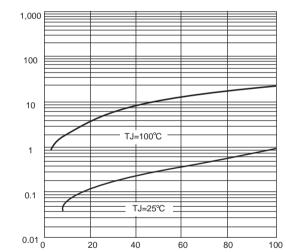
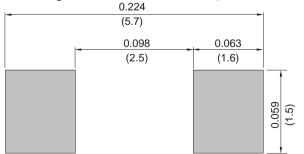


Fig.5 TYPICAL CAPACITANCE



PERCENT OF RATED PEAK REVERSE VOLTAGE (%)

IR, INSTANTANEOUS REVERSE CURRENT (μA)



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