SPECIFICATION FOR APPROVAL

CUSTOMER:	鹿鸣
CUSTOMER P/N	
PART NO:	
DESCRIPTION:	SMD POWER INDUCTORS
PRODUCTS NO:	CYSTF0805TL-251Y
PRODUCTS REV:	1
DATE:	2018-7-20

PURCHASER CONFIRMED			
REMARK			

PROVIDER ENGINEER DEPT.		
APPROVAL BY CHECK BY DRAWN BY		
		chenlinli

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CHINA FACTORY



REVISION NOTES

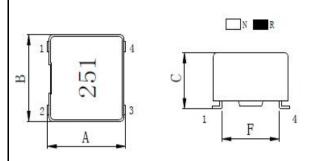
NO.	Date	Description of Revision
1	2018-7-20	首次送樣

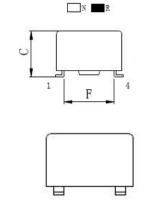
ROHS Compliant

TEST DATADIMENSION&ELECTRIC CHARACTER

CUSTOME	鹿鸣	PART NO.:	
ΓOMER :	SMD INDUCTOR	SERIES NO:	CYSTF0805TL-251Y

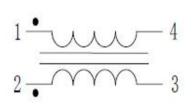
EXTERNAL DIMENSIONS

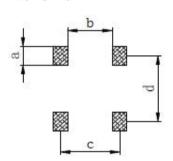




	UNIT: mm	
A	8.8±0.5	
В	8.5±0.5	
С	5.5±0.5	
D	6.22±0.5	
Е	1.0REF	
F	6.45REF	
a	1.5	
b	2.20	
С	6.22	
d	7.62	

RECOMMEND LAND PATTERN DIMENSIONS





ELECTRICAL CHARACTERISTICS(@ 25°C)

	(1-4)(2-3)	(1-4)(2-3)	Rated Current
Part No.	(uH)	(mΩ)	(A)
CYSTF0805TL-251Y	250±40%	30 Max	1.25
	100KHz/0.1V		ΔT≦40°CTyp
	TH21816B	GKT-502BC	CH2816+WR7210

NOTE:

Operating temperature: '-40°C∼+125°C

storage: 温度: 0℃~+40℃ 湿度: RH10%~70%

APPROVED BY: Vincent CHECKED BY: Yasir DRAWN BY: chenlinli

TEST DATA

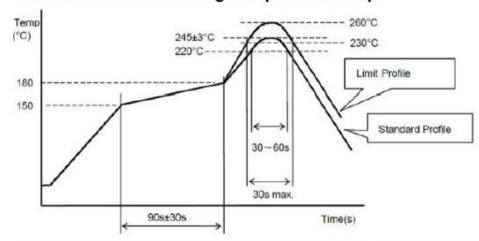
DIMENSION&ELECTRIC CHARACTER

CUSTOME	鹿鸣	PART NO.:	
ГОМЕR :	SMD INDUCTOR	SERIES NO:	CYSTF0805TL-251Y

Material List

No.	Item	Material	Specification	Supplier	UL
a	Core	Ferrite core	T core	ACME OR EQU	
b	Wire	Enamelled copper wire	G2P180	ELEKTRISOLA OR EQU	E258243
С	Base	Mental Base	DAP9100 C5191 (PIN)	WAH HONG OR EQU JINMEI OR EQU	
d	Adhesive	Epoxy resin	6020H-6(NH)	WELLS OR EQU	
d	Terminal	Sn/Cu	N107H	THOUSAND OR EQU	

Recommended Soldering Temperature Grapl



	Standard Profile	Limit Profile
Pre-heating	150~180	0℃、90s±30s
Heating	above 220℃、30s-60s	above 240 ℃、30s max
Peak temperature	245℃±3℃	260℃、10s
Cycle of reflow	2 times	2 times

Product photos



APPROVED BY	CHECKED BY	PREPARED BY
Vincent	Yasir	chenlinli

■GENERAL CHARA	ACTERISTICS	page. 1
Operation Temperature	-40°C to +125°C (Includes temperature when the co	il is heated)
External Appearance	On visual inspection, the coil has no external defects	S.
Solder Ability Test	More than 90% of terminal electrode should be cover a large of large of the large	
Heat endurance of Soldering	1.Components should have not evidence of electrica 2.Inductance: within±10% of initial value. 3.Impedance: within±10% of initial value. Preheat:150±5°C 60seconds. Solder temperature: 250±5°C. Flux: rosin. Dip time:10±0.5seconds.	Preheating Dipping Natural cooling
Terminal Strength	After soldering of X,Y withstanding at below condit off. (Refer to figure at below)	ions .The terminal should not Peel 5N y
Insulating Resistance	Over 100MΩ at 100V D.C. between coil and core. No dielectric breakdown at 30V D.C. for 1 minute between coil and core. Inductance deviation within +10% after vibration for 1 hour. In each of three orientations at sweep vibration(10-~55-~10HZ)with 1.5mmP-P amplitudes Inductance deviation within +10% after being dropped once with 981m/s2 (100G) shock Attitude upon a rubber block method shock testing machine, in three different orientations	
Dielectric Strength		
VibrationTest		
Drop test		

v Application Notice/Handling

1. Storage Conditions

To maintain the solder ability of terminal electrodes:

- (1) Temperature and humidity conditions: less than 40°C and 70% RH.
- (2) Products should be used within 6 months.
- (3) The packaging material should be kept where no chlorine or sulfur exists in the air.
- 2. Handling
- (1) Do not touch the electrodes(soldering terminals) with fingers as this may lead to deterioration of solderability.
- (2) The use of tweezers or vacuum pick-ups is strongly recommended for individual components.
- (3) Bulk handling should ensure that abrasion and mechanical shock are minimized.

■ GENERAL CHARACTE	ERISTICS	page. 2
TEST	Required Characteristics	Test Method/Condition
High Temperature StorageTest Reference documents: MIL-STD-202G Method108A	 No case deformation or change in appearance △L/L≤10% △Q/Q≤30% △DCR/DCR≤10% 	High temperature 25°C High temperature 1H 1H 96H Test Time Temperature: 125°C±2°C Time: 96±2 hours. Tested not less than 1 hour, nor more than 2 hours at room.
Low Temperature Storage Test Reference documents: IEC 68-2-1A 6.1 6.2	 No case deformation or change in appearance △L/L≦10% △Q/Q≦30% △DCR/DCR≦10% 	
Humidity Test Reference documents: MIL-STD-202G Method103B	 No case deformation or change in appearance ΔL/L≤10% ΔQ/Q≤30% ΔDCR/DCR≤10% 	
Thermal Shock Test Reference documents: MIL-STD-202G Method107G	 No case deformation or change in appearance △L/L≤10% △Q/Q≤30% △DCR/DCR≤10% 	First-40°C for 30 Minutes, last 125°C for 30 Minutes as 1 cycle. Go through 20 cycles.

■Application Notice/Handling

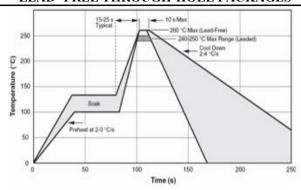
- (1) Temperature and humidity conditions: less than 40°C and 70% RH.
- (2) Products should be used within 6 months.
- (3) The packaging material should be kept where no chlorine or sulfur exists in the air.
- (4) Do not touch the electrodes (soldering terminals) with fingers as this may lead to deterioration of solder ability
- (5) The use of tweezers or vacuum pick-ups is strongly recommended for individual components.
- (6) Bulk handling should ensure that abrasion and mechanical shock are minimized.

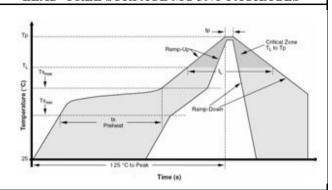
■THE CONDITION OF REFLOW(RECOMMENDATION)

page. 3

TYPICAL WAVE SOLDER PROFILE FOR LEAD-FREE THROUGH-HOLE PACKAGES

TYPICAL IR REFLOW PROFILE FOR LEADED AND LEAD -FREE SURFACE MOUNT PACKAGES





IPC/JEDEC J-STD-020C, Figure 5-1

Profile Feature	Sn-Pb Eutectic Assembly	Pb-Free Assembly 3 °C/second max. 150 °C 200 °C 60-180 seconds	
Average Ramp-Up Rate (Ts _{max} to Tp)	3 °C/second max.		
Preheat ± Temperature Min (Ts _{min}) ± Temperature Max (Ts _{max}) ± Time (ts _{min} to ts _{max})	100 °C 150 °C 60-120 seconds		
Time maintained above: ± Temperature (T _L) ± Time (t _L)	183 °C 60-150 seconds	217 °C 60-150 seconds	
Peak/Classification Temperature (Tp)	See Table 4.1	See Table 4.2	
Time within 5 °C of actual Peak Temperature (tp)	10-30 seconds	20-40 seconds	
Ramp-Down Rate	6 °C/second max.	6 °C/second max.	
Time 25 °C to Peak Temperature	6 minutes max.	8 minutes max.	

Table 4. Classification Reflow Profiles (per IPC/JEDEC J-STD-020C, Table 5.2)

Note 1: All temperatures refer to topside of the package, measured on the package body surface.

Package Thickness	Volume mm³ <350	Volume mm³ ≥350
<2.5 mm	240 +0/-5 °C	225 +0/-5 °C
≥2.5 mm	225 +0/-5 °C	225 +0/-5 °C

Table 5. SnPb Eutectic Process - Package Peak Reflow Temperatures (per IPC/JEDEC J-STD-020C, Table 4.1)

Package Thickness	Volume mm ³ <350	Volume mm ³ 350-2000	Volume mm ³ >2000
<1.6 mm	260 + 0 °C *	260 + 0 °C *	260 + 0 °C *
1.6 mm - 2.5 mm	260 + 0 °C *	250 + 0 °C *	245 + 0 °C *
≥2.5 mm	250 + 0 °C *	245 + 0 °C *	245 + 0 °C *

^{*} Tolerance: Process compatibility is up to and including the stated classification temperature (this means Peak reflow temperature + 0 °C. For example 260 °C + 0 °C) at the rated MSL level.

Table 6. Pb-free Process - Package Classification Reflow Temperatures (per IPC/JEDEC J-STD-020C, Table 4.2)

Note 1: The profiling tolerance is +0 °C, -X °C (based on machine variation capability) whatever is required to control the profile process but at no time will it exceed -5 °C. Process compatibility at the peak reflow profile temperatures as defined in Table 4.2.

Note 2: Package volume excludes external terminals (balls, bumps, lands, leads) and/or nonintegral heat sinks.

Note 3: The maximum component temperature reached during reflow depends on package thickness and volume. The use of convection reflow processes reduces the thermal gradients between packages. However, thermal gradients due to differences in thermal mass of SMD packages may still exist.

Note 4: Components intended for use in a "lead-free" assembly process shall be evaluated using the "lead-free" classification temperatures and profiles defined in Tables 4.1, 4.2 and 5.2 whether or not lead free.