

3-Pin One-Cell Step-Up DC/DC Converter

FEATURES

- A Guaranteed Start-Up from less than 0.9 V.
- High Efficiency.
- Low Quiescent Current.
- Less Number of External Components needed.
- Low Ripple and Low Noise.
- Fixed Output Voltage: 2.0V, 2.2V, 2.7V, 2.8V, 3.0V, 3.1V, 3.3V, 3.7V, 4.5V and 5V.
- Space Saving Packages: SOT-89, TO-92 (3 pin) and SOT-23 (3 & 5 pin).

APPLICATIONS

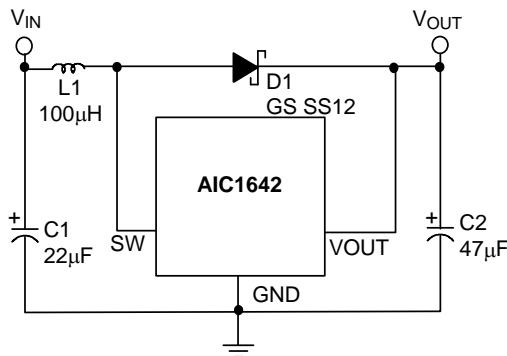
- Pagers.
- Cameras.
- Wireless Microphones.
- Pocket Organizers.
- Battery Backup Suppliers.
- Portable Instruments.

DESCRIPTION

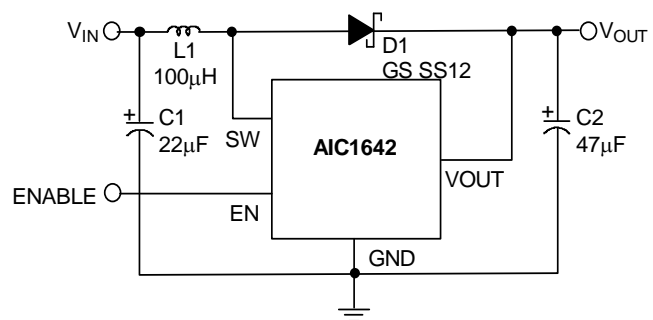
The AIC1642 is a high efficiency step-up DC/DC converter for applications using 1 to 4 NiMH battery cells. Only three external components are required to deliver a fixed output voltage of 2.0V, 2.2V, 2.7V, 2.8V, 3.0V, 3.1V, 3.3V, 3.7V, 4.5V or 5V. The AIC1642 starts up from less than 0.9V input with 1mA load. Pulse Frequency Modulation scheme brings optimized performance for applications with light output loading and low input voltages. The output ripple and noise are lower compared with the circuits operating in PSM mode.

The PFM control circuit operating in 100KHz (max.) switching rate results in smaller passive components. The space saving SOT-23, SOT-89 and TO-92 packages make the AIC1642 an ideal choice of DC/DC converter for space conscious applications, like pagers, electronic cameras, and wireless microphones.

TYPICAL APPLICATION CIRCUIT



One Cell Step-Up DC/DC Converter



One Cell Step-Up DC/DC Converter with Enable Control

ORDERING INFORMATION

AIC1642-XXXXXX

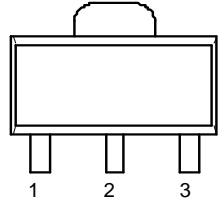
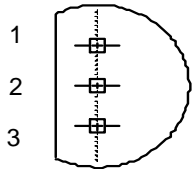
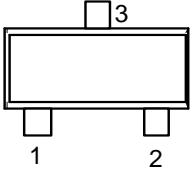
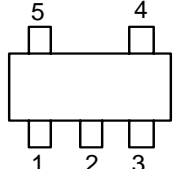
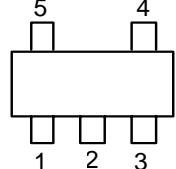
PACKING TYPE
 TR: TAPE & REEL
 TB: TUBE
 BG: BAG

PACKAGE TYPE
 U: SOT-23
 V: SOT-23-5
 VL: SOT-23-5
 X: SOT-89
 Z: TO-92

C: COMMERCIAL
 P: LEAD FREE COMMERCIAL
 G: GREEN PACKAGE

OUTPUT VOLTAGE
 20: 2.0V
 22: 2.2V
 27: 2.7V
 28: 2.8V
 30: 3.0V
 31: 3.1V
 33: 3.3V
 37: 3.7V
 45: 4.5V
 50: 5.0V

Example: AIC1642-27CXTR
 → 2.7V Version, in SOT-89 Package & Tape & Reel Packing Type
 AIC1642-27PXTR
 → 2.7V Version, in Lead Free SOT-89 Package & Tape & Reel Packing

PIN CONFIGURATION	
SOT-89 TOP VIEW 1: GND 2: VOUT 3: SW	
TO-92 TOP VIEW 1: GND 2: VOUT 3: SW	
SOT-23 TOP VIEW 1: GND 2: SW 3: VOUT	
SOT-23-5(GV) TOP VIEW 1: EN 2: VOUT 3: NC 4: GND 5: SW	
SOT-23-5(GVL) TOP VIEW 1: SW 2: GND 3: OUT 4: NC 5: NC	

ORDERING INFORMATION (Continuous)
SOT-23-5 MARKING

Part No.	GV	GVL
AIC1642-20	GW20G	GY20G
AIC1642-22	GW22G	GY22G
AIC1642-27	GW27G	GY27G
AIC1642-28	GW28G	GY28G
AIC1642-30	GW30G	GY30G
AIC1642-31	GW31G	GY31G
AIC1642-33	GW33G	GY33G
AIC1642-37	GW37G	GY37G
AIC1642-45	GW45G	GY45G
AIC1642-50	GW50G	GY50G

SOT-23 MARKING

Part No.	CU	PU	GU
AIC1642-20	GM20	GM20P	GM20G
AIC1642-22	GM22	GM22P	GM22G
AIC1642-27	GM27	GM27P	GM27G
AIC1642-28	GM28	GM28P	GM28G
AIC1642-30	GM30	GM30P	GM30G
AIC1642-31	GM31	GM31P	GM31G
AIC1642-33	GM33	GM33P	GM33G
AIC1642-37	GM37	GM37P	GM37G
AIC1642-45	GM45	GM45P	GM45G
AIC1642-50	GM50	GM50P	GM50G

SOT-89 MARKING

Part No.	CX	PX	GX
AIC1642-20	AM20	AM20P	AM20G
AIC1642-22	AM22	AM22P	AM22G
AIC1642-27	AM27	AM27P	AM27G
AIC1642-28	AM28	AM28P	AM28G
AIC1642-30	AM30	AM30P	AM30G
AIC1642-31	AM31	AM31P	AM31G
AIC1642-33	AM33	AM33P	AM33G
AIC1642-37	AM37	AM37P	AM37G
AIC1642-45	AM45	AM45P	AM45G
AIC1642-50	AM50	AM50P	AM50G

■ ABSOLUTE MAXIMUM RATINGS

Supply Voltage (VOUT pin).....	6V
SW pin Voltage.....	6V
SW pin Switch Current.....	0.6A
EN pin Voltage.....	6V
Operating Temperature Range.....	-40°C to 85°C
Maximum Junction Temperature.....	125°C
Storage Temperature Range.....	-65°C to 150 °C
Lead Temperature (Soldering 10 Sec.).....	260°C
Thermal Resistance Junction to Case	
TO-92.....	120°C/W
SOT-23.....	115°C/W
SOT-23-5.....	115°C/W
SOT-89.....	45°C/W
Thermal Resistance Junction to Ambient	
TO-92.....	150°C/W
(Assume no ambient airflow, no heatsink)	
SOT-23.....	250°C/W
SOT-23-5.....	250°C/W
SOT-89.....	160°C/W

Absolute Maximum Ratings are those values beyond which the life of a device may be impaired.

■ TEST CIRCUIT

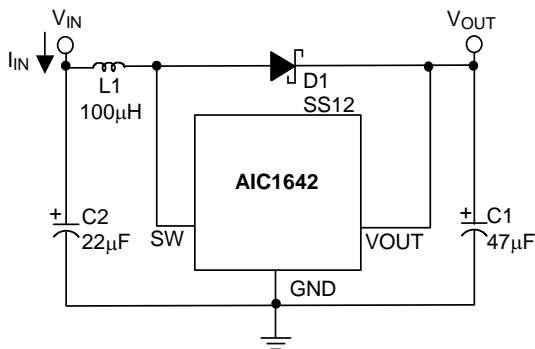


Fig. 1 Test Circuit 1

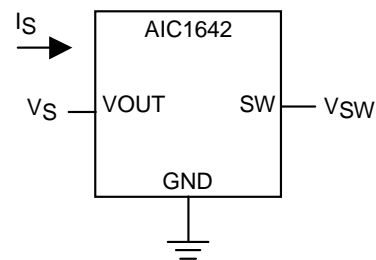


Fig. 2 Test Circuit 2

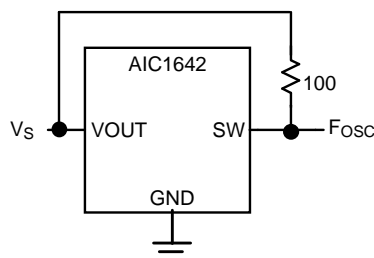


Fig. 3 Test Circuit 3

■ ELECTRICAL CHARACTERISTICS

 (T_A=25°C, I_{OUT}=10mA, Unless otherwise specified) (Note1)

PARAMETER	TEST CONDITIONS	TEST CKT	SYMBOL	MIN.	TYP.	MAX.	UNIT
Output Voltage	AIC1642-20 V _{IN} =1.8V	1	V _{OUT}	1.950	2.000	2.050	V
	AIC1642-22 V _{IN} =1.8V			2.145	2.200	2.255	
	AIC1642-27 V _{IN} =1.8V			2.633	2.700	2.767	
	AIC1642-28 V _{IN} =1.8V			2.732	2.800	2.868	
	AIC1642-30 V _{IN} =1.8V			2.925	3.000	3.075	
	AIC1642-31 V _{IN} =1.8V			3.022	3.100	3.177	
	AIC1642-33 V _{IN} =2.0V			3.218	3.300	3.382	
	AIC1642-37 V _{IN} =2.0V			3.607	3.700	3.792	
	AIC1642-45 V _{IN} =3.0V			4.387	4.500	4.613	
	AIC1642-50 V _{IN} =3.0V			4.875	5.000	5.125	
Start-Up Voltage	I _{OUT} =1mA, V _{IN} :0→2V	1	V _{START}		0.8	0.9	V
Min. Hold-on Voltage	I _{OUT} =1mA, V _{IN} :2→0V	1	V _{HOLD}			0.7	V
No-Load Input Current	I _{OUT} =0mA	1	I _{IN}		15		μA
SW Leakage Current	V _{SW} =6V, V _S =V _{OUT} + 0.5V	2				0.5	μA
Supply Current	AIC1642-20	2	I _{S1}		16		μA
	AIC1642-22				20		
	AIC1642-27				42		
	AIC1642-28				44		
	AIC1642-30				50		
	AIC1642-31				55		
	AIC1642-33				60		
	AIC1642-37				65		
	AIC1642-45				70		
	AIC1642-50				90		
	V _S =V _{OUT} x 0.95 Measurement of the IC input current (V _{OUT} pin)						

ELECTRICAL CHARACTERISTICS (Continued)

PARAMETER	TEST CONDITIONS	TEST CKT	SYMBOL	MIN.	TYP.	MAX.	UNIT
Supply Current	AIC1642-20	2	I _{S2}		7		μA
	AIC1642-22			7			
	AIC1642-27			7			
	AIC1642-28			7			
	AIC1642-30			7			
	AIC1642-31			7			
	AIC1642-33			7			
	AIC1642-37			7			
	AIC1642-45			7			
	AIC1642-50			7			
	V _S =V _{OUT} + 0.5V Measurement of the IC input current (V _{OUT} pin)						
	SW Switch-On Resistance			AIC1642-20	2	R _{ON}	
AIC1642-22		2.3					
AIC1642-27		2.2					
AIC1642-28		2.2					
AIC1642-30		2.1					
AIC1642-31		2.1					
AIC1642-33		2.0					
AIC1642-37		2.0					
AIC1642-45		1.9					
AIC1642-50		1.9					
V _S =V _{OUT} x 0.95, V _{SW} =0.4V							
Oscillator Duty Cycle	V _S =V _{OUT} x 0.95 Measurement of the SW pin waveform	3	DUTY	65	75	85	%
Max. Oscillator Freq.	V _S =V _{OUT} x 0.95 Measurement of the SW pin waveform	3	F _{OSC}	80	105	130	KHz
Efficiency		1	η		85		%
EN Pin Current	V _{EN} = V _{OUT}		I _{EN}		0.1	1	μA
EN Input Threshold	Chip Enable		V _{ENH}	1.6			V
	Chip Disable		V _{ENL}			0.4	

Note 1: Specifications are production tested at T_A=25°C. Specifications over the -40°C to 85°C operating temperature range are assured by design, characterization and correlation with Statistical Quality Controls (SQC).

TYPICAL PERFORMANCE CHARACTERISTICS

Test circuit refer to typical application circuit

Capacitor (C2) : 47 μ F (Tantalum Type)

Diode (D1) : 1N5819 Schottky Type

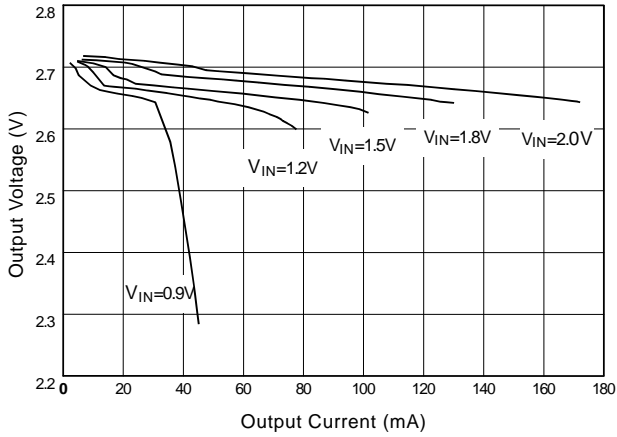


Fig. 4 AIC1642-27 Load Regulation (L=100 μ H CD54)

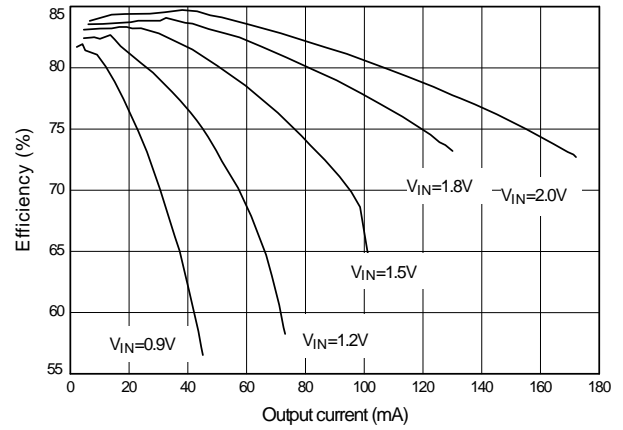


Fig. 5 AIC1642-27 Efficiency (L=100 μ H CD54)

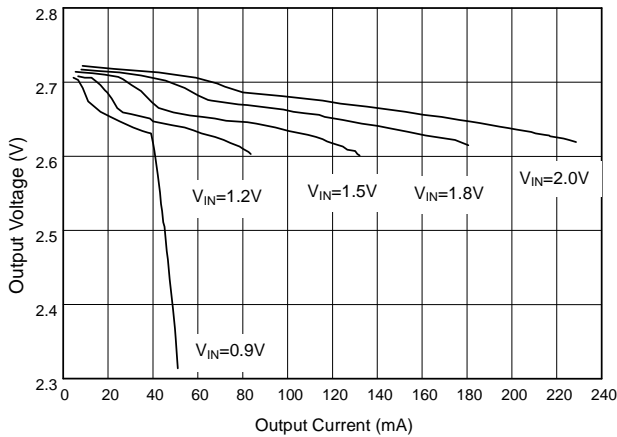


Fig. 6 AIC1642-27 Load Regulation (L=47 μ H CD54)

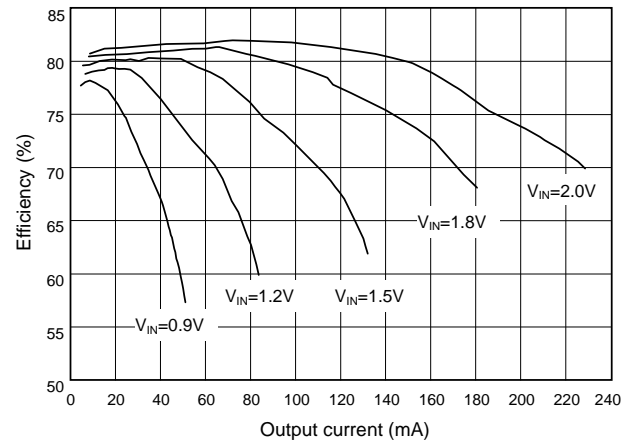


Fig. 7 AIC1642-27 Efficiency (L=47 μ H CD54)

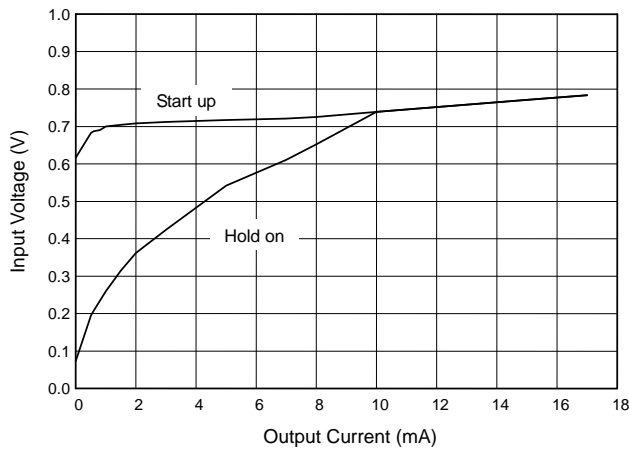


Fig. 8 AIC1642-27 Start-Up & Hold-ON Voltage (L=47 μ H CD54)

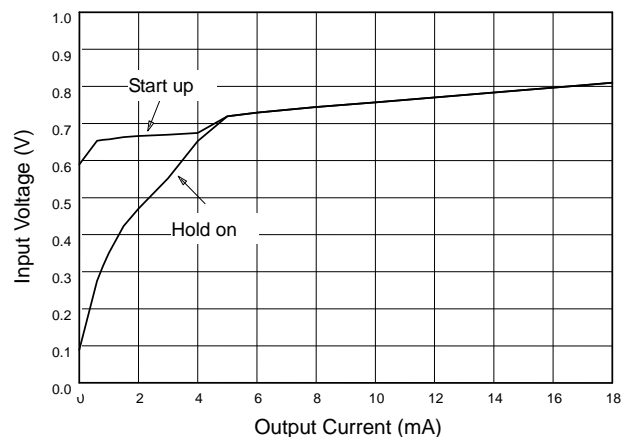


Fig. 9 AIC1642-27 Start-Up & Hold-ON Voltage (L=100 μ H CD54)

TYPICAL PERFORMANCE CHARACTERISTICS (Continued)

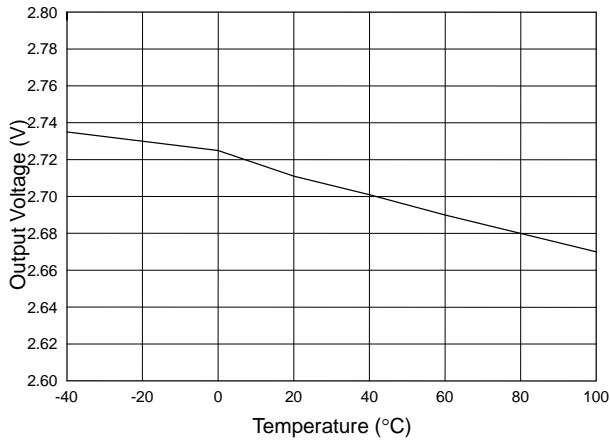


Fig. 10 AIC1642-27 Output Voltage vs. Temperature

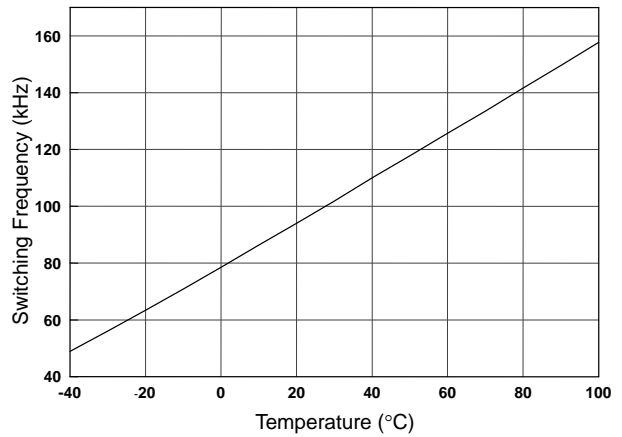


Fig. 11 AIC1642-27 Switching Frequency vs. Temperature

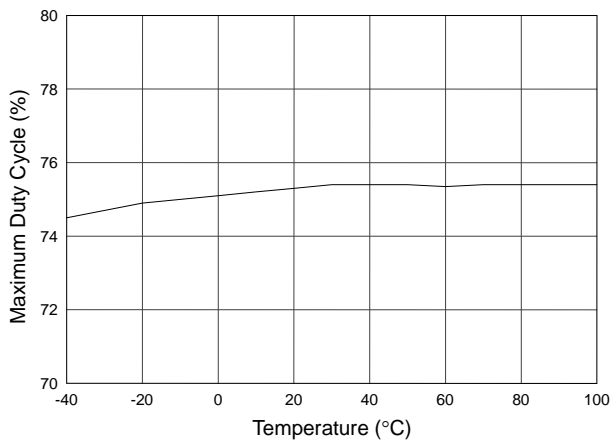


Fig. 12 AIC1642-27 Maximum Duty Cycle vs. Temperature

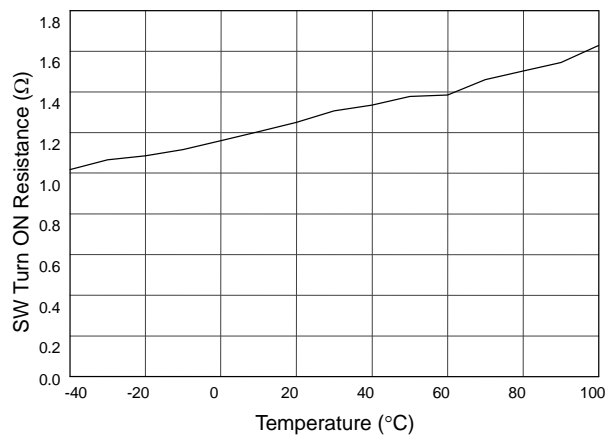


Fig. 13 AIC1642-27 SW Turn ON Resistance vs. Temperature

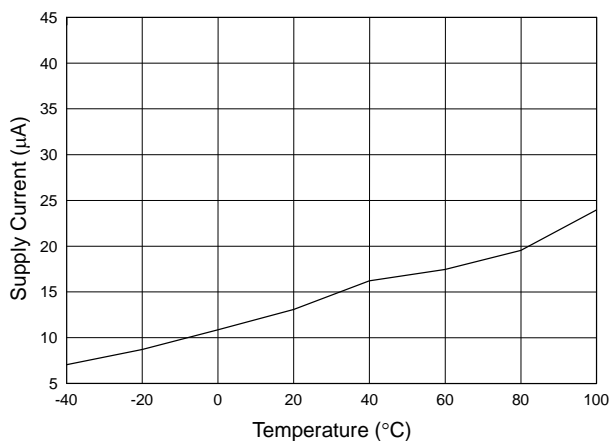


Fig. 14 AIC1642-27 Supply Current vs. Temperature

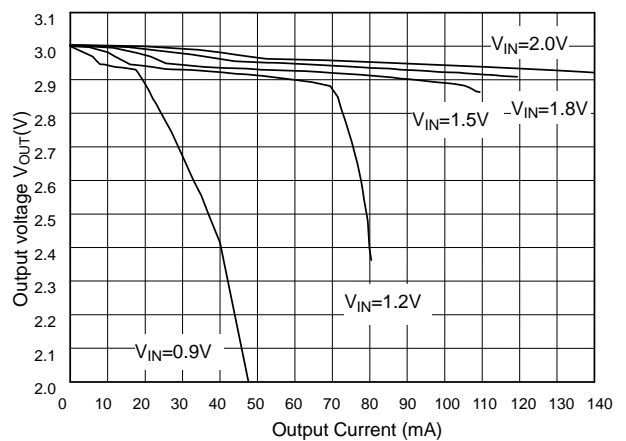


Fig. 15 AIC1642-30 Load Regulation (L=100μH, CD54)

TYPICAL PERFORMANCE CHARACTERISTICS (Continued)

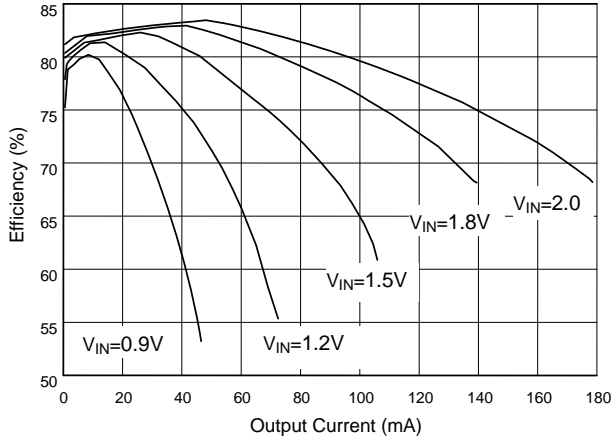


Fig. 16 AIC1642-30 Efficiency (L=100µH, CD54)

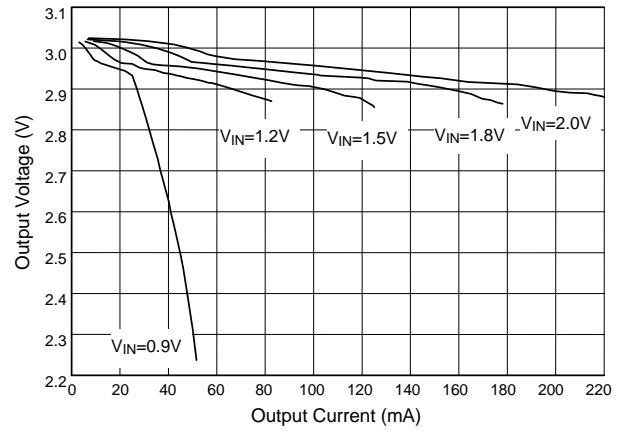


Fig. 17 AIC1642-30 Load Regulation (L=47µH CD54)

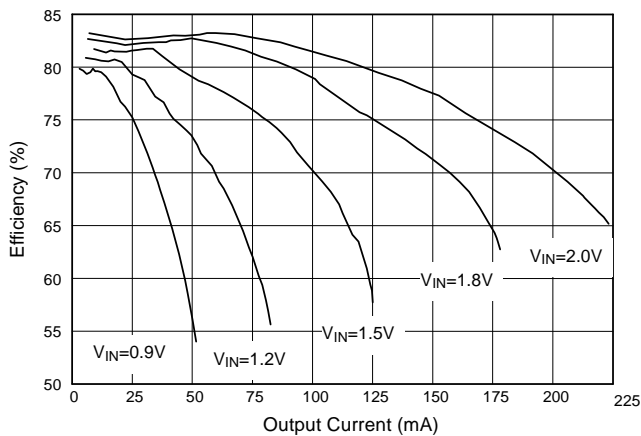


Fig. 18 AIC1642-30 Efficiency (L=47µH CD54)

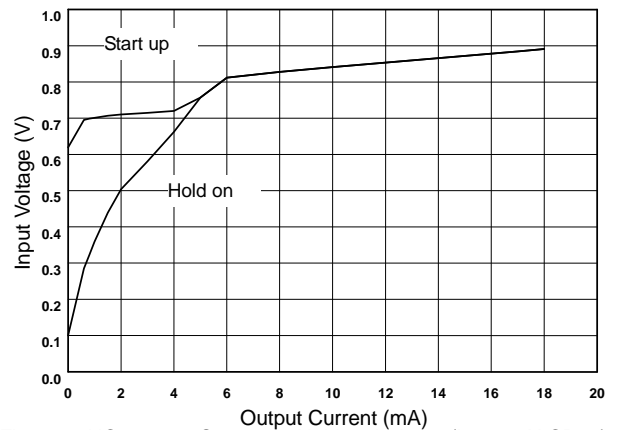


Fig. 19 AIC1642-30 Start-up & Hold-on Voltage (L=100µH CD54)

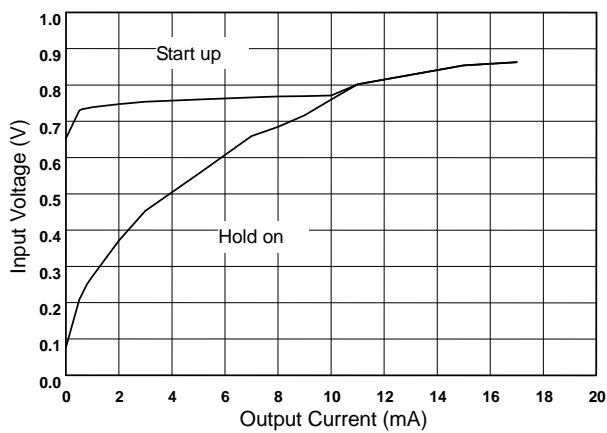


Fig. 20 AIC1642-30 Start-up & Hold-on Voltage (L=47µH CD54)

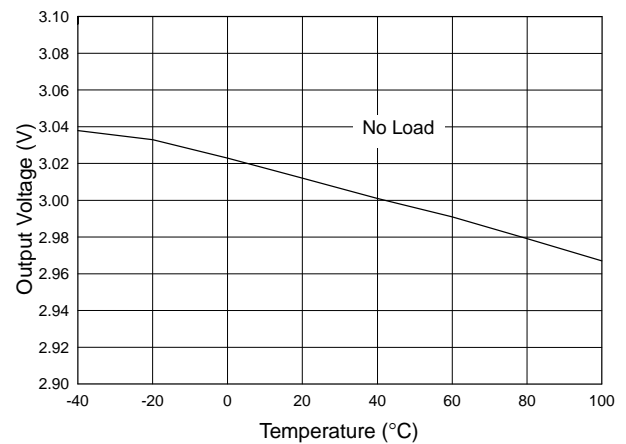


Fig. 21 AIC1642-30 Output Voltage vs. Temperature

■ TYPICAL PERFORMANCE CHARACTERISTICS (Continued)

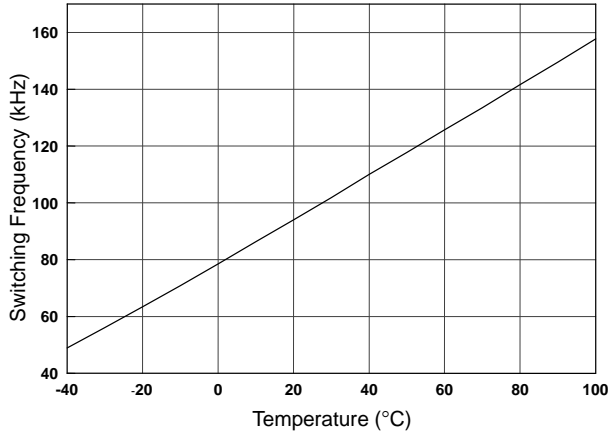


Fig. 22 AIC1642-30 Switching Frequency vs. Temperature

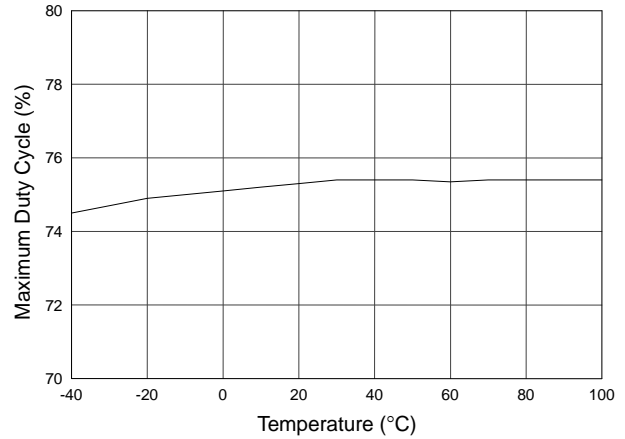


Fig. 23 AIC1642-30 Maximum Duty Cycle vs. Temperature

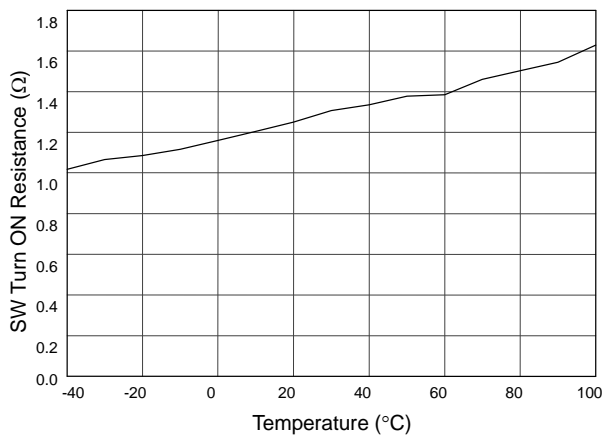


Fig. 24 AIC1642-30 SW Turn ON Resistance vs. Temperature

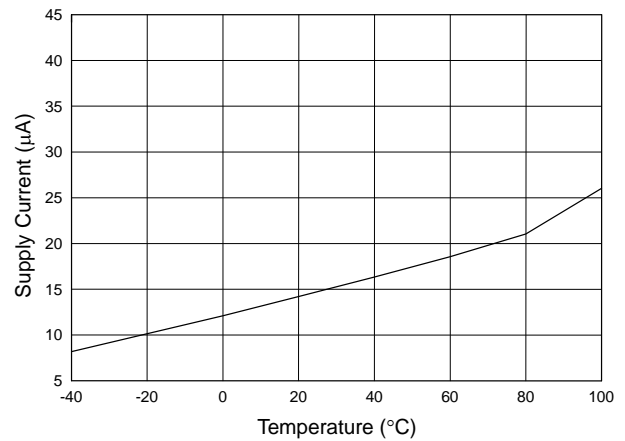


Fig. 25 AIC1642-30 Supply Current vs. Temperature

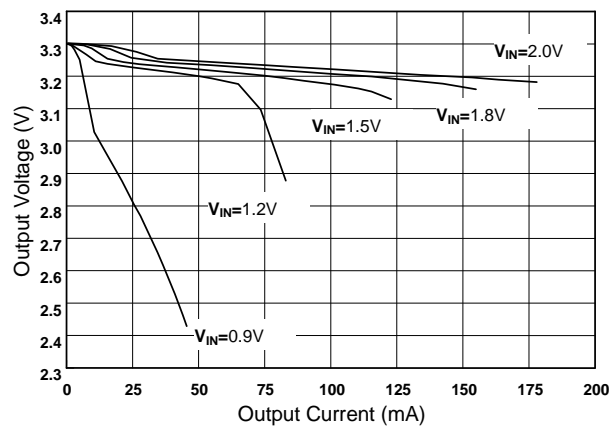


Fig. 26 AIC1642-33 Load Regulation (L=100μH, CD54)

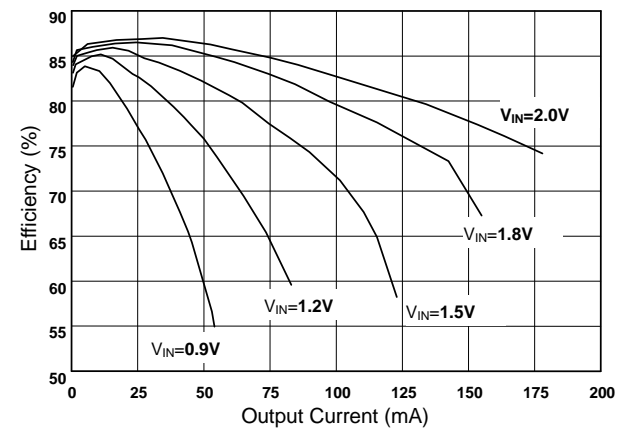


Fig. 27 AIC1642-33 Efficiency (L=100μH, CD54)

TYPICAL PERFORMANCE CHARACTERISTICS (Continued)

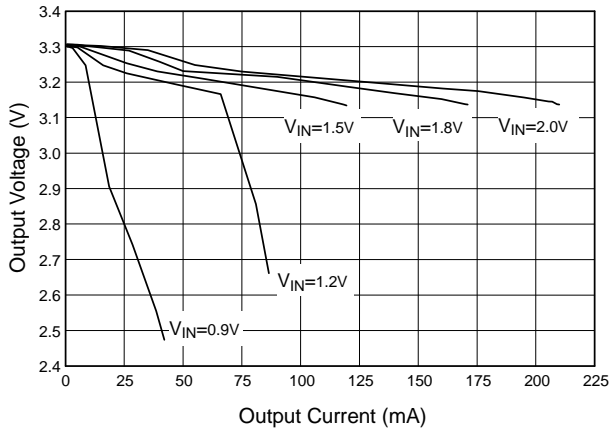


Fig. 28 AIC1642-33 Load Regulation (L=47µH, CD54)

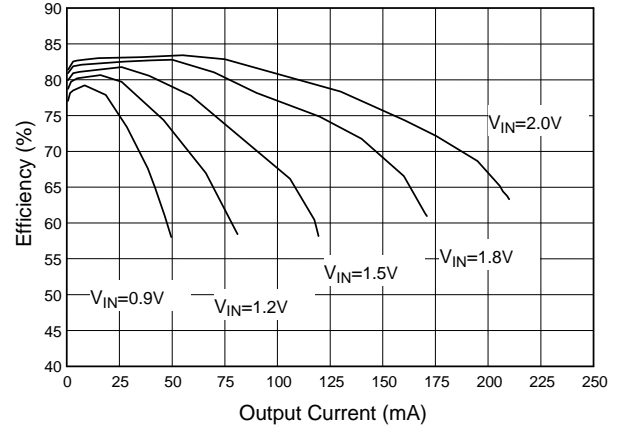


Fig. 29 AIC1642-33 Efficiency (L=47µH, CD54)

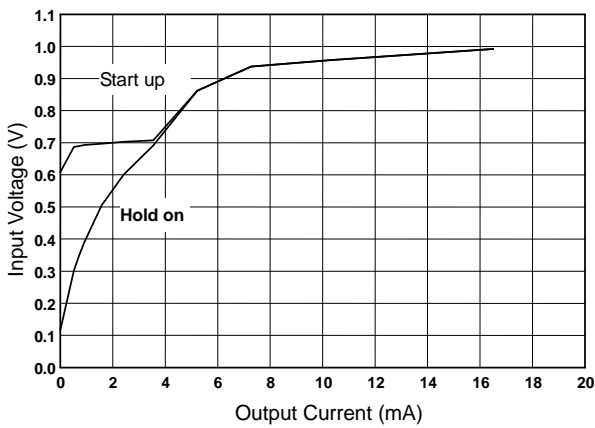


Fig. 30 AIC1642-33 Start-up & Hold-on Voltage (L=100µH CD54)

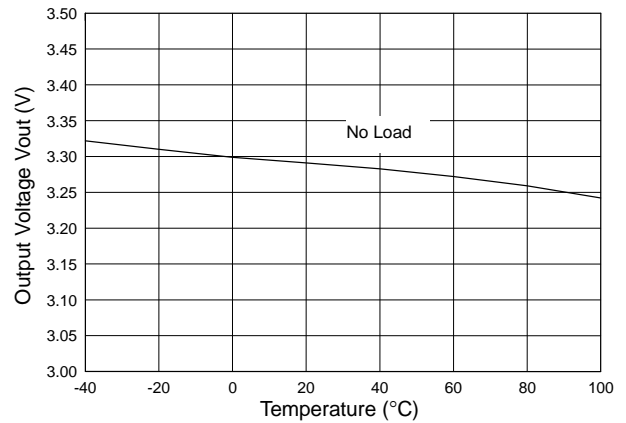


Fig. 31 AIC1642-33 Output Voltage vs. Temperature

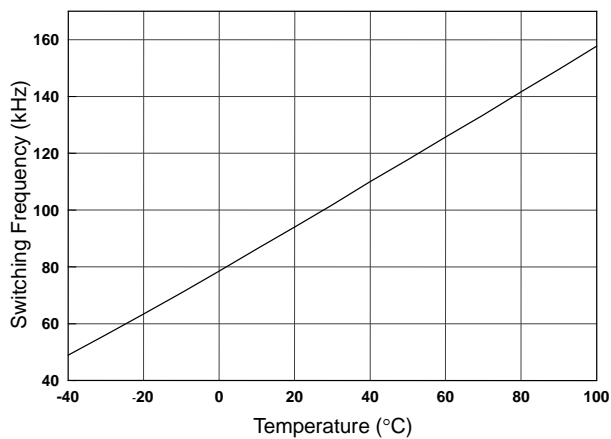


Fig. 32 AIC1642-33 Switching Frequency vs. Temperature

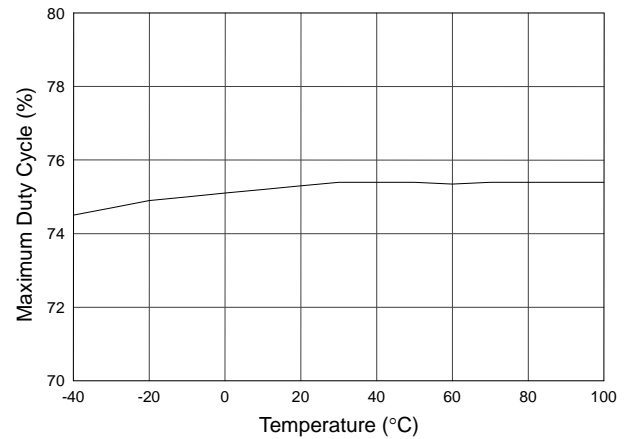


Fig. 33 AIC1642-33 Maximum Duty Cycle vs. Temperature

TYPICAL PERFORMANCE CHARACTERISTICS (Continued)

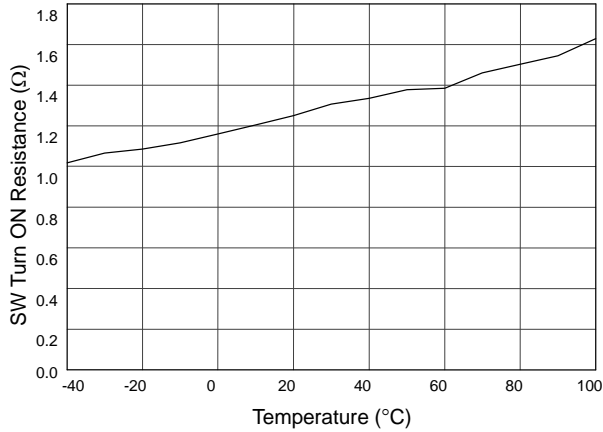


Fig. 34 AIC1642-33 SW Turn ON Resistance vs. Temperature

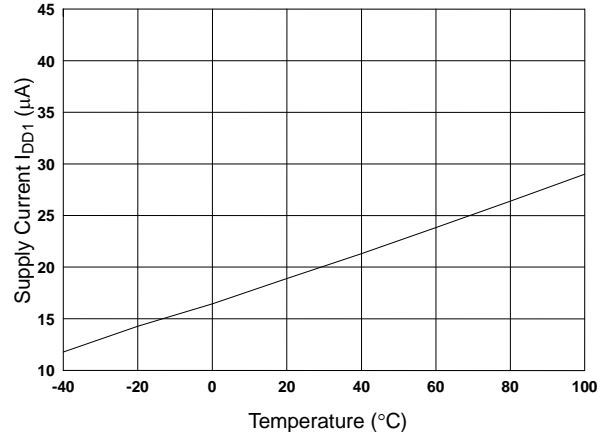


Fig. 35 AIC1642-33 Supply Current vs. Temperature

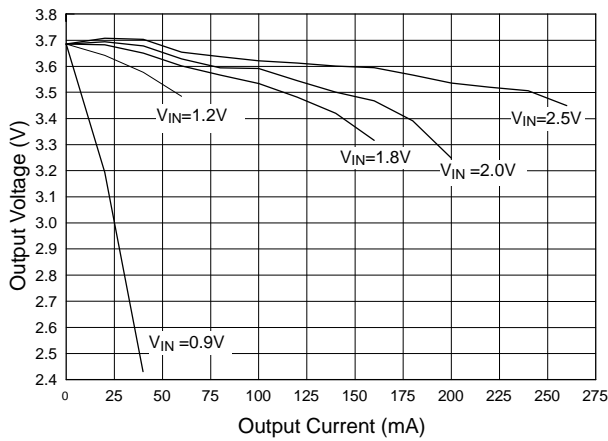


Fig. 36 AIC1642-37 Load Regulation (L=100μH)

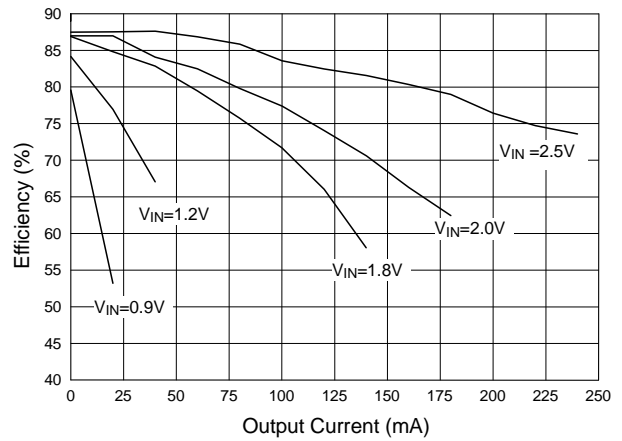


Fig. 37 AIC1642-37 Efficiency (100μH)

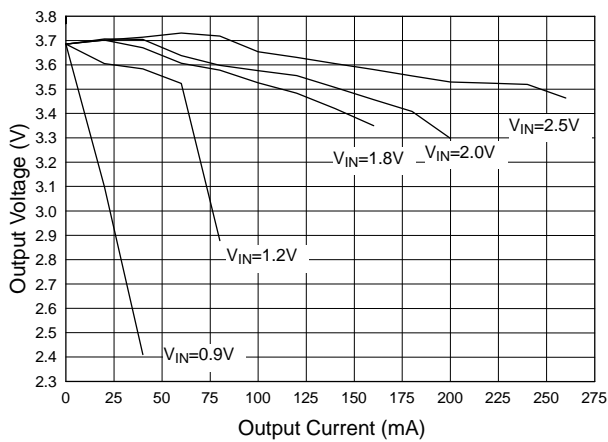


Fig. 38 AIC1642-37 Load Regulation (L=47μH)

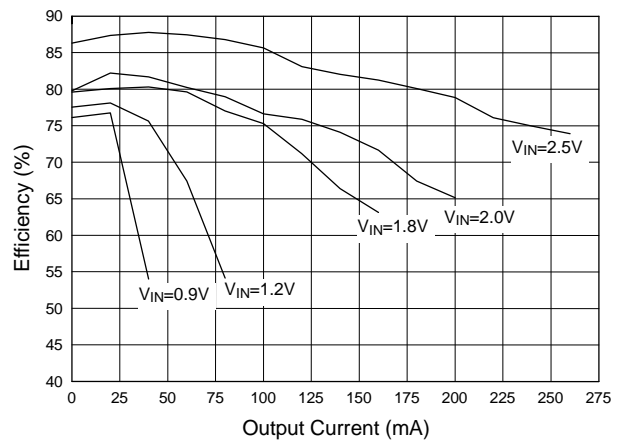


Fig. 39 AIC1642-37 Efficiency (47μH)

TYPICAL PERFORMANCE CHARACTERISTICS (Continued)

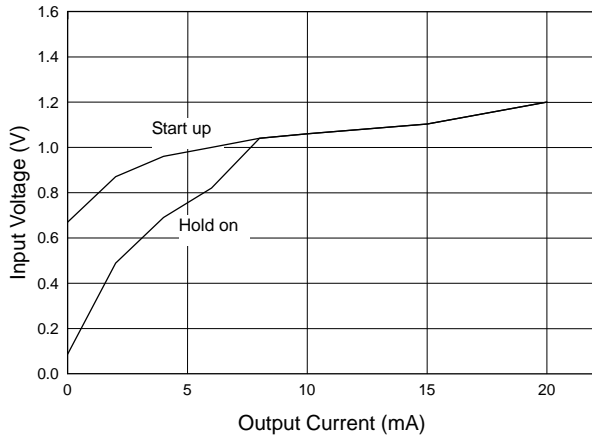


Fig. 40 AIC1642-37 Start-up & Hold-on Voltage (L=100 μ H)

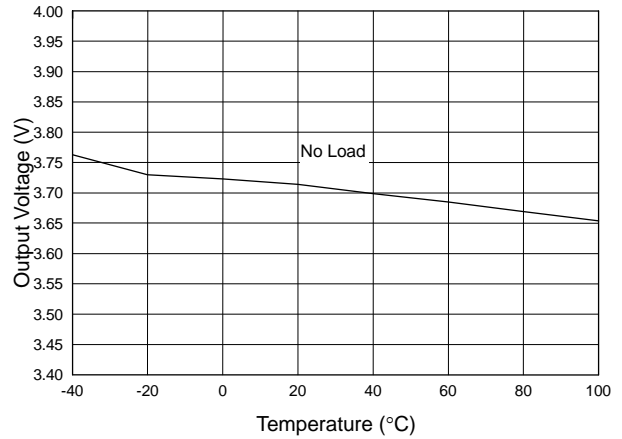


Fig. 41 AIC1642-37 Output Voltage vs. Temperature

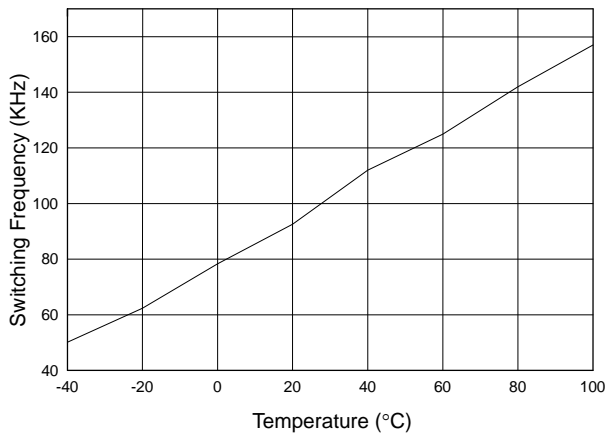


Fig. 42 AIC1642-37 Switching Frequency vs. Temperature

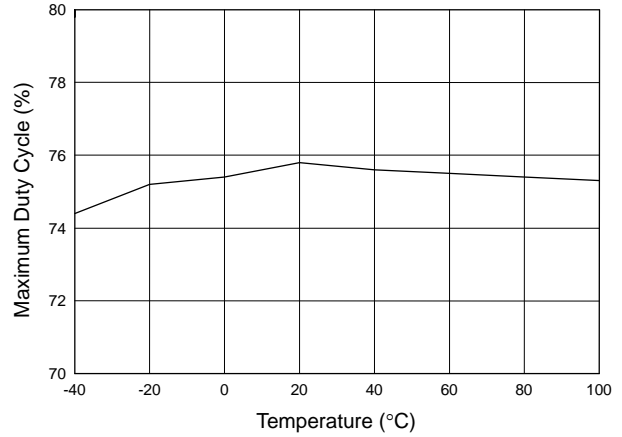


Fig. 43 AIC1642-37 Maximum Duty Cycle vs Temperature

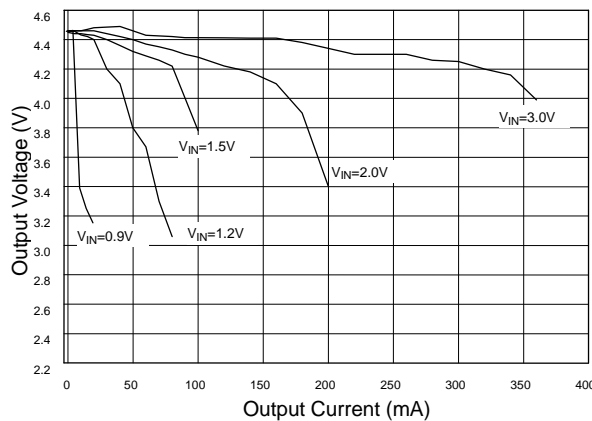


Fig. 44 AIC1642-45 Load Regulation (L=100 μ H)

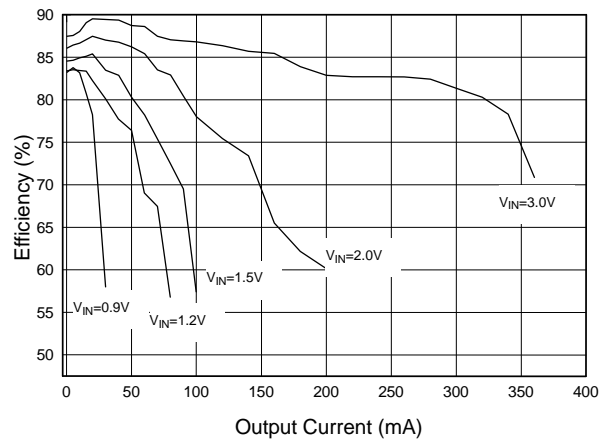


Fig. 45 AIC1642-45 Efficiency (L=100 μ H)

TYPICAL PERFORMANCE CHARACTERISTICS (Continued)

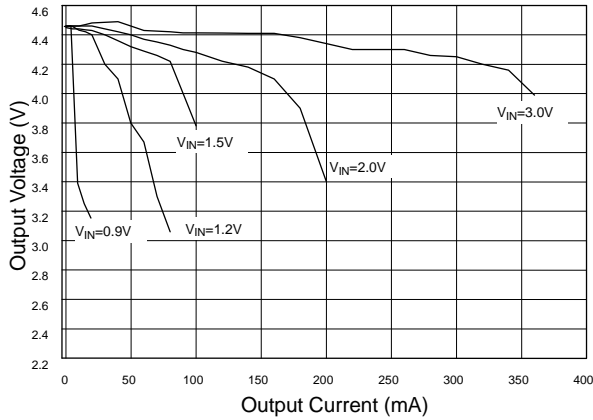


Fig. 46 AIC1642-45 Load Regulation (L=100µH)

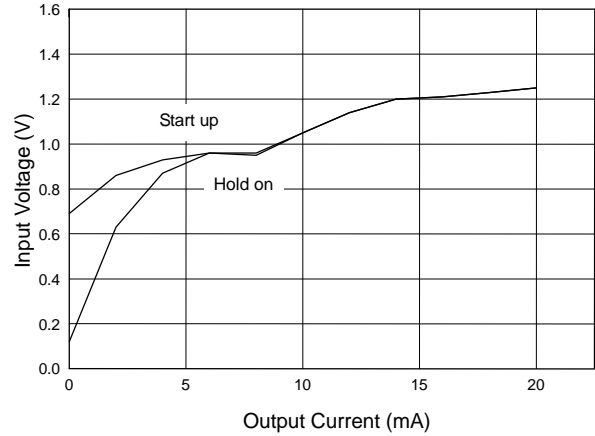


Fig. 47 AIC1642-45 Start-up & Hold-On Voltage (L=100µH)

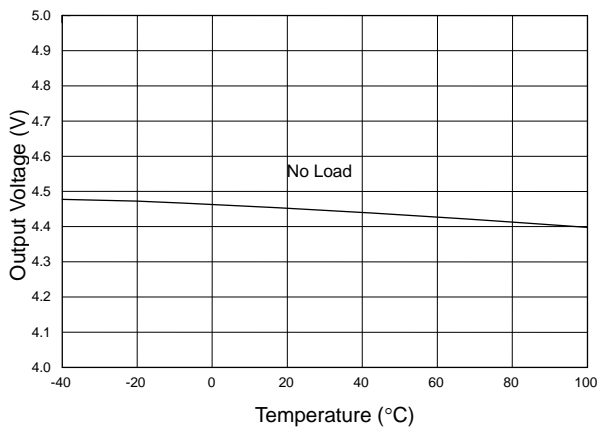


Fig. 48 AIC1642-45 Output Voltage vs. Temperature

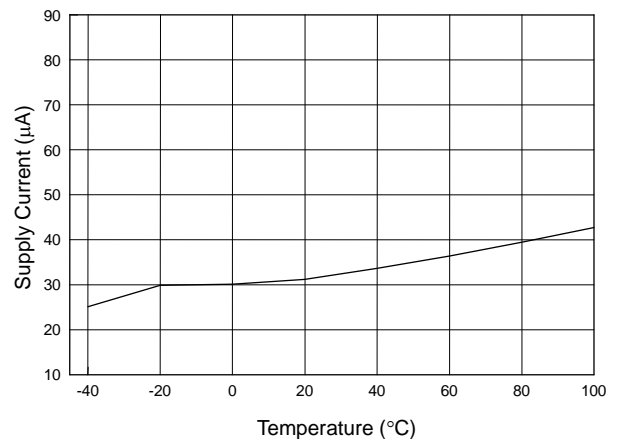


Fig. 49 AIC1642-45 Supply Current vs. Temperature

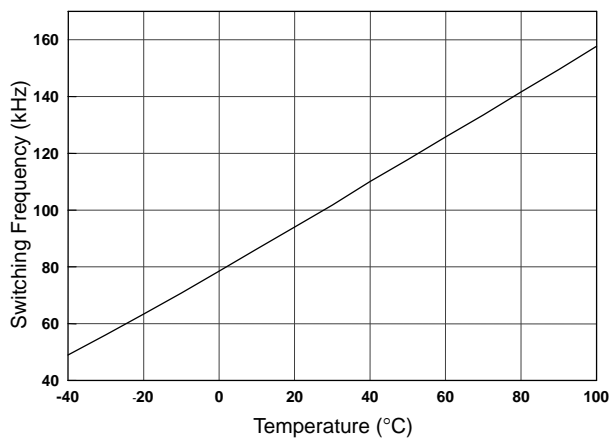


Fig. 50 AIC1642-45 Switching Frequency vs. Temperature

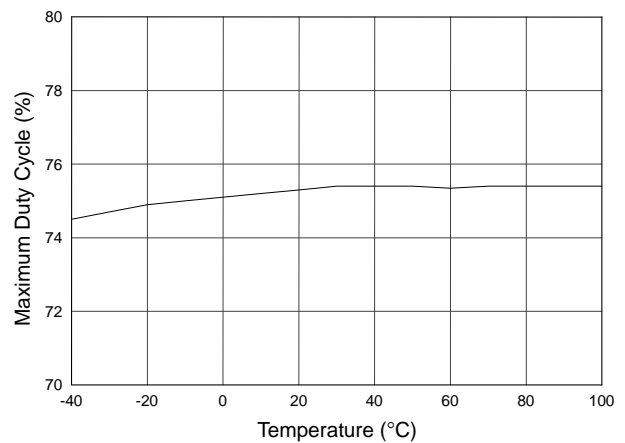


Fig. 51 AIC1642-45 Maximum Duty Cycle vs. Temperature

TYPICAL PERFORMANCE CHARACTERISTICS (Continued)

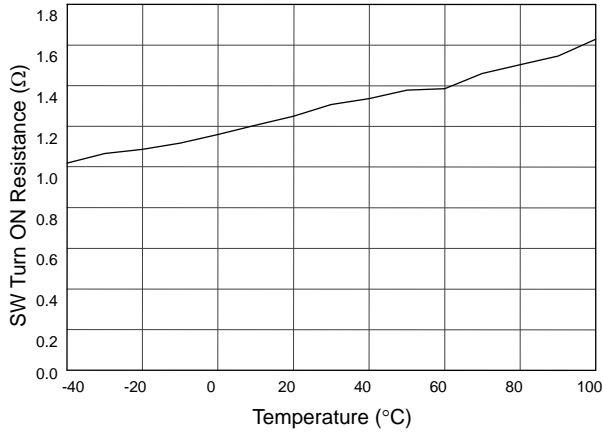


Fig. 52 AIC1642-45 SW Turn ON Resistance vs. Temperature

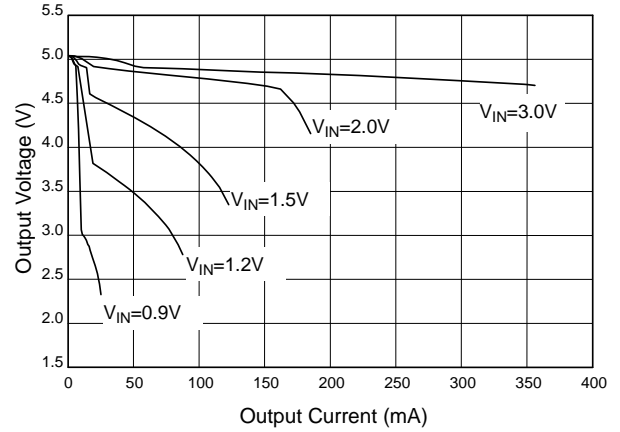


Fig. 53 AIC1642-50 Load Regulation (L=100μH CD54)

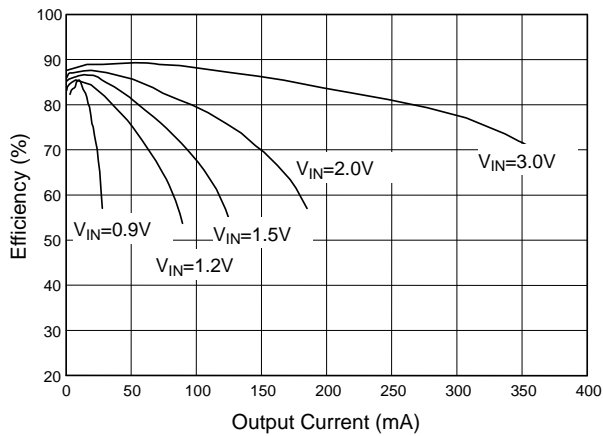


Fig. 54 AIC1642-50 Efficiency (L=100μH CD54)

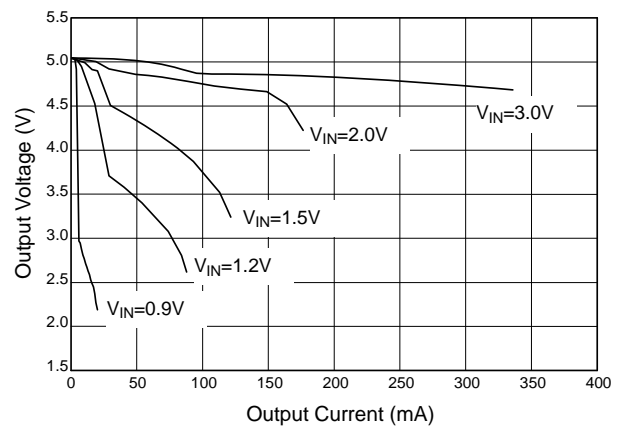


Fig. 55 AIC1642-50 Load Regulation (L=47μH CD54)

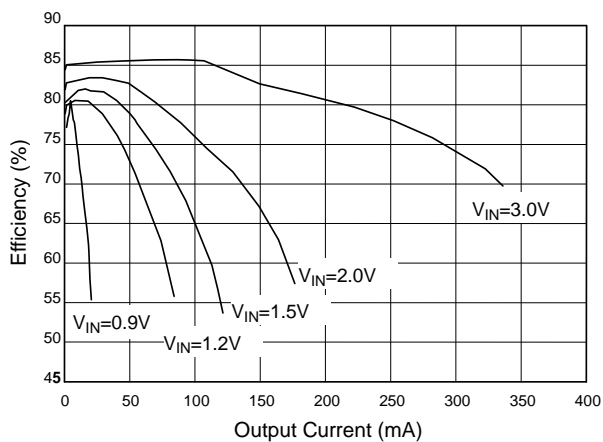


Fig. 56 AIC1642-50 Efficiency (L=47μH CD54)

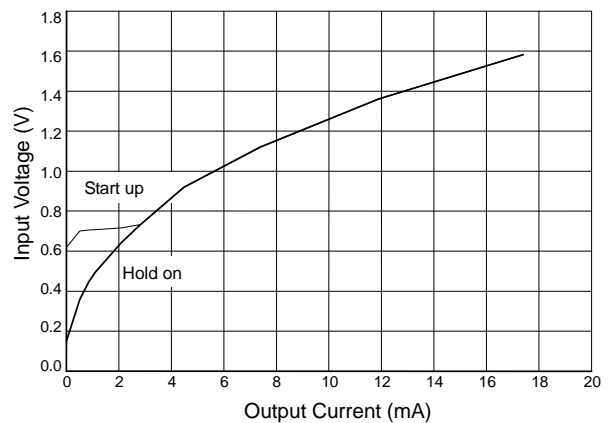


Fig. 57 AIC1642-50 Start-up & Hold-on Voltage (L=100μH CD50)

TYPICAL PERFORMANCE CHARACTERISTICS (Continued)

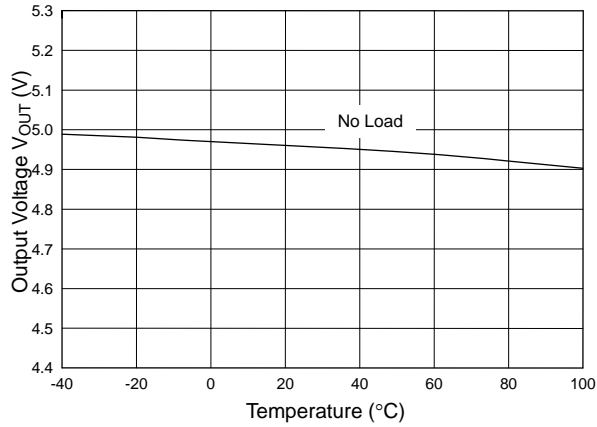


Fig. 58 AIC1642-50 Output Voltage vs. Temperature

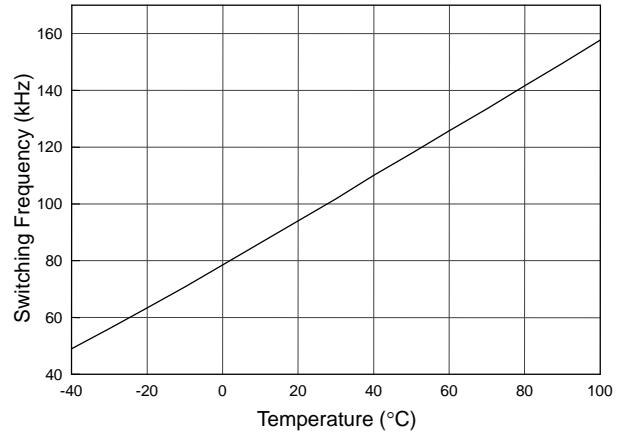


Fig. 59 AIC1642-50 Switching Frequency vs. Temperature

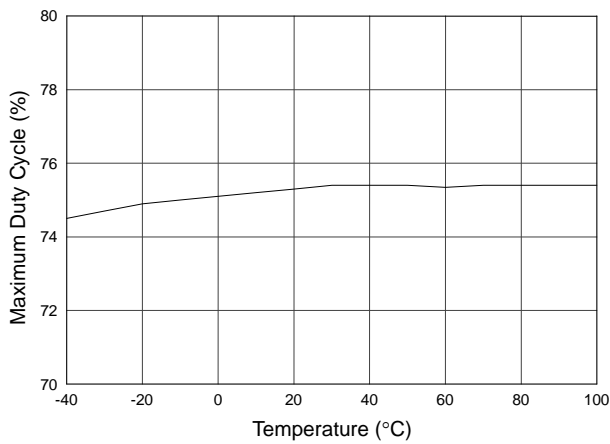


Fig. 60 AIC1642-50 Maximum Duty Cycle vs. Temperature

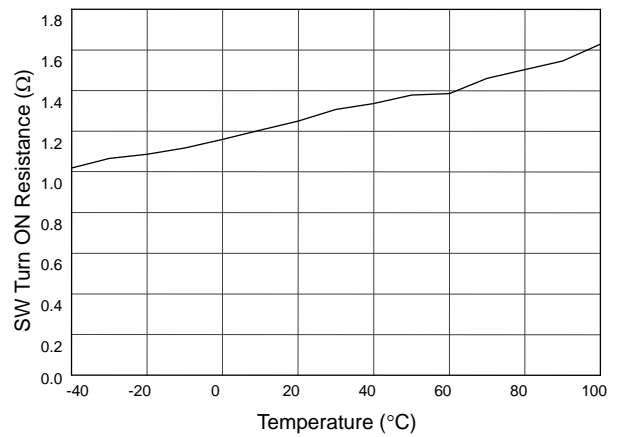


Fig. 61 AIC1642-50 SW Turn ON Resistance vs. Temperature

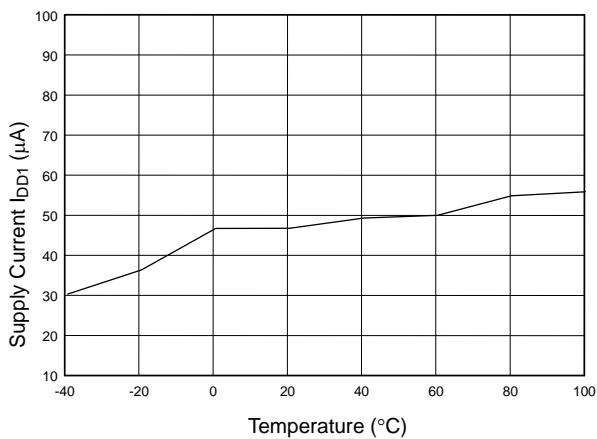


Fig. 62 AIC1642-50 Supply Current vs. Temperature

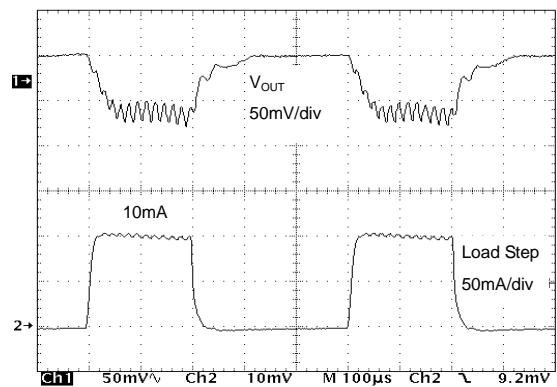


Fig. 63 Load Transient Response
($L_1=100\mu H$, $C_2=47\mu F$, $V_{IN}=2V$)

TYPICAL PERFORMANCE CHARACTERISTICS (Continued)

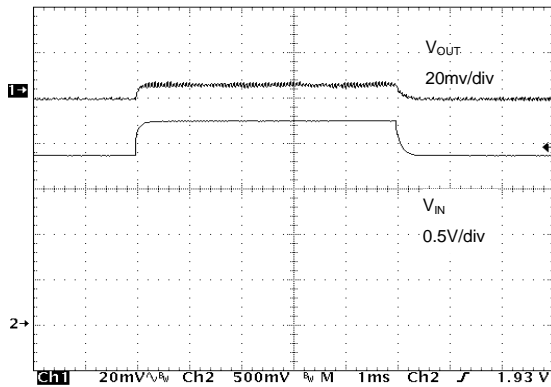
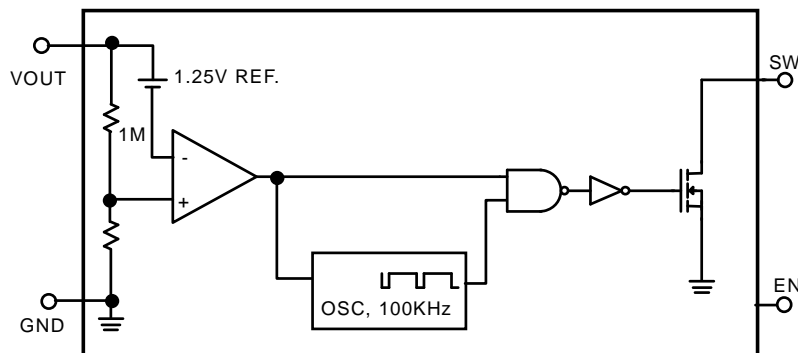


Fig. 64 Line Transient Response
($L_1=100\mu\text{H}$, $C_2=47\mu\text{F}$)

BLOCK DIAGRAM



PIN DESCRIPTIONS

- GND – Ground. Must be low impedance; solder directly to ground plane.
- VOUT – IC supply pin. Connect VOUT to the regulator output.

- SW – Internal drain of N-MOSFET switch.
- EN (5 Pin) – Chip Enable. This pin is not allowed to float.

3-Pin One-Cell Step-Up DC/DC Converter

APPLICATION INFORMATION

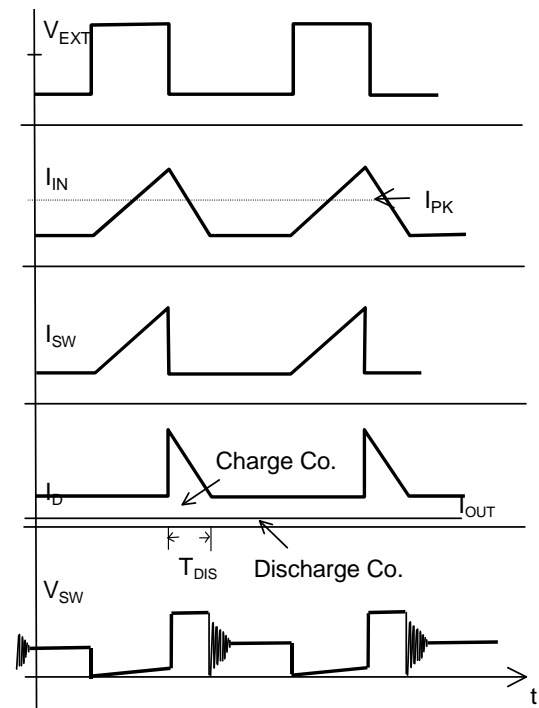
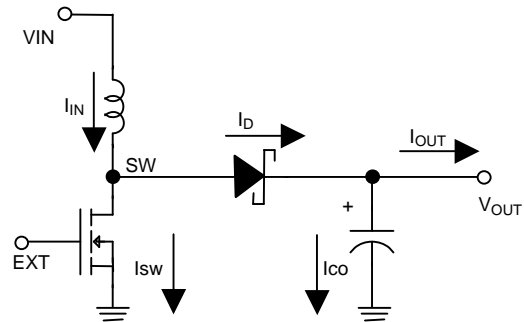
GENERAL DESCRIPTION

AIC1642 PFM (pulse frequency modulation) controller ICs combine a switch mode regulator, N-channel power MOSFET, precision voltage reference, and voltage detector in a single monolithic device. They offer extreme low quiescent current, high efficiency, and very low gate threshold voltage to ensure start-up with low battery voltage (0.8V typ.). Designed to maximize battery life in portable products, and minimize switching losses by only switching as needed service the load.

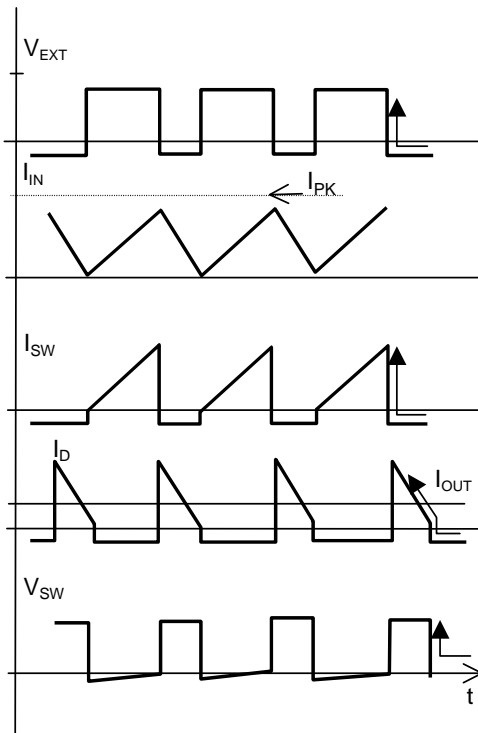
PFM controllers transfer a discrete amount of energy per cycle and regulate the output voltage by modulating switching frequency with the constant turn-on time. Switching frequency depends on load, input voltage, and inductor value, and it can range up to 100KHz. The SW on-resistance is typically 1.9 to 2.2Ω to minimize switch losses.

When the output voltage drops, the error comparator enables 100kHz oscillator that turns on the MOSFET around 7.5us and 2.5us off time. Turning on the MOSFET allows inductor current to ramp up, storing energy in a magnetic field. When MOSFET turns off that force inductor current through diode to the output capacitor and load. As the stored energy is depleted, the current ramp down until the diode turns off. At this point, inductor may ring due to residual energy and stray capacitance. The output capacitor stores charge when current flowing through the diode is high, and release it when current is low, thereby maintaining a steady voltage across the load. As the load increases, the output capacitor discharges faster and the error comparator initiates cycles sooner, increasing the switching frequency. The maximum duty cycle ensure adequate time for energy transfer to output during the second half each cycle. Depending on circuit, PFM controller can operate in either discontinuous mode or continuous conduction mode. Continuous conduction mode

means that the inductor current does not ramp to zero during each cycle.



Discontinuous Conduction Mode



Continuous Conduction Mode

Continuous Conduction Mode

At the boundary between continuous and discontinuous mode, output current (I_{OB}) is determined by

$$I_{OB} = \left(\frac{V_{IN}}{V_{OUT}} \right) * \frac{1}{2} * \frac{V_{IN}}{L} * T_{ON} * (1 - x)$$

where V_d is the diode drop,

$$x = (R_{ON} + R_S) * \frac{T_{ON}}{L}$$

R_{ON} = Switch turn on resistance, R_S = Inductor DC resistance

T_{ON} = Switch ON time

In the discontinuous mode, the switching frequency (F_{sw}) is

$$F_{sw} = \frac{2 * (L) * (V_{OUT} + V_D - V_{IN}) * (I_{OUT})}{V_{IN}^2 * T_{ON}^2} (1 + x)$$

In the continuous mode, the switching frequency is

$$f_{sw} = \frac{1}{T_{ON}} \frac{(V_{OUT} + V_D - V_{IN})}{(V_{OUT} + V_D - V_{SW})} * \left[1 + \frac{x}{2} \left(\frac{V_{IN} - V_{SW}}{V_{OUT} + V_D - V_{SW}} \right) \right] \cong \frac{1}{T_{ON}} \left(\frac{V_{OUT} + V_D - V_{IN}}{V_{OUT} + V_D - V_{SW}} \right)$$

where V_{sw} = switch drop and proportion to output current.

Inductor Selection

To operate as an efficient energy transfer element, the inductor must fulfill three requirements. First, the inductance must be low enough for the inductor to store adequate energy under the worst case condition of minimum input voltage and switch ON time. Second, the inductance must also be high enough so maximum current rating of AIC1642 and inductor are not exceeded at the other worst case condition of maximum input voltage and ON time. Lastly, the inductor must have sufficiently low DC resistance so excessive power is not lost as heat in the windings. But unfortunately this is inversely related to physical size.

Minimum and maximum input voltage, output voltage and output current must be established in advance and then inductor can be selected.

In discontinuous mode operation, at the end of the switch ON time, peak current and energy in the inductor build according to

$$I_{PK} = \left(\frac{V_{IN}}{R_{ON} + R_S} \right) * \left(1 - \exp\left(-\frac{R_{ON} + R_S}{L} * T_{ON}\right) \right) \cong \left(\frac{V_{IN}}{L} \right) * (T_{ON}) * \left(1 - \frac{x}{2} \right) \cong \frac{V_{IN}}{L} T_{ON} \quad (\text{simple loss equation}),$$

where $x = (R_{ON} + R_S) * \frac{T_{ON}}{L}$

$$E_L = \frac{1}{2} L * I_{pk}^2$$

Power required from the inductor per cycle must be equal or greater than

$$P_{L/fsw} = (V_{OUT} + V_D - V_{IN}) * (I_{OUT}) * \left(\frac{1}{f_{sw}}\right)$$

In order for the converter to regulate the output.

When loading is over IOB, PFM controller operates in continuous mode. Inductor peak current can be derived from

$$I_{PK} = \left(\frac{V_{OUT} + V_D - V_{SW} - \frac{x}{2}}{V_{IN} - V_{SW}}\right) * I_{OUT} + \left(\frac{V_{IN} - V_{SW}}{2L}\right) * T_{ON} * \left(1 - \frac{x}{2}\right)$$

Valley current (Iv) is

$$I_V = \left(\frac{V_{OUT} + V_D - V_{SW} - \frac{x}{2}}{V_{IN} - V_{SW}}\right) * I_{OUT} - \left(\frac{V_{IN} - V_{SW}}{2L}\right) * T_{ON} * \left(1 - \frac{x}{2}\right)$$

Table 1 Indicates resistance and height for each coil.

Power Inductor Type		Inductance (μH)	Resistance (Ω)	Rated Current (A)	Height (mm)
Coilcraft SMT Type (www.coilcraft.com)	DS1608	22	0.10	0.7	2.9
		47	0.18	0.5	
		100	0.38	0.3	
	DO3316	22	0.08	2.7	5.2
		47	0.14	1.8	
Sumida SMT Type CD54		47	0.25	0.7	4.5
		100	0.50	0.5	
Hold SMT Type PM54		47	0.25	0.7	4.5
		100	0.50	0.5	
Hold SMT Type PM75		33	0.11	1.2	5.0

Capacitor Selection

A poor choice for an output capacitor can result in poor efficiency and high output ripple. Ordinary aluminum electrolytic, while inexpensive may have unacceptably poor ESR and ESL. There are low ESR aluminum capacitors for switch mode DC-DC converters which work much well than general unit. Tantalum capacitors provide still better performance at more expensive. OS-CON capacitors have extremely low ESR in a small size. If capacitance is reduced, output ripple will increase.

Most of the input supply is supplied by the input bypass capacitor, the capacitor voltage rating should be at least 1.25 times greater than a maximum input voltage.

Diode Selection

Speed, forward drop, and leakage current are the three main considerations in selecting a rectifier diode. Best performance is obtained with Schottky rectifier diode such 1N5819. Motorola makes MBR0530 in surface mount. For lower

output power a 1N4148 can be used although efficiency and start-up voltage will suffer substantially.

Component Power Dissipation

Operating in discontinuous mode, power loss in the winding resistance of inductor can be approximate equal to

$$P_{DL} = \frac{2}{3} \left(\frac{T_{ON}}{L}\right) * (R_D) * \left(\frac{V_{OUT} + V_F}{V_{OUT}}\right) * (P_{OUT})$$

where $P_{OUT} = V_{OUT} * I_{OUT}$; R_S = Inductor DC R;

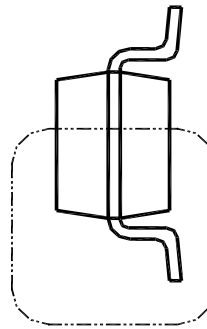
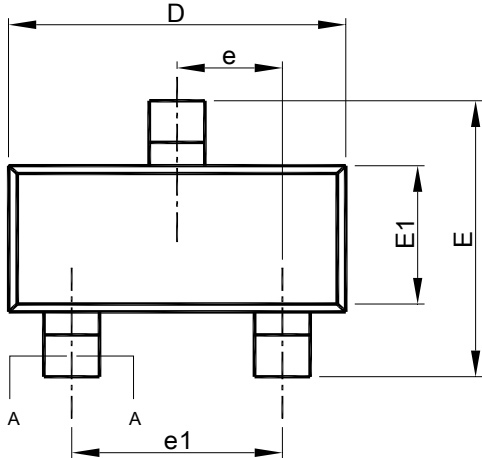
V_D = Diode drop.

The power dissipated in a switch loss is

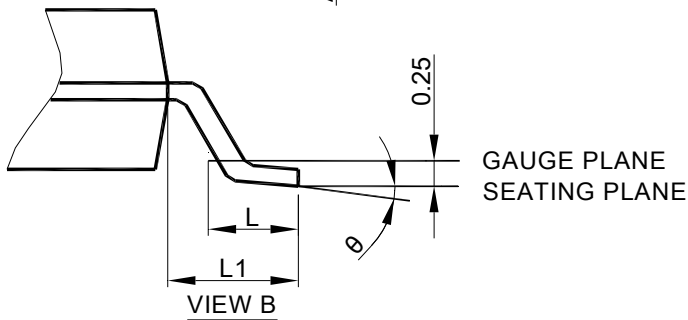
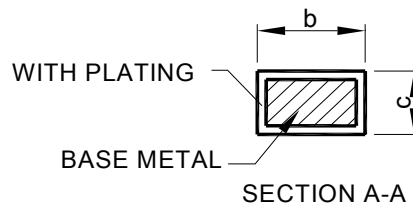
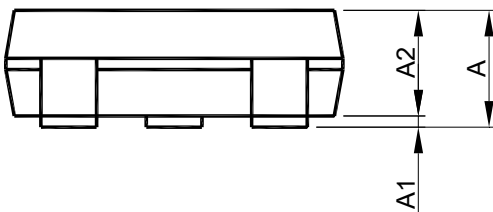
$$P_{DSW} = \frac{2}{3} \left(\frac{T_{ON}}{L}\right) * (R_{ON}) * \left(\frac{V_{OUT} + V_D - V_{IN}}{V_{OUT}}\right) * (P_{OUT})$$

The power dissipated in rectifier diode is

$$P_{Dd} = \left(\frac{V_D}{V_{OUT}}\right) * (P_{OUT})$$

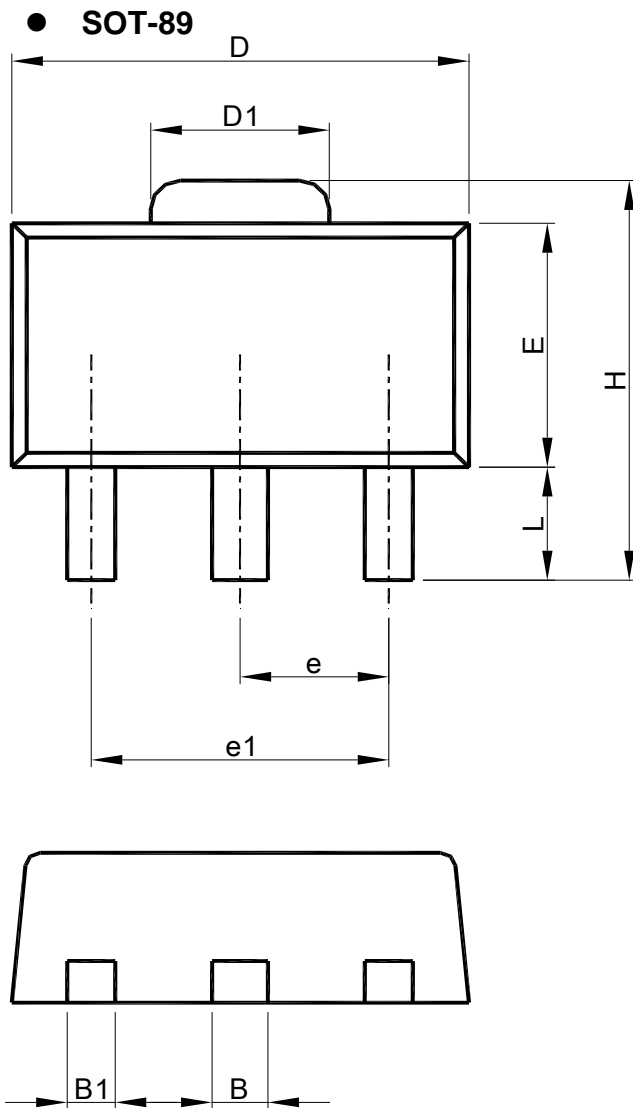
PHYSICAL DIMENSIONS (unit: mm)
SOT-23


SEE VIEW B



SYMBOL	SOT-23	
	MILLIMETERS	
	MIN.	MAX.
A	0.95	1.45
A1	0.00	0.15
A2	0.90	1.30
b	0.30	0.50
c	0.08	0.22
D	2.80	3.00
E	2.60	3.00
E1	1.50	1.70
e	0.95 BSC	
e1	1.90 BSC	
L	0.30	0.60
L1	0.60 REF	
θ	0°	8°

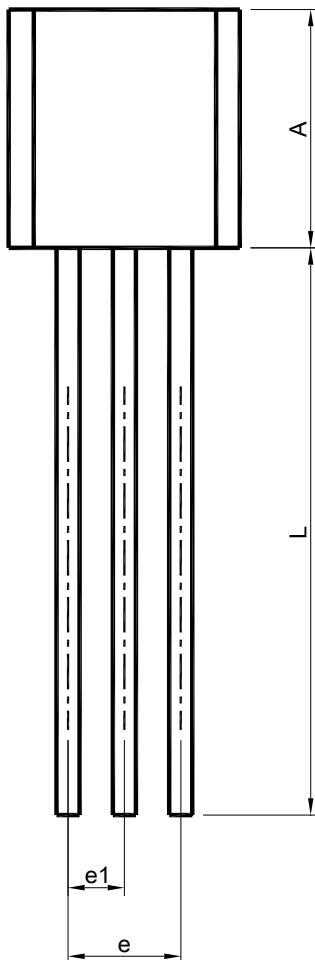
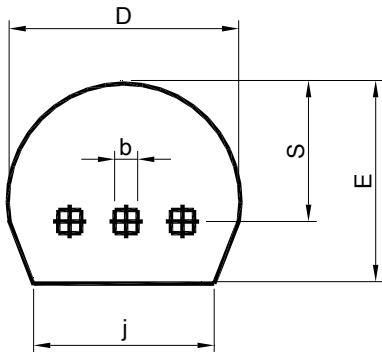
- Note: 1. Refer to JEDEC MO-178.
 2. Dimension "D" does not include mold flash, protrusions or gate burrs. Mold flash, protrusion or gate burrs shall not exceed 10 mil per side.
 3. Dimension "E1" does not include inter-lead flash or protrusions.
 4. Controlling dimension is millimeter, converted inch dimensions are not necessarily exact.



SOT-89		
MILLIMETERS		
SYMBOL	MIN.	MAX.
A	1.40	1.60
B	0.44	0.56
B1	0.36	0.48
C	0.35	0.44
D	4.40	4.60
D1	1.50	1.83
E	2.29	2.60
e	1.50 BSC	
e1	3.00 BSC	
H	3.94	4.25
L	0.89	1.20

- Note: 1. Refer to JEDEC TO-243AA.
 2. Dimension "D" does not include mold flash, protrusions or gate burrs. Mold flash, protrusion or gate burrs shall not exceed 6 mil per side.
 3. Dimension "E" does not include inter-lead flash or protrusions.
 4. Controlling dimension is millimeter, converted inch dimensions are not necessarily exact.

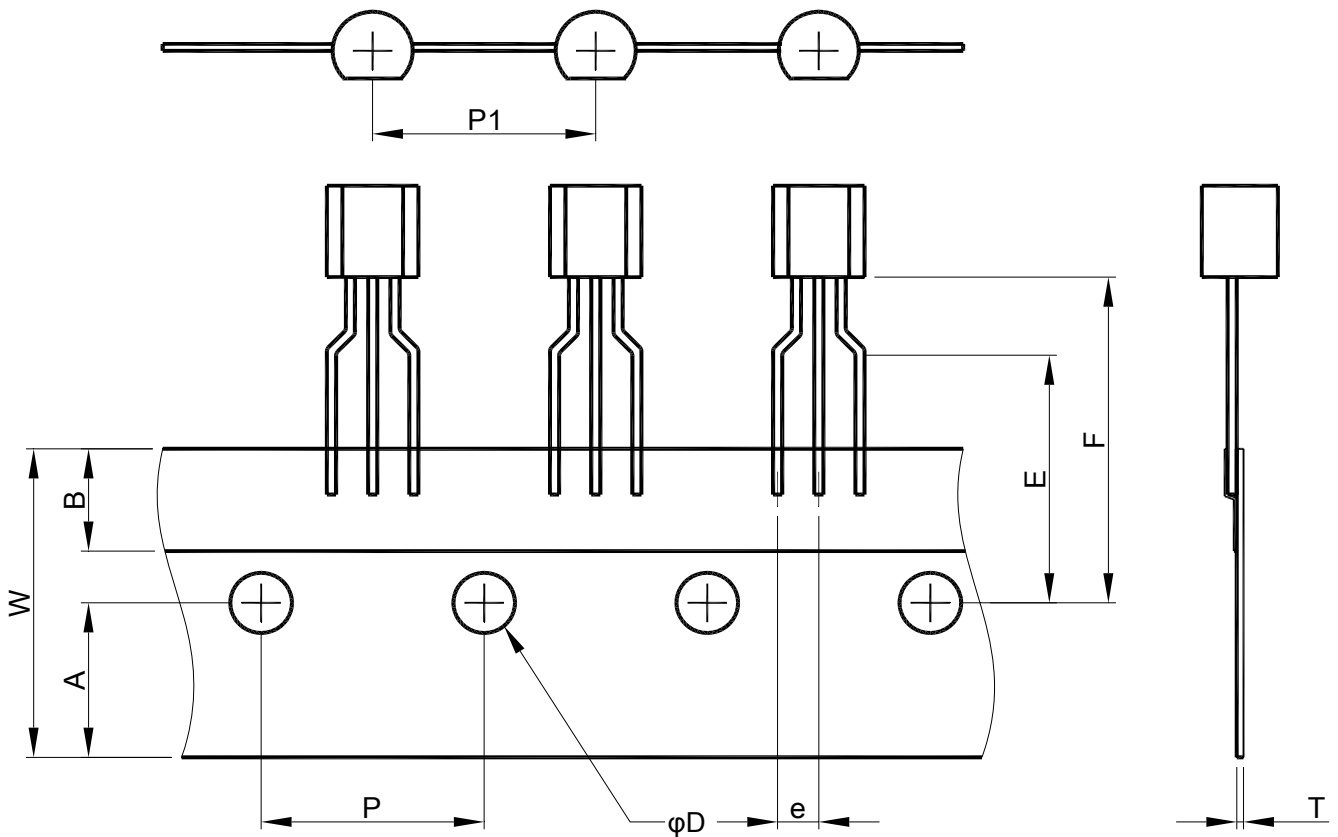
- TO-92 (Straight lead option available in Bag packing)



SYMBOL	TO-92	
	MILLIMETERS	
	MIN.	MAX.
A	4.32	5.33
b	0.36	0.47
D	4.45	5.20
E	3.18	4.19
e	2.42	2.66
e1	1.15	1.39
j	3.43	
L	12.70	
S	2.03	2.66

- Note: 1. Refer to JEDEC TO-226.
 2. Dimension "D" does not include mold flash, protrusions or gate burrs. Mold flash, protrusion or gate burrs shall not exceed 6 mil per side .
 3. Dimension "A" does not include inter-lead flash or protrusions.
 4. Controlling dimension is millimeter, converted inch dimensions are not necessarily exact.

- TO-92 (Formed lead option available in Reel packing)



SYMBOL	W	A	B	E	F
SPEC.	18.0±0.2	9.0±0.2	6.0±0.20	16.0±0.5	19.0±0.5
SYMBOL	P	P1	D	e	T
SPEC.	12.7 BSC	12.7 BSC	4.0±0.2	2.5 BSC	0.6±0.1

Note:

Information provided by AIC is believed to be accurate and reliable. However, we cannot assume responsibility for use of any circuitry other than circuitry entirely embodied in an AIC product; nor for any infringement of patents or other rights of third parties that may result from its use. We reserve the right to change the circuitry and specifications without notice.

Life Support Policy: AIC does not authorize any AIC product for use in life support devices and/or systems. Life support devices or systems are devices or systems which, (i) are intended for surgical implant into the body or (ii) support or sustain life, and whose failure to perform, when properly used in accordance with instructions for use provided in the labeling, can be reasonably expected to result in a significant injury to the user.