

1.0MHz, 3.5A Synchronous Step-Down Converter

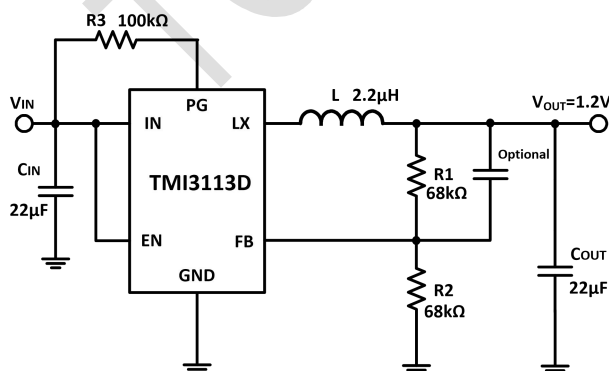
FEATURES

- High Efficiency: Up to 96% at 5V to 3.3V
- 1.0MHz Switching Frequency
- Up to 3.5A Output Current
- No Schottky Diode Required
- 2.7V to 5.5V Input Voltage Range
- Output Voltage as Low as 0.6V
- PFM Mode for High Efficiency in Light Load
- 100% Duty Cycle in Dropout Operation
- Low Quiescent Current: 50 μ A
- Short Circuit Protection
- Thermal Shutdown Protection
- Power Good Output Function
- Inrush Current Limit and Soft Start
- Input overvoltage protection (OVP)
- <1 μ A Shutdown Current
- DFN3*3-10 package

APPLICATIONS

- Cellular and Smart Phones
- Wireless and DSL Modems
- Portable Instruments
- Digital and Video Cameras
- PC Cards

TYPICAL APPLICATION



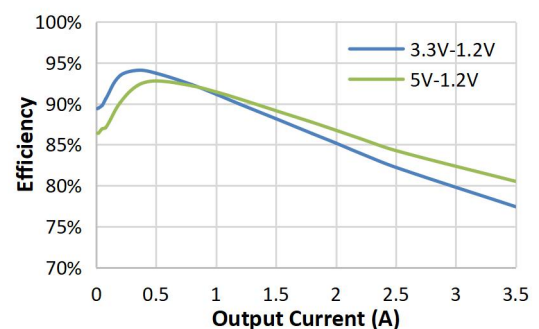
GENERAL DESCRIPTION

The TMI3113D is a 1.0MHz constant frequency, current mode step-down converter. It is ideal for portable equipment requiring high output current up to 3.5A from single-cell Lithium-ion batteries. The device also can run at 100% duty cycle for low dropout operation, extending battery life in portable systems while light load operation provides very low output ripple for noise sensitive applications. The 1MHz high switching frequency of TMI3113D could minimize the size of external components while keeping switching losses low. The internal slope compensation setting allows the device to operate with smaller inductor values to optimize size and provide efficient operation. TMI3113D has power good function and it is offered in DFN3*3-10 package.

The device offers two operation modes, PWM control and PFM Mode switching control, which allow a high efficiency over the wider range of the load.

Efficiency

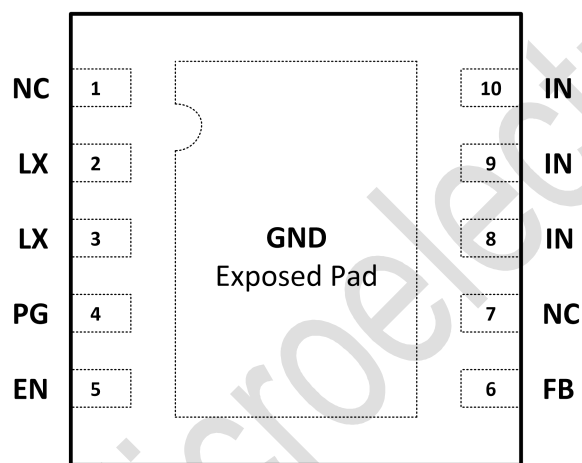
$V_{OUT}=1.2V$, $I_{OUT}=0.01A$ to $3.5A$, $T_A=25^{\circ}C$



ABSOLUTE MAXIMUM RATINGS

Parameter	Value	Unit
Input Supply Voltage	-0.3~7	V
LX Voltages	-0.3~7	V
EN, FB, PG Voltage	-0.3~7	V
Junction Temperature (Note2)	160	°C
Power Dissipation	2.6	W
Lead Temperature (Soldering, 10s)	300	°C

PACKAGE/ORDER INFORMATION



DFN3*3-10 (Top View)

Top Mark: T3113D/YYXXX (T3113D: Device Code, YYXXX: Inside Code)

Part Number	Package	Top mark	Quantity/ Reel
TMI3113D	DFN3*3-10	T3113D YYXXX	5000

TMI3113D devices are Pb-free and RoHS compliant.

PIN DESCRIPTIONS

Pin	Name	Function
1	NC	No Connection
2	LX	Power Switch Output. It is the switch node connection to Inductor. This pin connects to the drains of the internal P-ch and N-ch MOSFET switches.
3		
4	PG	Power Good Open Drain Output Pin.
5	EN	Enable Pin. Drive EN above 1.5V to turn on the part. Drive EN below 0.4V to turn it off. Do not leave EN floating.
6	FB	Output Voltage Feedback Pin.
7	NC	No Connection
8	IN	Power Supply Input Pin.
9		
10		
11	GND	Ground Pin (Exposed Pad).

ESD RATING

Items	Description	Value	Unit
V_{ESD}	Human Body Model for all pins	±2000	V

JEDEC specification JS-001

RECOMMENDED OPERATING CONDITIONS

Items	Description	Min	Max	Unit
Voltage Range	IN	2.7	5.5	V
T_J	Operating Junction Temperature Range	-40	125	°C
T_A	Operating Ambient Temperature Range	-40	85	°C

THERMAL RESISTANCE

Items	Description	Value	Unit
θ_{JA}	Junction-to-ambient thermal resistance	48	°C/W
θ_{JC}	Junction-to-case(bottom) thermal resistance	8	°C/W

ELECTRICAL CHARACTERISTICS

($V_{IN}=V_{EN}=3.6V$, $V_{OUT}=1.8V$, $T_A = 25^\circ C$, unless otherwise noted.)

Parameter	Conditions	Min	Typ	Max	Unit
Input Voltage Range		2.7		5.5	V
Input OVP Threshold	V_{IN} Rising		6.1		V
Input UVLO Threshold	V_{IN} Rising		2.5		V
Input UVLO Hysteresis			0.5		V
Quiescent Current	$V_{EN}=2.0V$, $I_{OUT}=0A$, $V_{FB}=V_{REF} \times 105\%$		50	85	μA
Shutdown Current	$V_{EN} = 0V$		0.1	1.0	μA
Regulated Feedback Voltage V_{FB}	PWM operation, $T_A = 25^\circ C$	0.588	0.600	0.612	V
	PFM operation, No Load, $T_A = 25^\circ C$		0.609		V
Oscillation Frequency	$V_{OUT}=1.8V$		1.0		MHz
	$V_{OUT}=0V$		300		kHz
On Resistance of PMOS	$I_{LX}=100mA$		95		$m\Omega$
On Resistance of NMOS	$I_{LX}=-100mA$		50		$m\Omega$
Peak Current Limit	$V_{IN}= 5V$, $V_{FB}=90\%*V_{REF}$		4		A
EN High Level Input Voltage		1.5			V
EN Low Level Input Voltage				0.4	V
EN Leakage Current			± 0.01	± 1.0	μA
Power Good Threshold	V_{FB} Reference to V_{REF} voltage		91%		
Power Good Sink Current				2	mA
LX Leakage Current	$V_{EN} = 0V$, $V_{IN} = V_{LX} = 5V$		± 0.01	± 1.0	μA
Thermal Shutdown Threshold (Note 3)			155		$^\circ C$
Thermal Shutdown Hysteresis (Note 3)			20		$^\circ C$

Note 1: Absolute Maximum Ratings are those values beyond which the life of a device may be impaired.

Note 2: T_J is calculated from the ambient temperature T_A and power dissipation P_D according to the following formula: $T_J = T_A + (P_D) \times \theta_{JA}$.

Note 3: Thermal shutdown threshold and hysteresis are guaranteed by design.

OPERATION

Overview

The TMI3113D is a high output current monolithic switch mode step-down DC-DC converter. The devices operate at a fixed 1.0MHz switching frequency, and uses a slope compensated current mode architecture. This step-down DC-DC converter can supply up to 3.5A output current and has an input voltage range from 2.7V to 5.5V. It minimizes external component size and optimizes efficiency at the heavy load range. The slope compensation allows the device to remain stable over a wider range of inductor values so that smaller values with lower DCR can be used to achieve higher efficiency. Only a small bypass input capacitor is required at the output.

In light and no load condition, TMI3113D are operating in PFM mode for power saving. In PFM mode, the device ramps up its output voltage with several SW switching pulse, while the error amplifier output voltage V_{COMP} drops. The device stops switching when V_{COMP} voltage drops down the inner threshold, so the FB voltage in PFM mode is a little bit higher than normal 0.6V reference voltage in PWM operation. In no load condition, FB voltage is typically 1.5% higher than normal 0.6V reference voltage.

The adjustable output voltage can be programmed with external feedback dividers, ranging from 0.6V to near the input voltage. It uses internal MOSFETs to achieve high efficiency and can generate very low output voltages by using an internal reference of 0.6V. At dropout operation, the converter duty cycle increases to 100% and the output voltage tracks the input voltage minus the low $R_{DS(ON)}$ drop of the P-channel high-side MOSFET and the inductor DCR. The internal error amplifier and compensation provides excellent transient response, load and line regulation. Internal soft start eliminates any output voltage overshoot when the device is enabled or the input voltage is applied.

Input Over Voltage Protection

TMI3113D has input side over voltage protection function. When input voltage is higher than input OVP threshold 6.1V typical, TMI3113D stops switching operation to protect device works with high input voltage. When input voltage is recovered from OVP and drops down input OVP threshold with OVP hysteresis typical 180mV, the device starts to switch as normal operation automatically. This function protects device from switching in abnormal high input voltage and input surge condition.

Input Under Voltage Lockout

TMI3113D implements input under voltage lockout function to avoid mis-operation at low input voltages. When the input voltage is lower than input UVLO threshold with UVLO hysteresis, the device is shut down. The typical 500mV input UVLO hysteresis value of TMI3113D is useful to prevent device from abnormal switching caused by input voltage oscillation around UVLO threshold during input voltage power-up and power-down with high load condition.

Soft Start

TMI3113D has built-in soft-start circuits to control output voltage rise rate to avoids excessive inrush current during IC start up. The typical soft-start time is 1ms.

Over Current Limit and Output Short Protection

TMI3113D has high side switching current limit function and prevents the device from high load current condition. The typical high side peak current limit value is 4A. When output load current increases and inductor current peak value reaches peak current limit value, high side MOSFET is turned off immediately and the output voltage drops down according to load condition. If output voltage keeps falling down, once the V_{FB} voltage is lower than 300mV typical, the device enters into output short protection condition. In output short protection condition, the switching frequency of TMI3113D decreases from 1MHz to 300kHz and the peak current limit value reduces from 4A to 3.6A typically in order to reduce power consumption and device thermal rise in the condition of output short to GND.

Thermal Shutdown

TMI3113D enters into thermal shutdown once the junction temperature exceeds thermal shutdown threshold 155°C typically. Once the device junction temperature falls below the threshold with hysteresis, TMI3113D returns to normal operation automatically.

Power Good

TMI3113D also has power good open drain output to indicate output voltage status. When input voltage is higher than UVLO and EN is enabled, PG status is determined by output voltage. The PG pin goes high impedance when the output is above 91% of regulated nominal voltage and PG pin is pulled low once output voltage falls below the threshold. When the device is shut down by EN pulling low, the PG is pulled low as well.

FUNCTIONAL BLOCK DIAGRAM

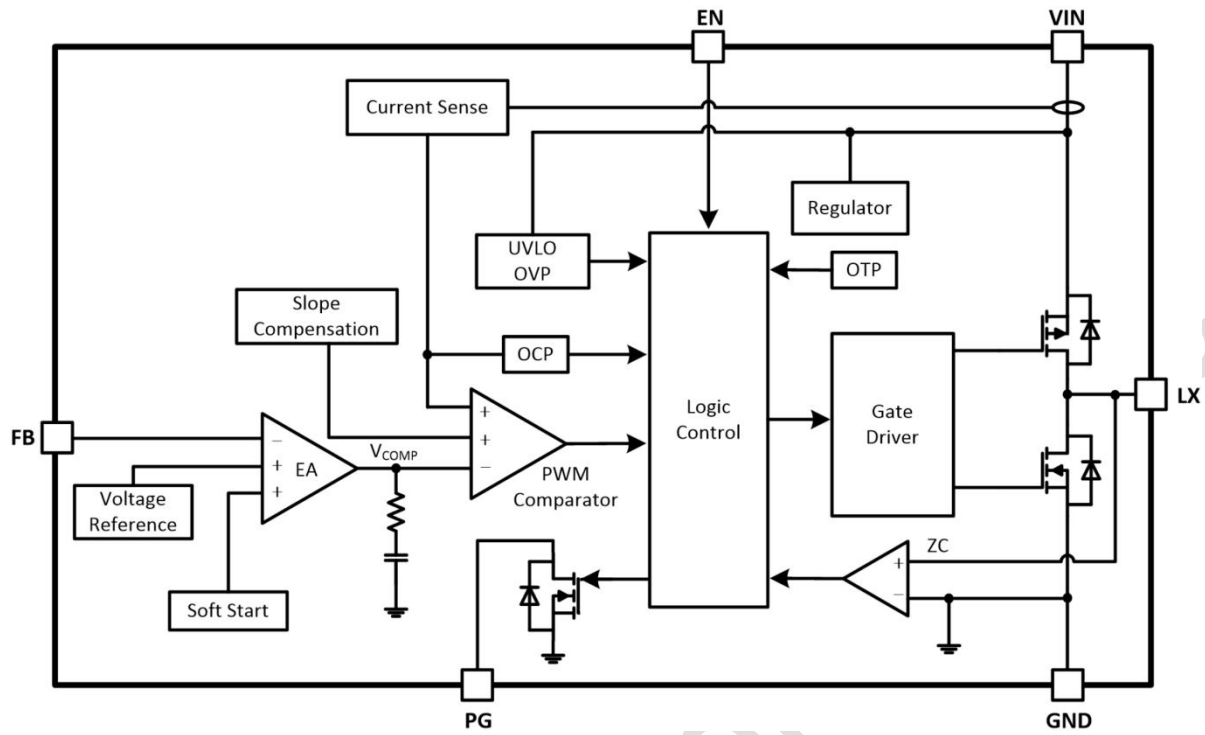


Figure 1. TMI3113D Block Diagram

TOLL Microelectronic

APPLICATION INFORMATION

Setting the Output Voltage

In the first page, the typical application circuit for the TMI3113D is shown. The output voltage of TMI3113D can be externally programmed. Resistors R1 and R2 in typical application program the output to regulate at a voltage higher than 0.6V.

The external resistor sets the output voltage according to the following equation:

$$V_{OUT} = 0.6 \times \left(1 + \frac{R_1}{R_2}\right)$$

$$R_1 = (V_{OUT} / 0.6 - 1) \times R_2$$

Inductor Selection

For most designs, 2.2μH inductance can satisfy most application conditions. Inductance value is related to inductor ripple current value, input voltage, output voltage setting and switching frequency. The inductor value can be derived from the following equation:

$$L = \frac{V_{OUT} \times (V_{IN} - V_{OUT})}{V_{IN} \times \Delta I_L \times f_{OSC}}$$

Where ΔI_L is inductor ripple current. Large value inductors result in lower ripple current and small value inductors result in high ripple current, so inductor value has effect on output voltage ripple value. DC resistance of inductor which has impact on efficiency of DC/DC converter should be taken into account when selecting the inductor.

Input Capacitor Selection

The input capacitor reduces the surge current drawn from the input and switching noise from the device. The input capacitor impedance at the switching frequency should be less than input source impedance to prevent high frequency switching current passing to the input. A low ESR input capacitor sized for maximum RMS current must be used. Ceramic capacitors with X5R or X7R dielectrics are highly recommended because of their low ESR and small temperature coefficients. A 22μF effective capacitance value ceramic capacitor for most applications is sufficient. A large value may be used for improved input voltage filtering.

Output Capacitor Selection

The output capacitor is required to keep the output voltage ripple small and to ensure regulation loop stability. The output capacitor must have low impedance at the switching frequency. Ceramic capacitors with X5R or X7R dielectrics are recommended due to their low ESR and high ripple current ratings. The

output ripple ΔV_{OUT} is determined by:

$$\Delta V_{OUT} \leq \frac{V_{OUT} \times (V_{IN} - V_{OUT})}{V_{IN} \times f_{OSC} \times L} \times \left(ESR + \frac{1}{8 \times f_{osc} \times C3} \right)$$

A 22μF effective capacitance value ceramic capacitor can satisfy most applications.

Layout Consideration

When laying out the printed circuit board, the following checking should be used to ensure proper operation of the TMI3113D. Check the following in your layout:

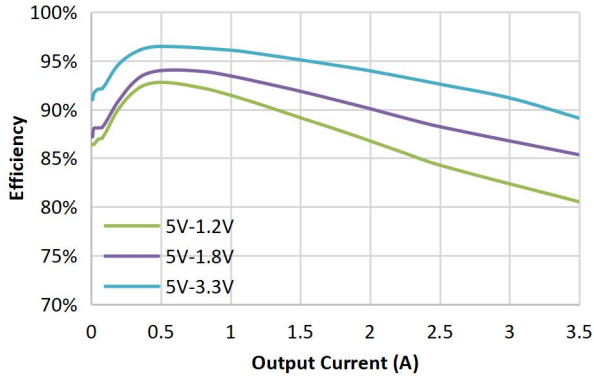
1. The power traces, consisting of the GND trace, the LX trace and the VIN trace should be kept short, direct and wide.
2. Does the (+) plates of C_{IN} connect to Vin as closely as possible. This capacitor provides the AC current to the internal power MOSFETs.
3. Keep the switching node, LX, away from the sensitive FB node.
4. Keep the (-) plates of C_{IN} and C_{OUT} as close as possible.

TYPICAL PERFORMANCE CHARACTERISTICS

$V_{IN}=5V$, $V_{OUT}=1.2V$, $C_{IN}=22\mu F$, $C_{OUT}=22\mu F$, $L=2.2\mu H$, $T_A=25^\circ C$, TMI3113, unless otherwise noted

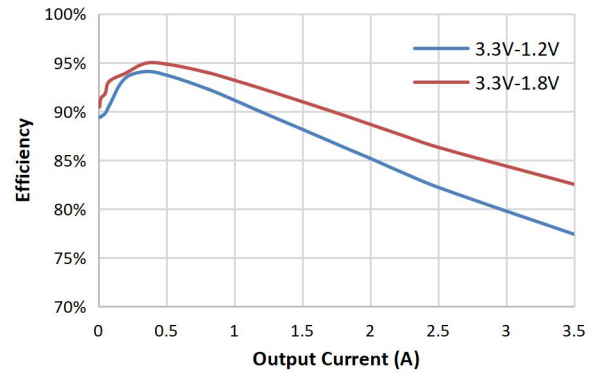
Efficiency at $V_{IN} = 5V$

$V_{IN}=5V$, $L=2.2\mu H$, $DCR=20m\Omega$



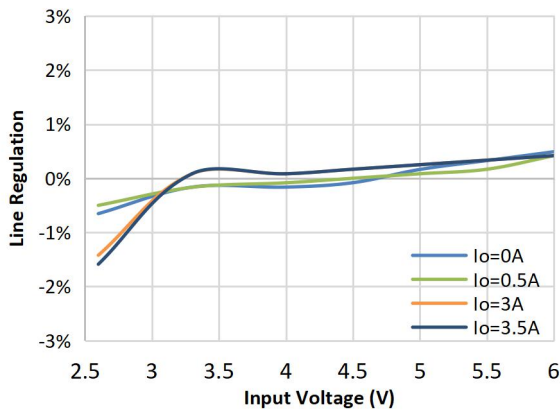
Efficiency at $V_{IN} = 3.3V$

$V_{IN}=3.3V$, $L=2.2\mu H$, $DCR=20m\Omega$



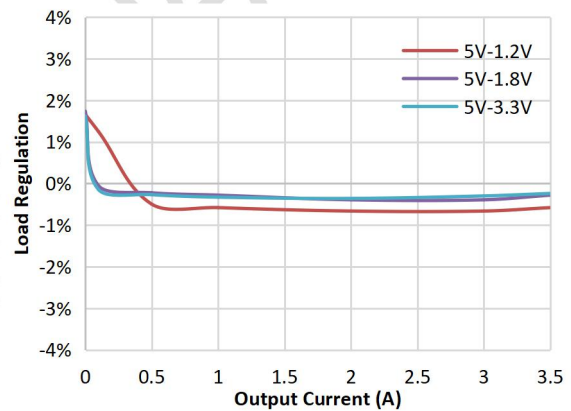
Line Regulation at $V_{OUT}=1.2V$

$V_{OUT}=1.2V$, $T_A=25^\circ C$



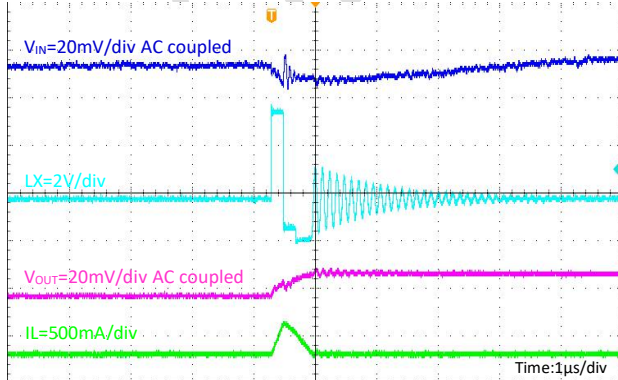
Load Regulation at $V_{IN} = 5V$

$V_{IN}=5V$, $T_A=25^\circ C$



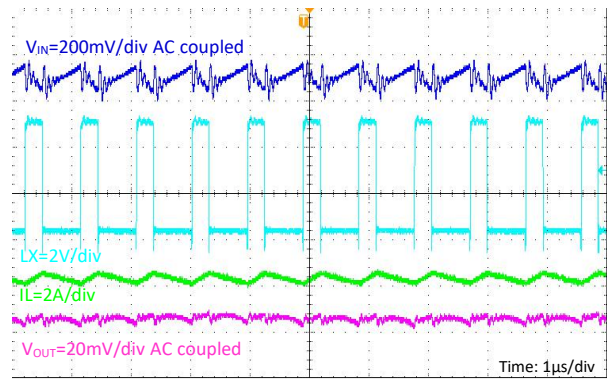
Steady State Operation

$V_{IN}=5V$, $V_{OUT}=1.2V$, No Load



Steady State Operation

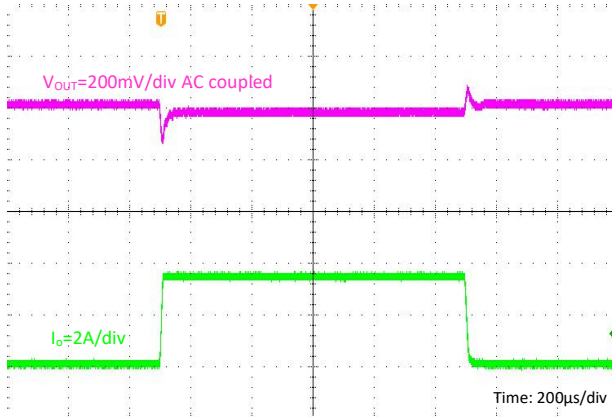
$V_{IN}=5V$, $V_{OUT}=1.2V$, $I_o=3.5A$



TYPICAL PERFORMANCE CHARACTERISTICS (continued)

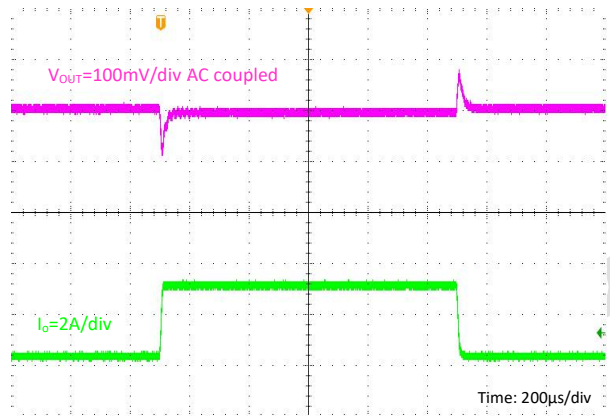
Load Transient

$V_{IN} = 5V, V_{OUT} = 1.2V, I_o = 0A \text{ to } 3.5A$



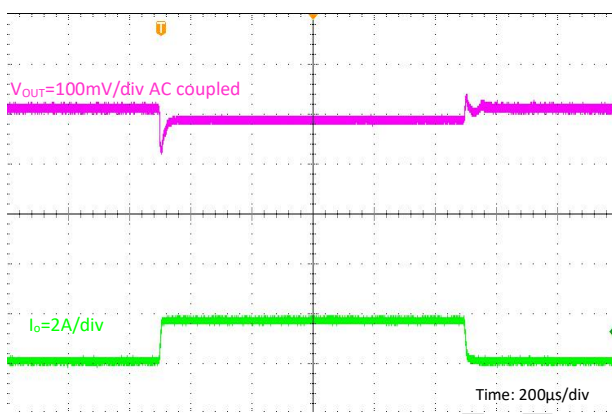
Load Transient

$V_{IN} = 5V, V_{OUT} = 1.2V, I_o = 0.35A \text{ to } 3.15A$



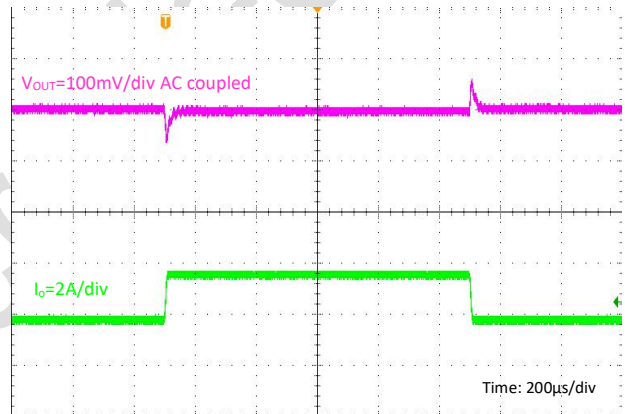
Load Transient

$V_{IN} = 5V, V_{OUT} = 1.2V, I_o = 0A \text{ to } 1.75A$



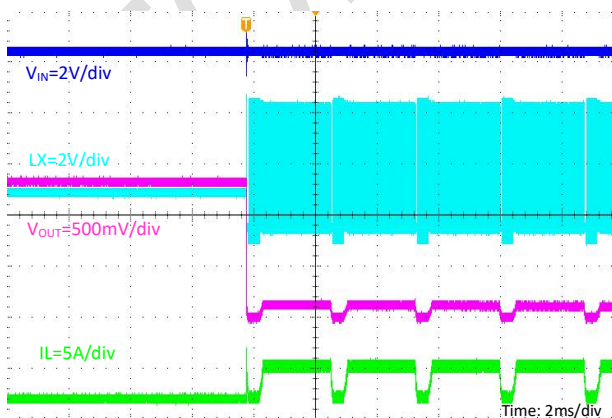
Load Transient

$V_{IN} = 5V, V_{OUT} = 1.2V, I_o = 1.75A \text{ to } 3.5A$



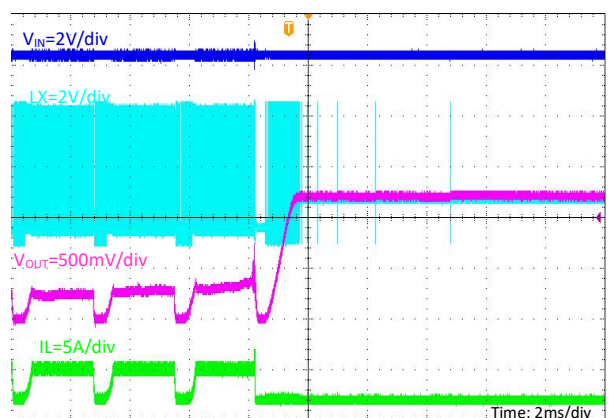
Output Short Entry

$V_{IN} = 5V, V_{OUT} = 1.2V, I_o = \text{No Load}$



Output Short Recovery

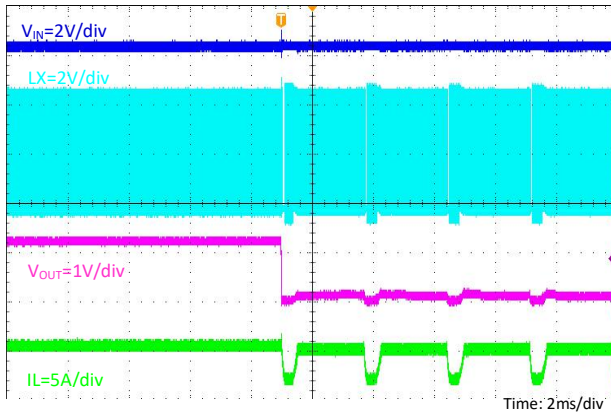
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TYPICAL PERFORMANCE CHARACTERISTICS (continued)

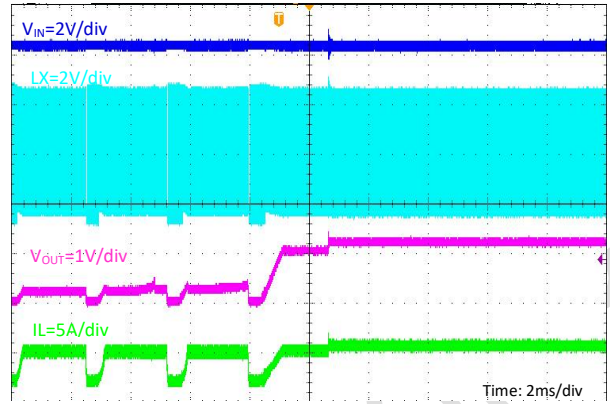
Output Short Entry

$V_{IN} = 5V, V_{OUT} = 1.2V, I_o = 3.5A$



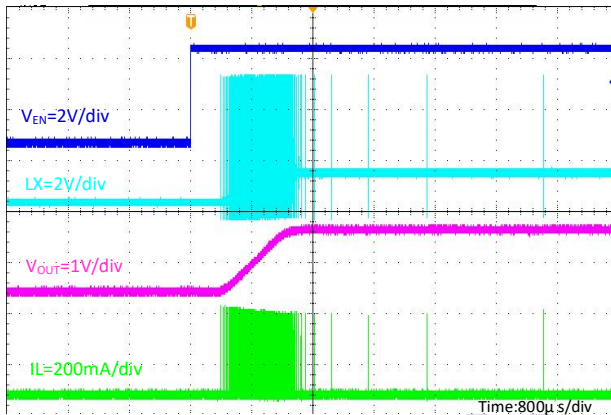
Output Short Recovery

$V_{IN} = 5V, V_{OUT} = 1.2V, I_o = 3.5A$



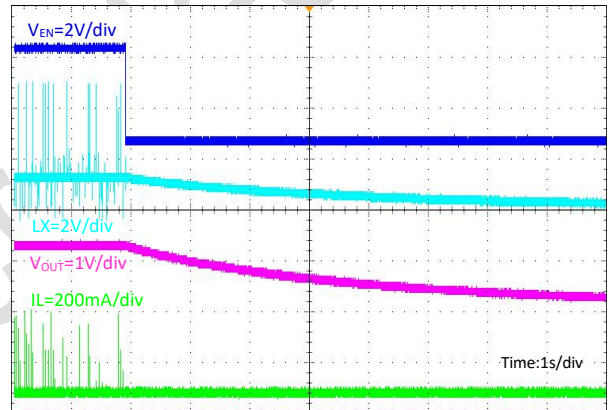
EN Enable Power On

$V_{IN} = 5V, V_{OUT} = 1.2V, \text{No Load}$



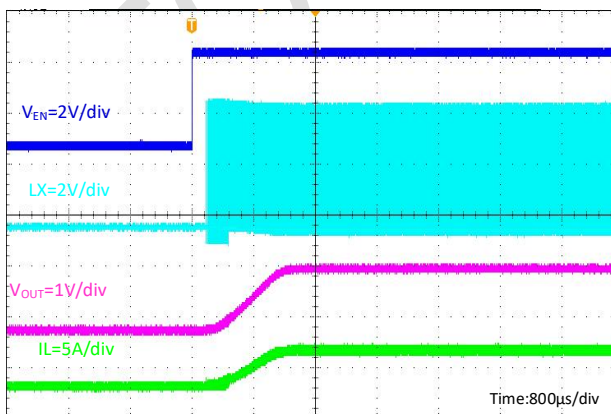
EN Disable Power down

$V_{IN} = 5V, V_{OUT} = 1.2V, \text{No Load}$



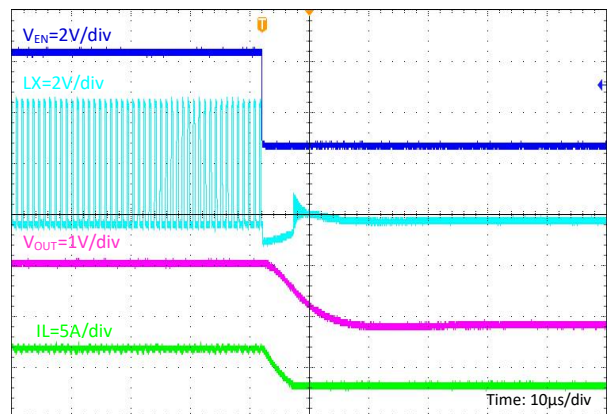
EN Enable Power On

$V_{IN} = 5V, V_{OUT} = 1.2V, I_o = 3.5A$



EN Disable Power down

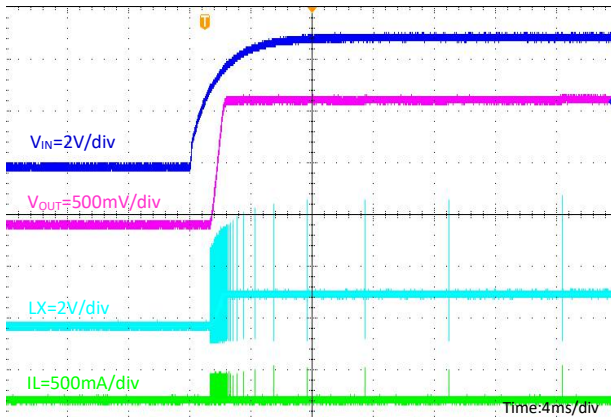
$V_{IN} = 5V, V_{OUT} = 1.2V, I_o = 3.5A$



TYPICAL PERFORMANCE CHARACTERISTICS (continued)

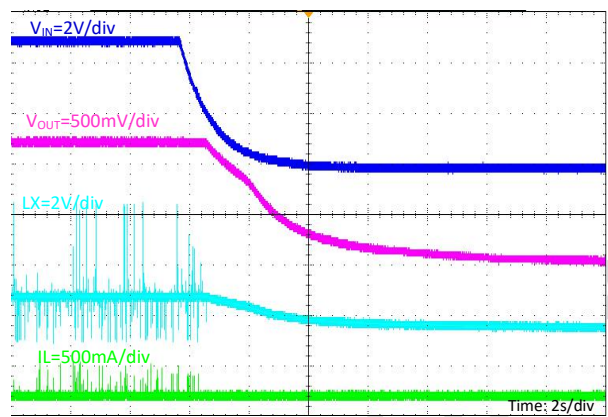
Input Power On

$V_{IN} = 5V, V_{OUT} = 1.2V, \text{No Load}$



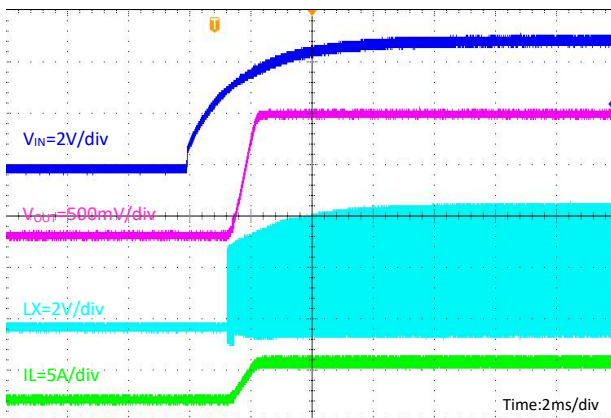
Input Power Down

$V_{IN} = 5V, V_{OUT} = 1.2V, \text{No Load}$



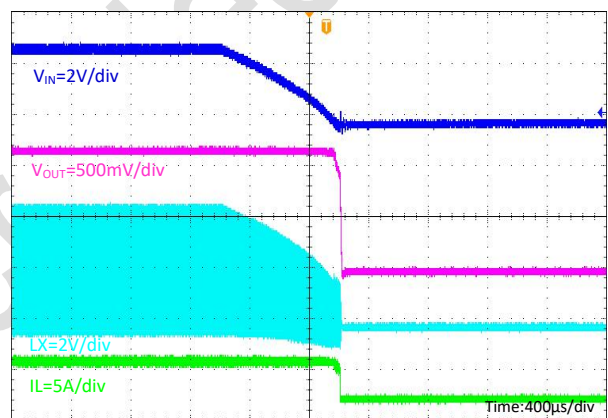
Input Power On

$V_{IN} = 5V, V_{OUT} = 1.2V, I_o = 3.5A$



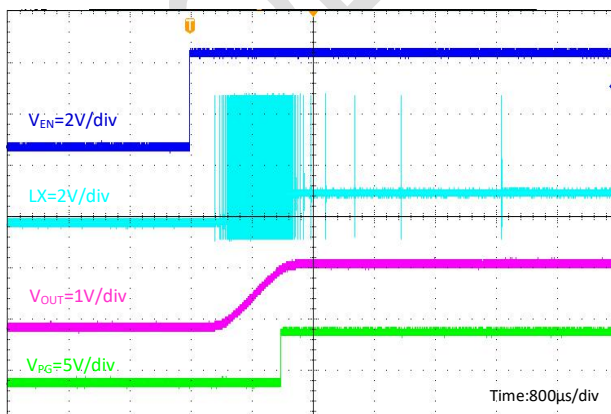
Input Power Down

$V_{IN} = 5V, V_{OUT} = 1.2V, I_o = 3.5A$



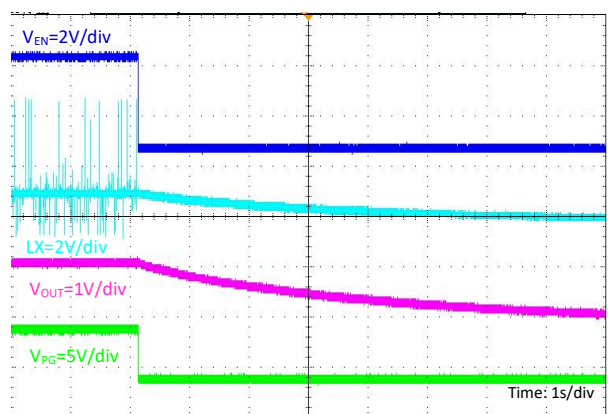
PG when EN Enable

$V_{IN} = 5V, V_{OUT} = 1.2V, \text{No Load}$



PG when EN Disable

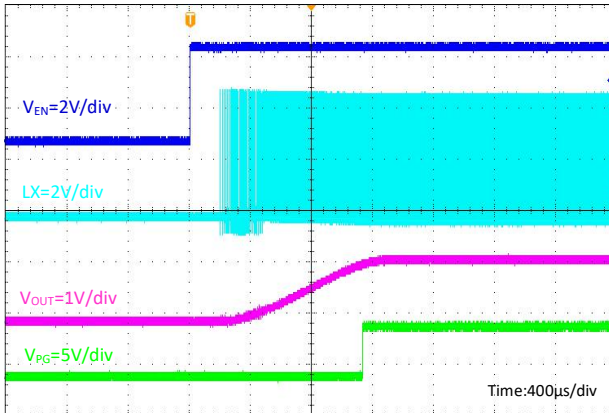
$V_{IN} = 5V, V_{OUT} = 1.2V, \text{No Load}$



TYPICAL PERFORMANCE CHARACTERISTICS (continued)

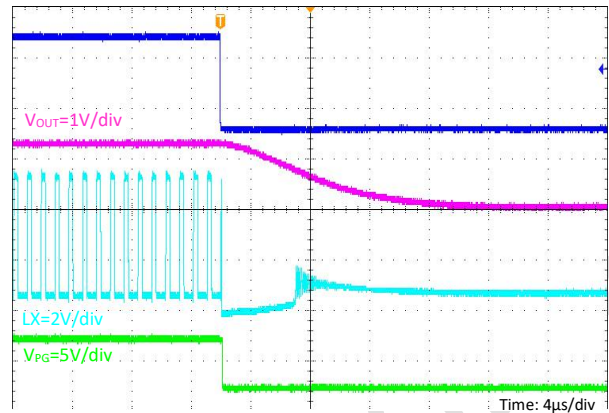
PG when EN Enable

$V_{IN} = 5V, V_{OUT} = 1.2V, I_o = 3.5A$



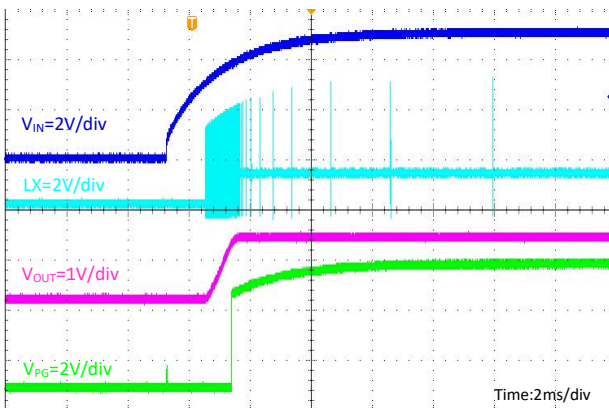
PG when EN Disable

$V_{IN} = 5V, V_{OUT} = 1.2V, I_o = 3.5A$



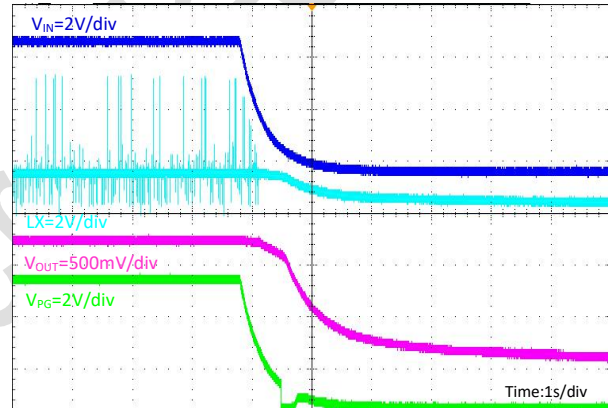
PG when Input Power On

$V_{IN} = 5V, V_{OUT} = 1.2V, \text{No Load}$



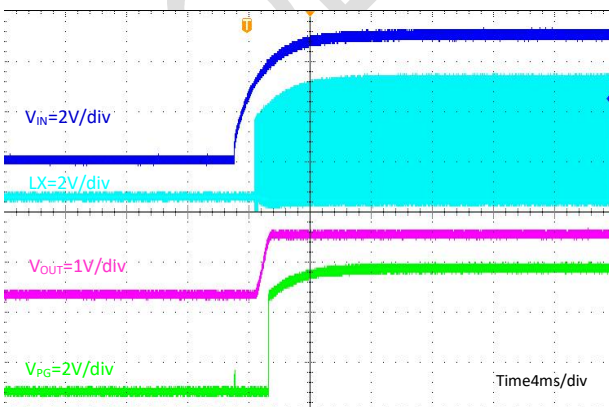
PG when Input Power Down

$V_{IN} = 5V, V_{OUT} = 1.2V, \text{No Load}$



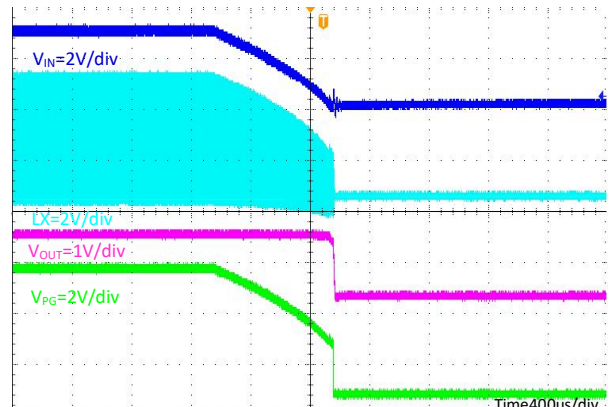
PG when EN Enable

$V_{IN} = 5V, V_{OUT} = 1.2V, I_o = 3.5A$



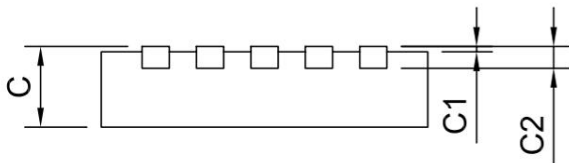
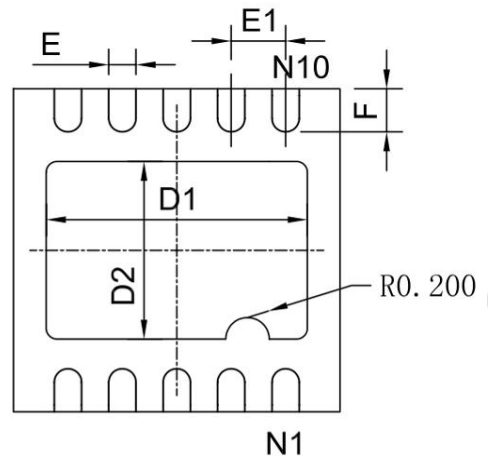
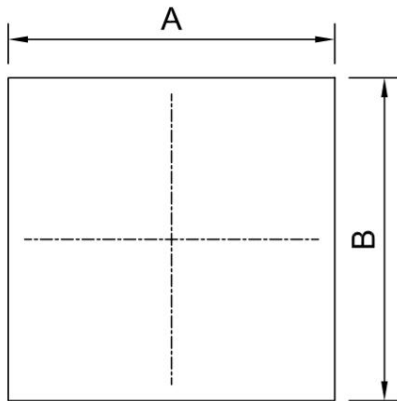
PG when EN Disable

$V_{IN} = 5V, V_{OUT} = 1.2V, I_o = 3.5A$



PACKAGE INFORMATION

DFN3*3-10

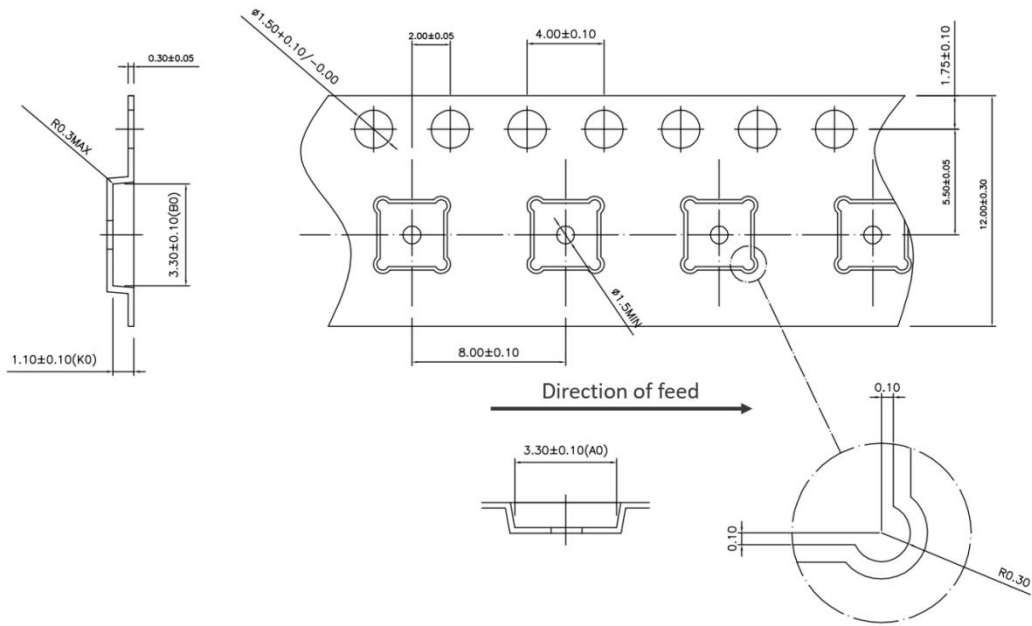


Unit: mm

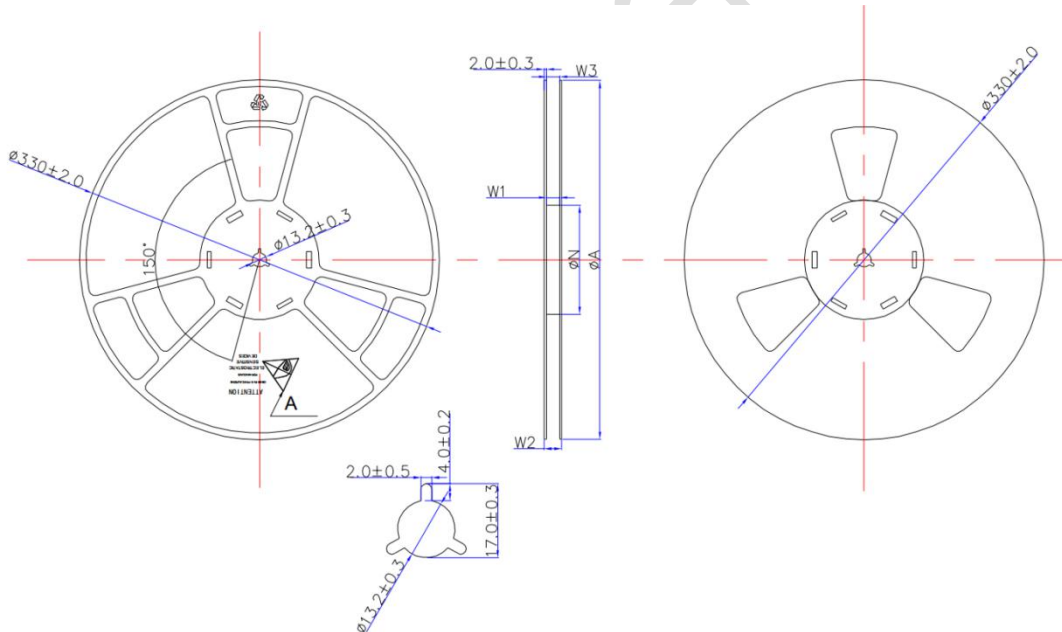
Symbol	Dimensions In Millimeters			Symbol	Dimensions In Millimeters		
	Min	Nom	Max		Min	Nom	Max
A	2.9	3.0	3.1	D1	2.40 TYP		
B	2.9	3.0	3.1	D2	1.65 TYP		
C	0.7		0.8	E	0.250 TYP		
C1	0		0.05	E1	0.500 TYP		
C2	-	0.203	-	F	0.400 TYP		

TAPE AND REEL INFORMATION

TAPE DIMENSIONS: DFN3*3-10



REEL DIMENSIONS: DFN3*3-10



Unit: mm

ϕA	ϕN	W1	W2(Max)	W3(Max)
330±2.0	100±1.0	12.4	18.4	11.9/15.4

Note:

- 1) All Dimensions are in Millimeter
- 2) Quantity of Units per Reel is 5000
- 3) MSL level is level 3.