LB05S THRU LB10S

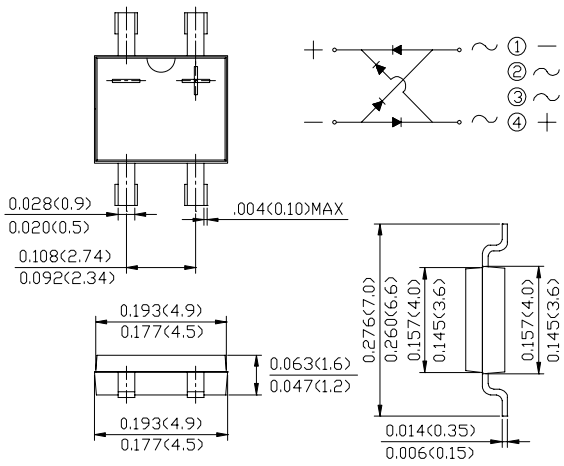
Reverse Voltage - 50 to 1000 Volts Forward Current - 6.0 Amperes

SILICON BRIDGE RECTIFIERS

Features

LBXS

- Ideal for printed circuit board
- Reliable low cost construction utilizing molded plastic technique
- Plastic material has U/L lammability classification 94V-0
- Reliable low cost constrution utilizing molded



Dimensions in inches and (millimeters)

Mechanical Data

Case : JEDEC LBXS Molded plastic body
Terminals : Solder plated, solderable per MIL-STD-750,Method 2026
Polarity : Polarity symbol marking on body
Mounting Position: Any
Weight : 0.00528ounce，0.134grams

Maximum Ratings And Electrical Characteristics

Ratings at 25°C ambient temperature unless otherwisespecified.

Single phase half-wave 60Hz,resistive or inductive load,for capacitive load current derate by 20%.

Parameter / Marking Code	SYMBOLS	MDD LB05S	MDD LB1S	MDD LB2S	MDD LB4S	MDD LB6S	MDD LB8S	MDD LB10S	UNITS
Maximum repetitive peak reverse voltage	V_{RRM}	50	100	200	400	600	800	1000	V
Maximum RMS voltage	V_{RMS}	35	70	140	280	420	560	700	V
Maximum DC blocking voltage	V_{DC}	50	100	200	400	600	800	1000	V
Maximum average forward(on galss-epoxy pcb NOTE 2)(on alumlnum substrate NOTE 3)	$I_{(AV)}$	0.5 0.8							A
Peak forward surge current 8.3ms single half sine-wave superimposed on rated load (JEDEC Method)	I_{FSM}	30.0							A
Maximum forward voltage at 0.4A DC	V_F	1.0							V
Maximum DC reverse current T_A=25°C	I_R	0.5							μA
at rated DC blocking voltage T_A=125°C		0.5							mA
Typical Junction Capacitance (Note 1)	C_J	13							pF
Typical Thermal Resistance (Note 2)	$R_{\theta JA}$	60							°C/W
Operating junction temperature range	T_J	-55 to +150							° C
storage temperature range	T_{STG}	-55 to +150							° C

NOTES: 1- Measured at 1 MH_Z and applied reverse voltage of 4.0 VDC.

2- On glass epoxy P.C.B. mounted on 0.05 x 0.05" (1.3 x 1.3mm) pads

Ratings And Characteristic Curves

Fig.1 Derating Curve For Output Rectified Current

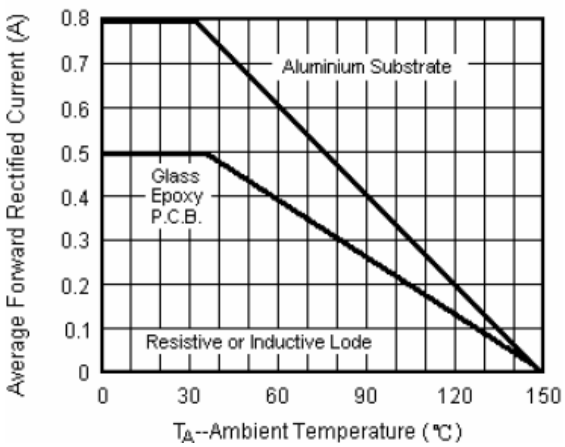


Fig.2 Maximum Non-Repetitive Peak Forward Surge Current Per Leg

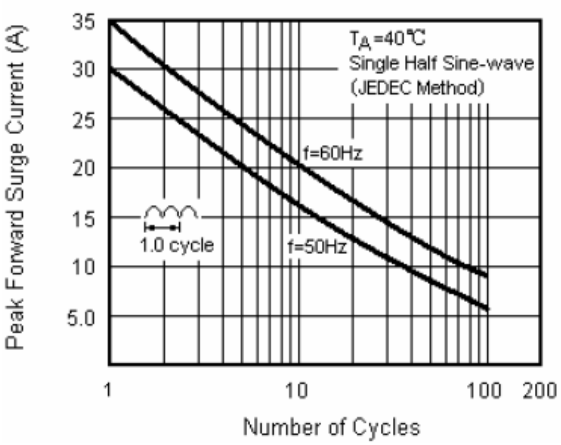


Fig.3 Typical Forward Voltage Characteristics Per Leg

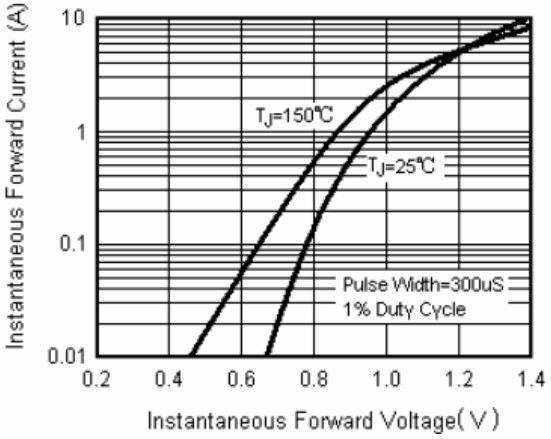


Fig.4 Typical Reverse Leakage Characteristics Per Leg

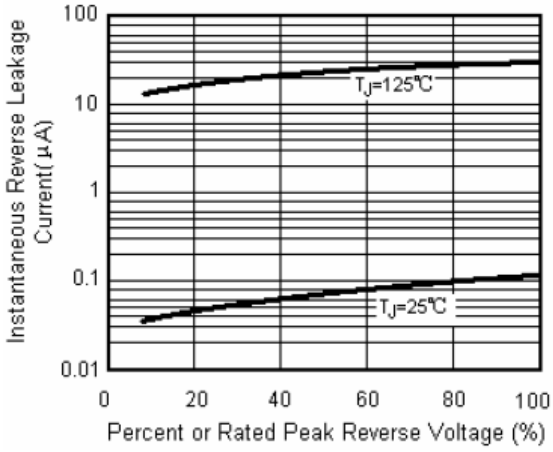
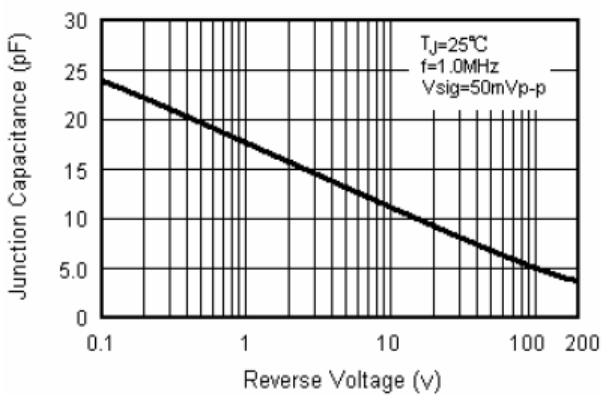


Fig.5 Typical Junction Capacitance Per Leg



The curve above is for reference only.