

#### Discription

The HPESD1IVN24-AX protects sensitive semiconductor components from damage or upset due to electrostatic discharge (ESD) and other voltage induced transient events. Excellent clamping capability, low leakage, low capacitance, and fast response time provide best in class protection on designs that are exposed to ESD.

It gives designer the flexibility to protect one bi-directional line in applications where arrays are not practical.

# HXY



Circuit Diagram

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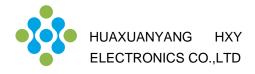
- ★ Small Body Outline Dimensions
- ★ Low Body Height
- ★ Peak Power up to 430 Watts @ 8 x 20 \_s Pulse
- ★ Low Leakage current
- ★ Response Time is Typically < 1 ns</p>
- ★ IEC61000-4-2 Level 4 ESD Protection
- ★ IEC61000-4-4 Level 4 EFT Protection

## **Orderingin formation**



## Absolute Ratings(Tamb = 25°C)

| Symbol           | Parameter  | Value                              | Units       |    |
|------------------|--|------------------------------------|-------------|----|
| P <sub>PP</sub>  | Peak Pulse Power ( $t_p = 8/20 \ \mu \ s$ )      | 430                                | W           |    |
| TL               | Maximum lead temperature for soldering during 10 | 260                                | °C          |    |
| T <sub>stg</sub> | Storage Temperature Range                        | -55 to +155                        | °C          |    |
| $T_{op}$         | Operating Temperature Range                      |                                    | -40 to +125 | °C |
| Tj               | Maximum junction temperature                     | 150                                | °C          |    |
|                  | IEC61000-4-2 (ESD)                               | air discharge<br>contact discharge | 土15<br>土8   | KV |
|                  | IEC61000-4-4 (EFT)                               |                                    | 40          | А  |
|                  | ESD Voltage Per                                  | Human Body Model                   | 16          | KV |



| V <sub>RWM</sub><br>(V) | I <sub>R</sub> (uA)<br>@ V <sub>RWM</sub> | V <sub>BR</sub> (V)@ I <sub>T</sub><br>(Note 1) | Ι <sub>Τ</sub> | V <sub>C</sub> (V)<br>@ I <sub>PP</sub> =1 A* | V <sub>c</sub> (V)<br>@ Max I <sub>PP</sub> * | І <sub>РР</sub><br>(А)* | Р <sub>РК</sub><br>(W)* | C<br>(pF) |
|-------------------------|---|---|----------------|---|---|-------------------------|-------------------------|-----------|
| Мах                     | Мах                                       | Min   | mA             | Тур   | Max   | Max                     | Max                     | Мах       |
| 24                      | 1.0                                       | 26  | 1              | 26  | 45  | 9.5                     | 430                     | 30        |

Electrical Characteristics Ratings at 25°C ambient temperature unless otherwise specified.

\*Surge current waveform per Figure 1.

1.  $V_{BR}$  is measured with a pluse test current  $I_T$  at an ambient temperature of  $25^\circ\!\mathbb{C}$  .

# **Typical Characteristics**

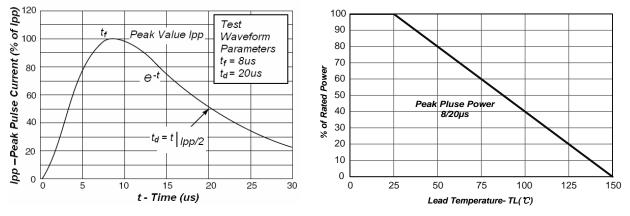


Fig1. Pulse Waveform

Fig2.Power Derating



## OUTLINE AND DIMENSIONS

HE

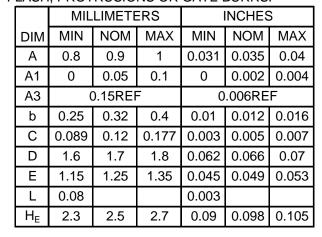
Notes:

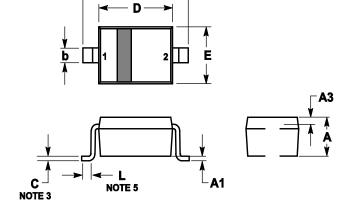
1. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.

2. CONTROLLING DIMENSION: MILLIMETERS.

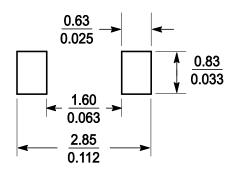
3. MAXIMUM LEAD THICKNESS INCLUDES LEAD FINISH. MINIMUM LEAD THICKNESS IS THE MINIMUM THICKNESS OF BASE MATERIAL.

4. DIMENSIONS D AND E DO NOT INCLUDE MOLD FLASH, PROTRUSIONS OR GATE BURRS.





## SOLDERING FOOTPRINT





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