

MSB100N023

N-Channel 100-V (D-S) MOSFET

Description

The device is using trench DMOS technology. This advanced technology has been especially tailored to minimize $R_{DS(ON)}$, provide superior switching performance, and withstand high energy pulse in the avalanche and commutation mode. These devices are well suited for high efficiency fast switching applications.

Features

- $R_{DS(ON)} = 2.5m\Omega @ V_{GS} = 10V$
- Fast switching
- Improve dv/dt Capability
- 100% EAS Guaranteed
- Green Device Available

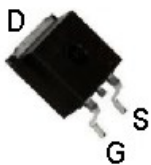
Typical Applications

- Networking
- Load Switch
- Synchronous Rectifier
- BMS Applications

Package type : TO-263

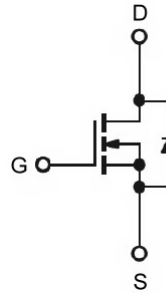
Packing & Order Information

800/Reel

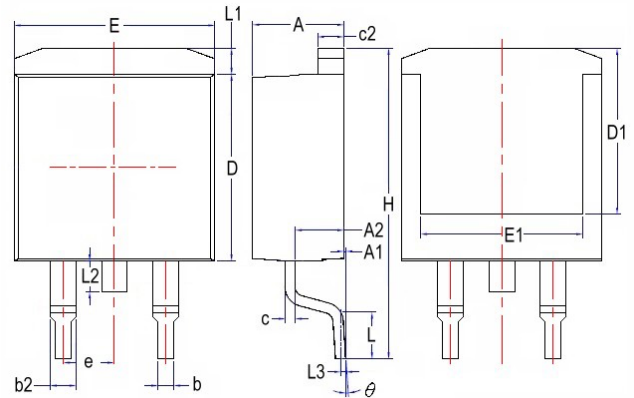


RoHS Compliant

Graphic Symbol

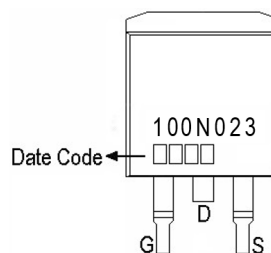


Package Dimension



REF.	Millimeter		REF.	Millimeter	
	Min.	Max.		Min.	Max.
A	4.37	4.77	E	9.80	10.36
A1	0.00	0.25	E1	7.06	-
A2	2.20	2.80	e	2.54 BSC	
b	0.70	0.96	H	14.70	15.70
b2	1.17	1.47	L	2.00	2.60
c	0.30	0.60	L1	1.07	1.47
c2	1.22	1.42	L2	1.40	1.75
D	8.50	9.30	L3	0.25 BSC	
D1	6.60	-	θ	0°	9°

Marking



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MAXIMUM RATINGS AND ELECTRICAL CHARACTERISTICS

Absolute Maximum Ratings			
Symbol	Parameter	Value	Units
V_{DS}	Drain-Source Voltage	100	V
V_{GS}	Gate-Source Voltage	± 20	V
I_D	Continuous Drain Current ¹ ($T_C = 25^\circ\text{C}$)	250	A
	Continuous Drain Current ¹ ($T_C = 100^\circ\text{C}$)	158	A
I_{DM}	Pulsed Drain Current ^{1,2}	1000	A
I_{AS}	Single Pulse Avalanche Current, $L = 0.1\text{mH}^3$	137	A
E_{AS}	Single Pulse Avalanche Energy, $L = 0.1\text{mH}^3$	938	mJ
P_D	Power Dissipation ⁴ ($T_C = 25^\circ\text{C}$)	278	W
	Power Dissipation ⁴ ($T_A = 25^\circ\text{C}$)	2	W
T_J/T_{STG}	Operating Junction and Storage Temperature	-50 to +150	$^\circ\text{C}$

Thermal Resistance Ratings			
Symbol	Parameter	Maximum	Units
$R_{\theta JA}$	Maximum Junction-to-Ambient ¹	62.5	$^\circ\text{C/W}$
$R_{\theta JC}$	Maximum Junction-to-Case ¹	0.45	$^\circ\text{C/W}$

Electrical Characteristics ($T_J = 25^\circ\text{C}$ unless otherwise specified)						
Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Units
$V_{GS(th)}$	Gate Threshold Voltage	$V_{DS} = V_{GS}$, $I_D = 250\mu\text{A}$	2	2.5	4	V
BV_{DSS}	Drain-Source Breakdown Voltage	$V_{GS} = 0\text{V}$, $I_D = 250\mu\text{A}$	100	-	-	V
g_{fs}	Forward Transconductance	$V_{DS} = 10\text{V}$, $I_D = 3\text{A}$	-	20	-	S
I_{GSS}	Gate-Source Leakage Current	$V_{DS} = 0\text{V}$, $V_{GS} = 20\text{V}$	-	-	100	nA
I_{DSS}	Drain-Source Leakage Current	$V_{DS} = 100\text{V}$, $V_{GS} = 0\text{V}$, $T_J = 25^\circ\text{C}$	-	-	1	μA
		$V_{DS} = 80\text{V}$, $V_{GS} = 0\text{V}$, $T_J = 85^\circ\text{C}$	-	-	10	μA
$R_{DS(on)}$	Static Drain-Source On-Resistance ²	$V_{GS} = 10\text{V}$, $I_D = 40\text{A}$	-	1.9	2.5	$\text{m}\Omega$
E_{AS}	Single Pulse Avalanche Energy ⁵	$V_{DD} = 50\text{V}$, $L = 0.1\text{mH}$, $I_{AS} = 60\text{A}$	180	-	-	mJ
V_{SD}	Diode Forward Voltage ²	$I_S = 1\text{A}$, $V_{GS} = 0\text{V}$, $T_J = 25^\circ\text{C}$	-	-	1	V
I_S	Continuous Source Current ^{1,6}	$V_G = V_D = 0\text{V}$, Force Current	-	-	250	A
I_{SM}	Pulsed Source Current ^{2,6}		-	-	500	

Notes

- The data tested by surface mounted on a 1 inch² FR-4 board with 2OZ copper.
- The data tested by pulsed, pulse width $\leq 300\mu\text{s}$, duty cycle $\leq 2\%$.
- The EAS data shows maximum rating. The test condition is $V_{DD} = 50\text{V}$, $V_{GS} = 10\text{V}$, $L = 0.1\text{mH}$, $I_{AS} = 60\text{A}$.
- The power dissipation is limited by 150°C junction temperature.
- The Min. value is 100% EAS tested guarantee.
- The data is theoretically the same as I_D and I_{DM} , in real applications, should be limited by total power dissipation.

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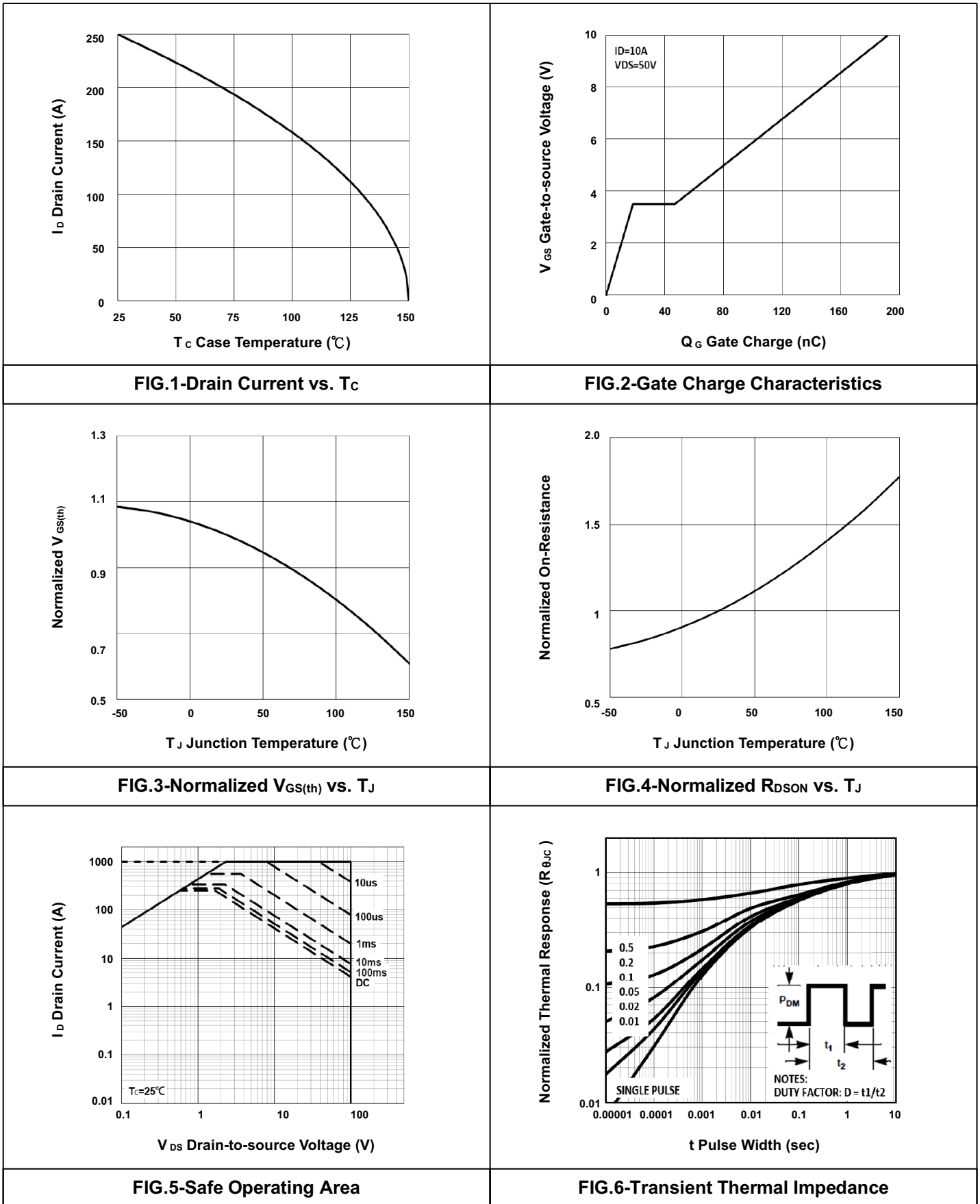
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Dynamic						
Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Units
Q_g	Total Gate Charge ²	$V_{DS} = 50V$	--	192	--	nC
Q_{gs}	Gate-Source Charge	$I_D = 100A$	--	18.5	--	
Q_{gd}	Gate-Drain Charge	$V_{GS} = 10V$	--	28.3	--	
$t_{d(on)}$	Turn-On Delay Time ²	$V_{DS} = 50V$	--	20.6	--	ns
t_r	Rise Time	$I_D = 100A$	--	19.8	--	
$t_{d(off)}$	Turn-Off Delay Time	$V_{GS} = 10V$	--	66	--	
t_f	Fall Time	$R_G = 3.3\Omega$	--	117	--	
C_{ISS}	Input Capacitance	$V_{DS} = 50V$	--	10100	--	pF
C_{OSS}	Output Capacitance	$V_{GS} = 0V$	--	2020	--	
C_{RSS}	Reverse Transfer Capacitance	$f = 1.0MHz$	--	53	--	
R_g	Gate Resistance	$V_{GS} = V_{DS} = 0V, f = 1.0MHz$	--	1.1	--	Ω

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- Typical Electrical Characteristics



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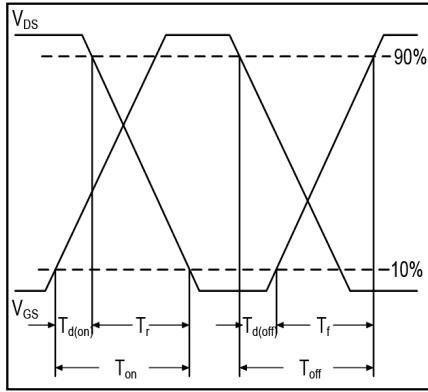


FIG.7-Switching Time Waveform

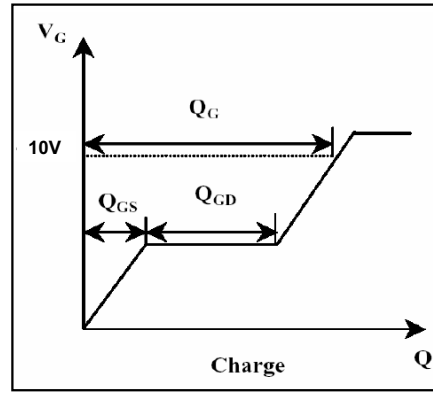


FIG.8-Gate Charge Waveform

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