

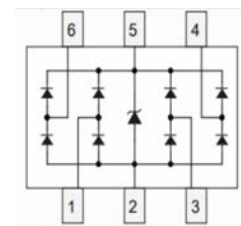


### Discription

The HDT1446-04S-7 is a 5-channel ultra low capacitance rail clamp ESD protection diodes array. Each channel consists of a pair of ESD diodes that steer positive or negative ESD current to either the positive or negative rail. A zener diode is integrated in to the array between the positive and negative supply rails. In the typical applications, the negative rail pin (assigned as GND) is connected with system ground. The Positive ESD current is steered to the ground through an ESD diode and Zener diode and the positive ESD voltage is clamped to the zener voltage.



SOT-363



Circuit Diagram

### Features

- ★ 5 channels of ESD protection
- ★ Provides ESD protection to IEC61000-4-2 level 4
  - ±27kV air discharge
  - ±15kV contact discharge
- ★ Low clamping voltage
- ★ Low operating voltage
- ★ Improved zener structure
- ★ Optimized package for easy high speed data lines PCB layout
- ★ RoHS compliant

### Ordering information

Product ID	Pack	Qty(PCS)
HDT1446-04S-7	SOT-363	3000

### Absolute Ratings(Tamb = 25°C)

Characteristics	Symbol	Ratings	Unit
Peak Pulse Power(8/20μs)	P <sub>PP</sub>	55	W
Peak Pulse Current(8/20μs)	I <sub>PP</sub>	4	A
ESD per IEC 61000-4-2(Air)	V <sub>ESD1</sub>	±20kV	kV
ESD per IEC 61000-4-2(Contact)	V <sub>ESD2</sub>	±15kV	kV
Operating Temperature Range	T <sub>opr</sub>	-55 ~ +125	°C
Storage Temperature Range	T <sub>stg</sub>	-55 ~ +150	°C

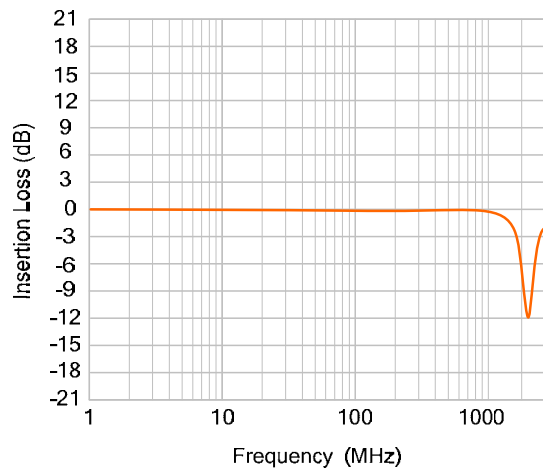


### Electrical Characteristics (T<sub>amb</sub>=25°C)

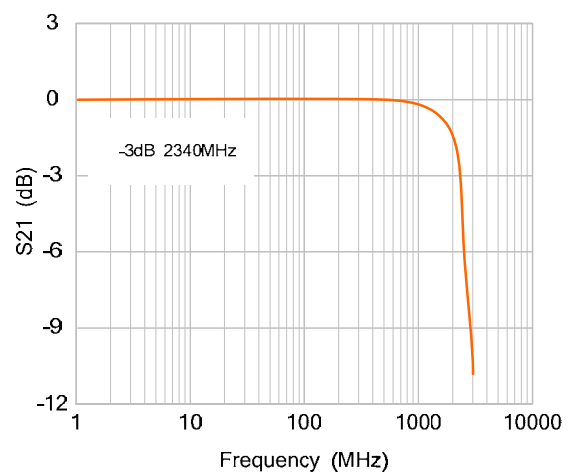
Characteristics	Symbol	Test Conditions	Min.	Typ.	Max.	Unit
Reverse Working Voltage	V <sub>RWM</sub>	Any I/O pin to GND			5	V
Reverse Breakdown Voltage	V <sub>BR</sub>	I <sub>t</sub> =1mA; Any I/O pin to GND	6			V
Reverse Leakage Current	I <sub>R</sub>	V <sub>RWM</sub> =5V, T=25°C; Any I/O pin to GND			1	μA
Positive Clamping Voltage	V <sub>C1</sub>	I <sub>PP</sub> =1A, t <sub>P</sub> =8/20 μs; Positive pulse; Any I/O pin to GND			13.0	V
Negative Clamping Voltage	V <sub>C2</sub>	I <sub>PP</sub> =1A, t <sub>P</sub> =8/20μs; Negative pulse; Any I/O pin to GND		1.8		V
Junction Capacitance Between Channel	C <sub>J1</sub>	V <sub>R</sub> =0V, f=1MHz; Between I/O pins		0.3	0.4	pF
Junction Capacitance Between I/O And GND	C <sub>J2</sub>	V <sub>R</sub> =0V, f=1MHz; Any I/O pin to GND		0.6	0.8	pF

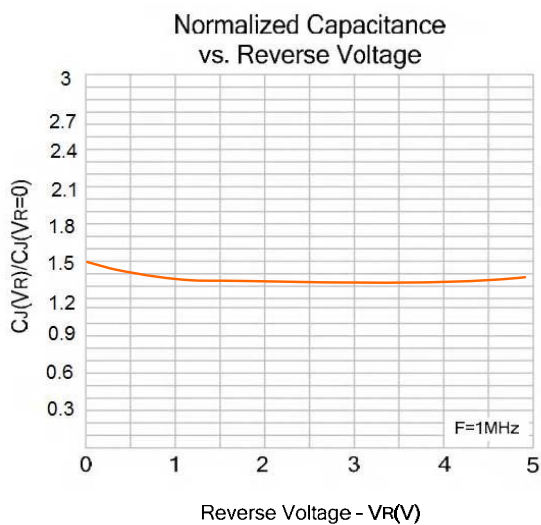
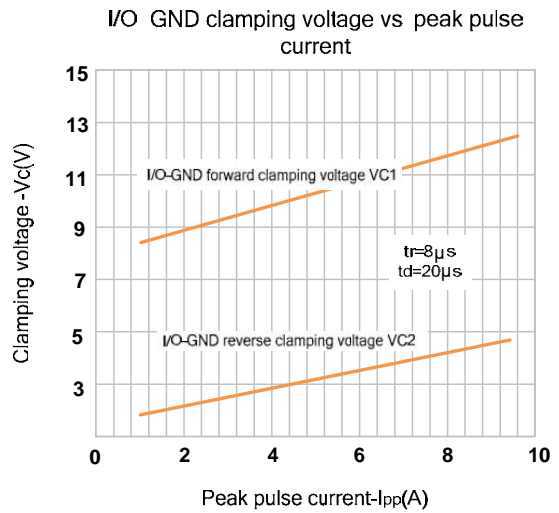
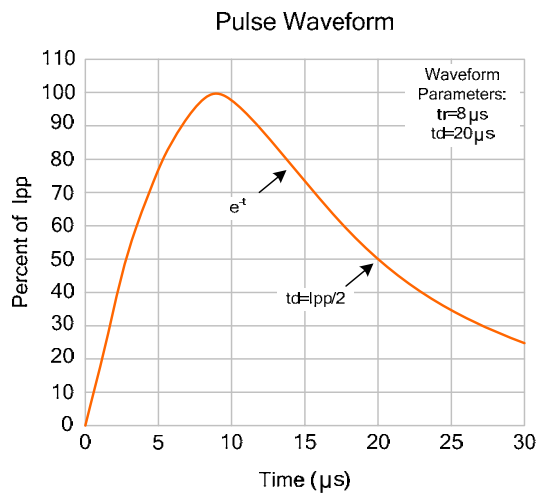
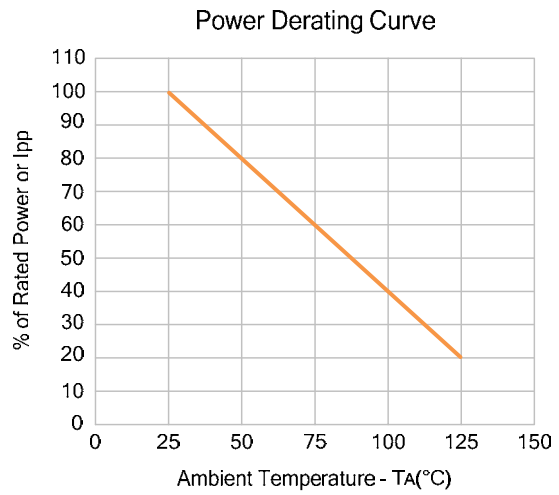
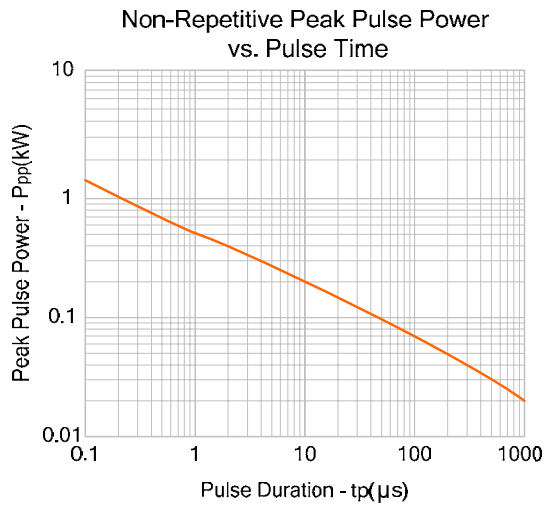
### Typical Characteristics

I/O - GND Insertion Loss vs. Frequency



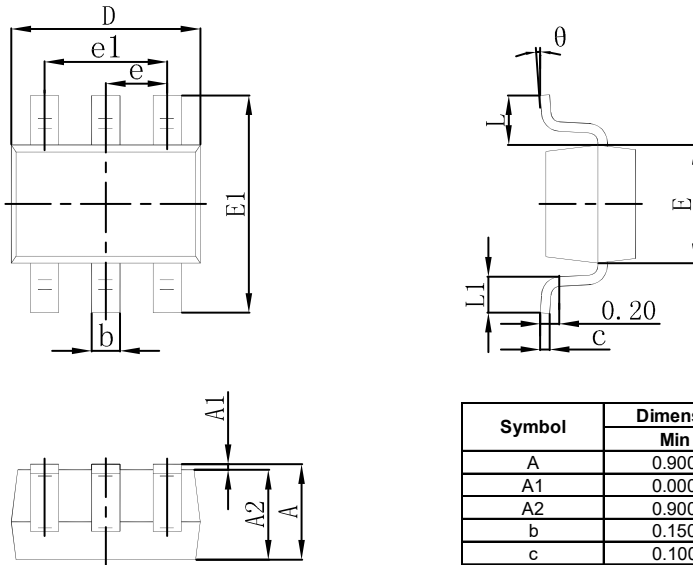
Insertion Loss vs. Frequency





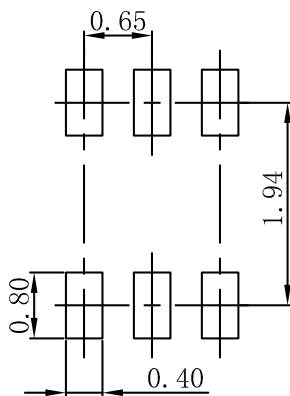


### SOT-363 Package Outline Dimensions



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min	Max	Min	Max
A	0.900	1.100	0.035	0.043
A1	0.000	0.100	0.000	0.004
A2	0.900	1.000	0.035	0.039
b	0.150	0.350	0.006	0.014
c	0.100	0.150	0.004	0.006
D	2.000	2.200	0.079	0.087
E	1.150	1.350	0.045	0.053
E1	2.150	2.400	0.085	0.094
e	0.650 TYP		0.026 TYP	
e1	1.200	1.400	0.047	0.055
L	0.525 REF		0.021 REF	
L1	0.260	0.460	0.010	0.018
theta	0°	8°	0°	8°

### SOT-363 Suggested Pad Layout



**Note:**

1. Controlling dimension: in millimeters.
2. General tolerance:  $\pm 0.05$ mm.
3. The pad layout is for reference purposes only.



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