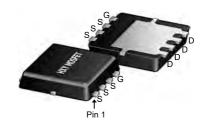


Description

The SI7848BDP-T1-GE3 uses advanced trench technology to provide excellent R_{DS(ON)}, low gate charge and operation with gate voltages as low as 4.5V. This device is suitable for use as a Battery protection or in other Switching application.



DFN5X6-8L

General Features

 $V_{DS} = 40V I_{D} = 70A$

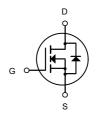
 $R_{DS(ON)}$ < 8.5m Ω @ V_{GS} =10V

Application

Battery protection

Load switch

Uninterruptible power supply



N-Channel MOSFET

Package Marking and Ordering Information

Product ID	Pack	Brand	Qty(PCS)
SI7848BDP-T1-GE3	DFN5X6-8L	HXY MOSFET	5000

Absolute Maximum Ratings (T_C=25°C unless otherwise noted)

Symbol	Parameter	Rating	Units		
VDS	Drain-Source Voltage	urce Voltage 40			
Vgs	Gate-Source Voltage	V			
I _D @T _C =25°C	Continuous Drain Current, V _{GS} @ 10V ¹	A			
I _D @T _C =100°C	Continuous Drain Current, V _{GS} @ 10V ¹	44	A		
IDM	Pulsed Drain Current ²	280	A		
EAS	Single Pulse Avalanche Energy³	76	mJ		
P _D @T _C =25°C	Total Power Dissipation ⁴	72.3	W		
Тѕтс	Storage Temperature Range -55 to 150		°C		
TJ	Operating Junction Temperature Range -55 to 15		°C		
ReJA	Thermal Resistance Junction-ambient (Steady State) ¹ 62		°C/W		
Rejc	Thermal Resistance Junction-Case ¹ 1.73		°C/W		



Electrical Characteristics (Ta=25°C, unless otherwise noted)

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit	
BVDSS	Drain-Source Breakdown Voltage	V _{GS} =0V , I _D =250uA	40			V	
		V _{GS} =10V , I _D =10A		6.5	8.5		
RDS(ON)	Static Drain-Source On-Resistance ²	V _{GS} =4.5V , I _D =5A		10	15	mΩ	
VGS(th)	Gate Threshold Voltage	e Threshold Voltage V _{GS} =V _{DS} , I _D =250uA		1.7	3	V	
		V _{DS} =32V , V _{GS} =0V , T _J =25°C			1		
Ipss	Drain-Source Leakage Current	V _{DS} =32V , V _{GS} =0V , T _J =55°C			5	uA	
Igss	Gate-Source Leakage Current	V _{GS} =±20V , V _{DS} =0V			±100	nA	
gfs	Forward Transconductance	V _{DS} =10V , I _D =5A		13		S	
Qg	Total Gate Charge (4.5V)			20			
Qgs	Gate-Source Charge	V _{DS} =20V , V _{GS} =10V , I _D =10A		2.8		nC	
Qgd	Gate-Drain Charge			5.1			
Td(on)	Turn-On Delay Time			13.2			
Tr	Rise Time	V _{DD} =15V , V _{GS} =10V		2.2			
Td(off)	Turn-Off Delay Time	R _G =3.3 Ω		72		ns	
T _f	Fall Time	I _D =1A		4.5			
Ciss	Input Capacitance			1278			
Coss	Output Capacitance	── V _{DS} =25V , V _{GS} =0V , f=1MHz		135		pF	
Crss	Reverse Transfer Capacitance			87			
ls	Continuous Source Current ^{1,5}	V _G =V _D =0V , Force Current			70	Α	
VsD	Diode Forward Voltage ²	V _{GS} =0V , I _S =1A , T _J =25°C			1	V	

Note:

- 1. The data tested by surface mounted on a 1 inch2 FR-4 board with 2OZ copper.
- 2.The data tested by pulsed , pulse width \leq 300us , duty cycle \leq 2%.
- 3. The EAS data shows Max. rating . The test condition is $V_{DD}=25V$, $V_{GS}=10V$, L=0.1mH, $I_{AS}=47A$.
- 4. The power dissipation is limited by 150°C junction temperature.
- 5. The data is theoretically the same as l_D and l_{DM} , in real applications, should be limited by total power dissipation.

Typical Characteristics

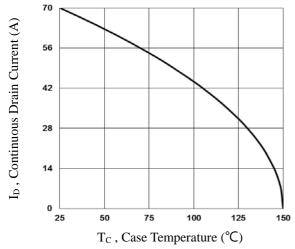


Fig.1 Continuous Drain Current vs. Tc

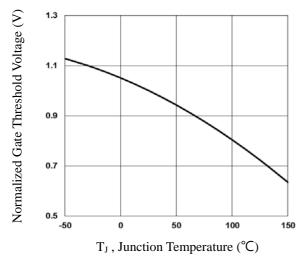


Fig.3 Normalized V_{th} vs. T_J

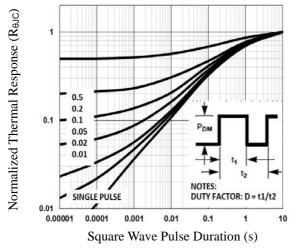


Fig.5 Normalized Transient Impedance

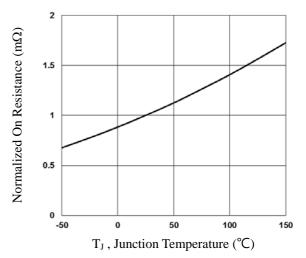


Fig. 2 Normalized RDSON vs. TJ

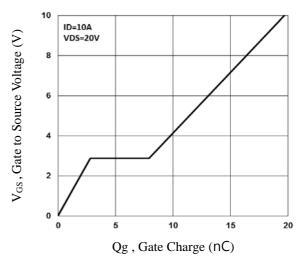


Fig. 4 Gate Charge Waveform

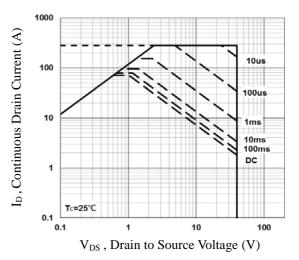


Fig.6 Maximum Safe Operation Area

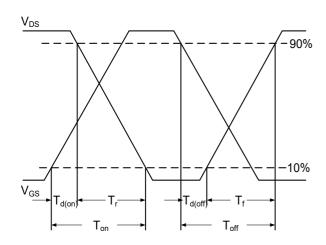


Fig.7 Switching Time Waveform

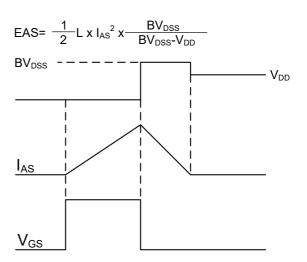
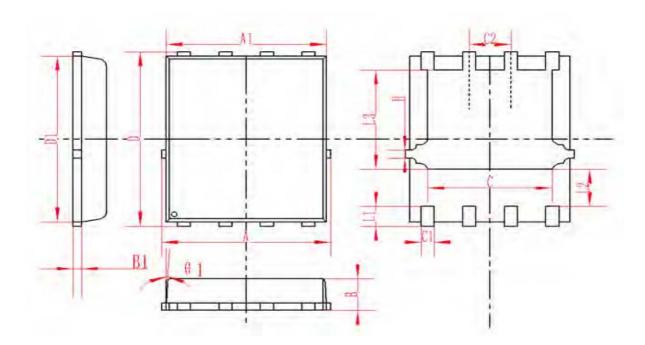


Fig.8 EAS Waveform

DFN5X6-8L Package Information



SYMBOL	MM		INCH			
	MIN	NOM	MAX	MIN	NOM	MAX
Α	5.3	5.5	5.7	0.208	0.216	0.224
A1	5.1	5.2	5.3	0.2	0.204	0.209
D	5.98	6	6.02	0.235	0.236	0.237
D1	5.85	6.05	6.25	0.23	0.238	0.246
В	0.85	0.95	1.05	0.033	0.037	0.041
B1	0.254REF		0.010REF			
С	3.95	4	4.05	0.156	0.157	0.159
C1	0.35	0.4	0.45	0.014	0.016	0.018
C2		1.27TYP			0.5TYP	
θ1	8°	10°	12°	8°	10°	12°
L1	0.63	0.64	0.65	0.025	0.025	0.026
L2	1.2	1.3	1.4	0.047	0.051	0.055
L3	3.415	3.42	3.425	0.134	0.135	0.135
Н	0.24	0.25	0.26	0.009	0.010	0.010



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