

#### Description

The FDMS7682 uses advanced trench technology

to provide excellent  $R_{\text{DS}(\text{ON})},$  low gate charge and

operation with gate voltages as low as 4.5V. This

device is suitable for use as a

Battery protection or in other Switching application.

#### **General Features**

V<sub>DS</sub> = 30V I<sub>D</sub> =80A

 $R_{DS(ON)} < 6m\Omega V_{GS}=10V$ 

#### Application

Battery protection

Load switch

Uninterruptible power supply

### Package Marking and Ordering Information

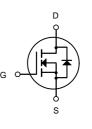
Product ID	Pack	Brand	Qty(PCS)
FDMS7682	DFN5X6-8L	HXY MOSFET	5000

# Absolute Maximum Ratings (Tc=25°C unless otherwise noted)

Symbol	Parameter	Rating	Units
Vds	Drain-Source Voltage	V	
Vgs	Gate-Source Voltage	±20	V
I⊳@Tc=25°C	Continuous Drain Current, V <sub>GS</sub> @ 10V	80	A
I⊳@Tc=70°C	Continuous Drain Current, V <sub>GS</sub> @ 10V	45	А
Ідм	Pulsed Drain Current <sup>2</sup>	280	А
EAS	Single Pulse Avalanche Energy <sup>3</sup>	56	mJ
P₀@Tc=25°C	Total Power Dissipation <sup>4</sup>	37	W
Тѕтс	Storage Temperature Range	-55 to 150	°C
TJ	Operating Junction Temperature Range -55 to 15		°C
Reja	Thermal Resistance Junction-Ambient <sup>1</sup>	30	°C/W







N-Channel MOSFET



Symbol	Parameter	Condition	Min.	Тур.	Max.	Unit
Static Ele	ectrical Characteristics @ Tj=25°C (unles	ss otherwise stated	)	1	1	
V <sub>(BR)DSS</sub>	Drain-Source Breakdown Voltage	Vgs=0V ld=250µA	30			V
	Zero Gate Voltage Drain Current	VDS=30V,VGS=0V			0.1	μA
DSS	Zero Gate Voltage Drain Current(Tj =125℃)	VDS=30V,VGS=0V			100	μA
GSS	Gate-Body Leakage Current	VGS=±20V,VDS=0V			±100	nA
V <sub>GS(TH)</sub>	Gate Threshold Voltage	Vos=Vgs,Io=250µA	1.0	1.7	2.5	V
RDS(ON)	Drain-Source On-State Resistance③	Vgs=10V, Id=20A		4.7		mΩ
RDS(ON)	Drain-Source On-State Resistance③	Vgs=4.5V, Id=16A		5.4	8	mΩ
Dynamic	Electrical Characteristics @ T <sub>j</sub> = 25°C (	unless otherwise st	ated)			
Ciss	Input Capacitance			1930		pF
C <sub>oss</sub>	Output Capacitance	Vbs=15V,Vgs=0V, f=1MHz		310		pF
C <sub>rss</sub>	Reverse Transfer Capacitance	]		260		pF
Rg	Gate Resistance	f=1MHz		0.85		
Qg	Total Gate Charge			38		nC
$Q_{gs}$	Gate-Source Charge	VDS=15V,ID=20A, VGS=10V		5.1		nC
$Q_{gd}$	Gate-Drain Charge			12		nC
Switching	g Characteristics			-	-	
<b>t</b> d(on)	Turn-on Delay Time			8.5		nS
tr	Turn-on Rise Time	Vdd=15V,		9		nS
t <sub>d(off)</sub>	Turn-Off Delay Time	ID=20A,		31		nS
t <sub>f</sub>	Turn-Off Fall Time	- RG=3, VGS=10V		9		nS
Source- [	Drain Diode Characteristics@ Tj = 25°C (	unless otherwise s	tated)			1
V <sub>SD</sub>	Forward on voltage	Isd=20A,Vgs=0V		0.8	1.2	V
t <sub>rr</sub>	Reverse Recovery Time	Tj=25℃,Isd=20A, VGs=0V		16		nS
Q <sub>rr</sub>	Reverse Recovery Charge	di/dt=500A/µs		42		nC

# Electrical Characteristics @Tj=25°C(unless otherwise specified)

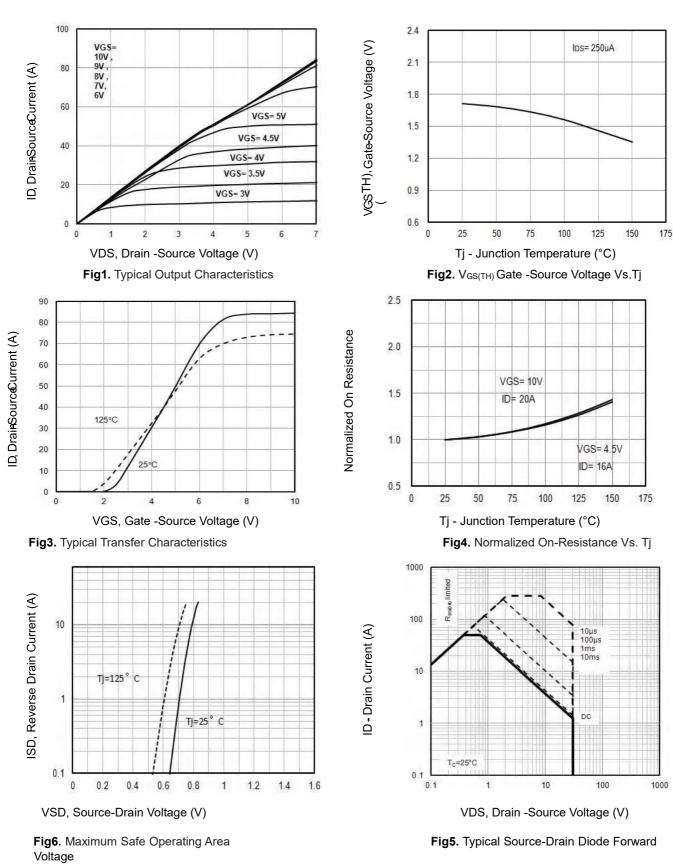
NOTE:

1 Repetitive rating; pulse width limited by max. junction temperature.

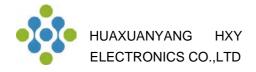
(2) Limited by TJmax, starting TJ = 25°C, L = 0.5mH,RG = 25, IAS = 15A, VGS = 10V. Part not recommended for use above this value

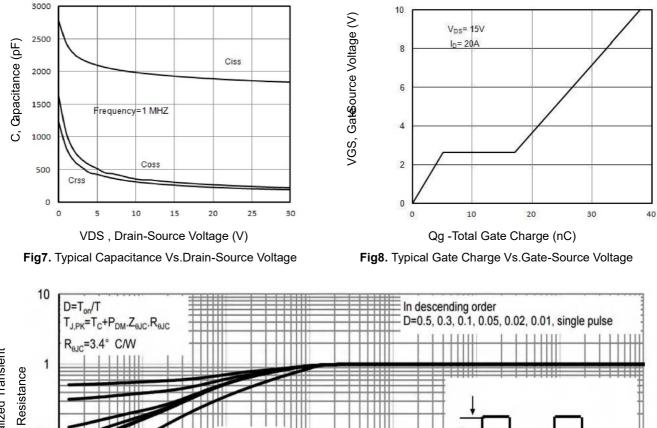
③ Pulse width  $\leq$  300µs; duty cycle $\leq$  2%.

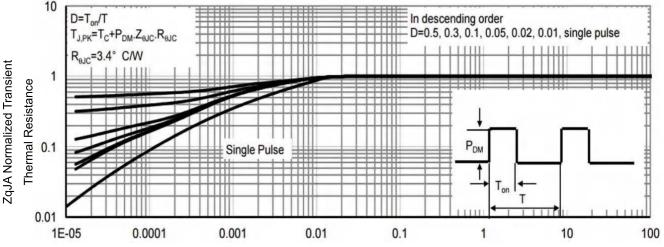




### **Typical Electrical and Thermal Characteristics (Curves)**







Pulse Width (s)

Fig9. Normalized Maximum Transient Thermal Impedance

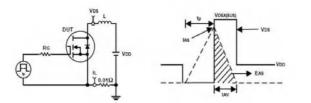


Fig10. Unclamped Inductive Test Circuit and waveforms

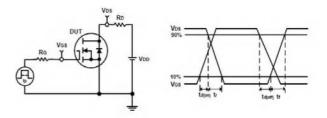
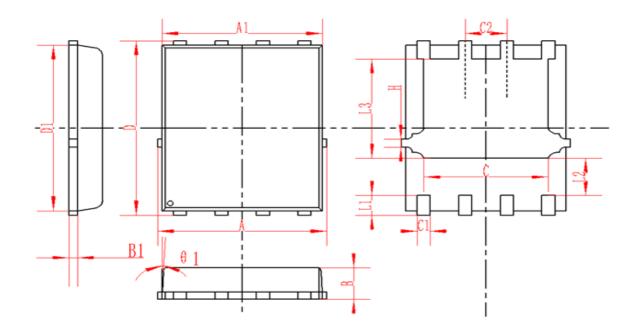


Fig11. Switching Time Test Circuit and waveforms



# DFN5X6-8L Package Information



SYMBOL	MM		INCH			
	MIN	NOM	MAX	MIN	NOM	MAX
А	4.95	5	5.05	0.195	0.197	0.199
A1	4.82	4.9	4.98	0.190	0.193	0.196
D	5.98	6	6.02	0.235	0.236	0.237
D1	5.67	5.75	5.83	0.223	0.226	0.230
В	0.9	0.95	1	0.035	0.037	0.039
B1	0.254REF		0.010REF			
С	3.95	4	4.05	0.156	0.157	0.159
C1	0.35	0.4	0.45	0.014	0.016	0.018
C2	1.27TYP		0.5TYP			
θ1	8°	10°	12°	8°	10°	12°
L1	0.63	0.64	0.65	0.025	0.025	0.026
L2	1.2	1.3	1.4	0.047	0.051	0.055
L3	3.415	3.42	3.425	0.134	0.135	0.135
Н	0.24	0.25	0.26	0.009	0.010	0.010



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