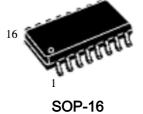


### DESCRIPTION

IC HMAX232EIDR is purposed for application in high-performance information processing systems and control devices of wide application. Input voltage levels are compatible with standard CMOS levels.

### **APPLICATIONS**

- Portable Computers
- Battery-Powered RS-232 Systems
- Interface Translation
- Low-Power Modems
- Terminals



# FEATURES

- Output voltage levels are compatible with input levels of C-MOS, N-MOS and TTL integrated circuits.
- Supply voltage range from 2.0 to 6.0 V.
- Low input current: 1.0 mkA; 0.1 mkA at T = 25 °C.
- Output current 24 mA.
- Latching current not less than 450 mA at T = 25°C
- Tolerable value of static potential not less than 2000V

### **ABSOLUTE MAXIMUM RATINGS**

PARAMETER	SYMBOL	MIN.	MAX.	UNIT
Supply Voltage	V <sub>CC</sub>	-0.3	6.0	V
Transmitter High Output Voltage	V <sub>+</sub>	V <sub>CC</sub> -0.3	14	V
Transmitter Low Output Voltage	V.	-0.3	-14	V
Transmitter Input Voltage	V <sub>TIN</sub>	-0.3	V <sub>+</sub> +0.3	V
Receiver Input Voltage	V <sub>RIN</sub>	-30	30	V
Voltage Applied to Transmitter Output	V <sub>TOUT</sub>	V <sub>-</sub> -0.3	V <sub>+</sub> +0.3	V
Voltage Applied to Receiver Output	V <sub>ROUT</sub>	-0.3	V <sub>CC</sub> +0.3	V
Storage Temperature Range	T <sub>STG</sub>	-65	150	°C

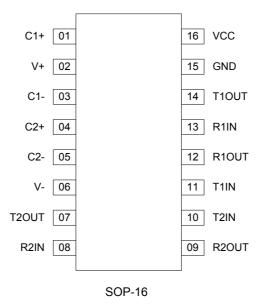
### FUNCTION TABLE

INPUT	OUTPUT
(RIN, TIN)	(ROUT, TOUT)
L (Low Level)	H (High Level)
H (High Level)	L (Low Level)





### **PIN CONFIGURATION**



### **PIN DESCRIPTION**

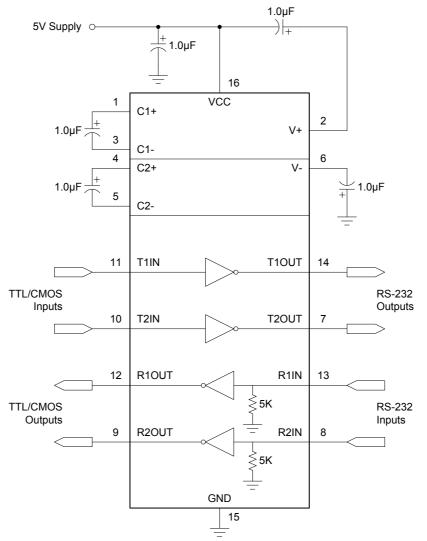
Pin No.	Pin Name	Pin Description
1	C1+	Terminal for Positive Charge-Pump C1 Capacitor
2	V+	Positive Voltage Generated by the Charge-Pump
3	C1-	Terminal for Negative Charge-Pump C1 Capacitor
4	C2+	Terminal for Positive Charge-Pump C2 Capacitor
5	C2-	Terminal for Negative Charge-Pump C2 Capacitor
6	V-	Negative Voltage Generated by the Charge-Pump
7	T2OUT	RS-232 Driver Output (Levels RS-232)
8	R2IN	RS-232 Receiver Input (Levels RS-232)
9	R2OUT	RS-232 Receiver Output (Levels TTL/CMOS)
10	T2IN	RS-232 Driver Input (Levels TTL/CMOS)
11	T1IN	RS-232 Driver Input (Levels TTL/CMOS)
12	R10UT	RS-232 Receiver Output (Levels TTL/CMOS)
13	R1IN	RS-232 Receiver Input (Levels RS-232)
14	T1OUT	RS-232 Driver Output (Levels RS-232)
15	GND	Ground
16	VCC	Supply Voltage Input

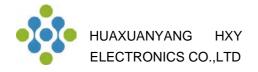


# **RECOMMENDED OPERATING CONDITIONS**

PARAMETER	SYMBOL	MIN.	MAX.	UNIT
Supply Voltage	V <sub>CC</sub>	4.5	5.5	V
Transmitter Input Voltage	V <sub>TIN</sub>	0	V <sub>CC</sub>	V
Receiver Input Voltage	V <sub>RIN</sub>	-30	30	V
Output Current of Transmitter Short Circuit	I <sub>SC</sub>	-	±60	mA
Ambient Temperature Range	T <sub>A</sub>	-40	+85	°C

# **TYPICAL APPLICATION CIRCUIT**





#### **ELECTRICAL CHARACTERISTICS**

(Limits in standard typeface are for  $T_A=25^{\circ}$ C, and the limits in boldface type apply over full operating temperature range.)

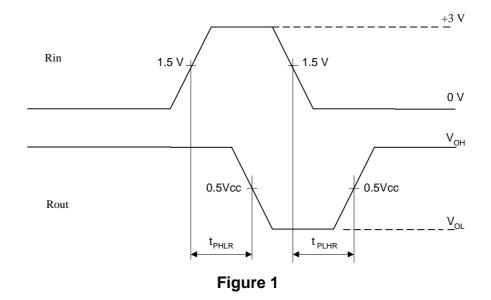
PARAMETER	SYMBOL	TEST CONDITIONS		MIN.	TYP.	MAX.	UNIT	
Supply Current	I <sub>CC</sub>	$V_{CC} = 5.5V$ $V_{IL} = 0V$		-	-	10.0 <b>14.0</b>	mA	
Receiver Parameters								
Hysteresis Voltage	V <sub>h</sub>	V <sub>CC</sub> = 5.0V		0.2 <b>0.2</b>	-	0.9 <b>1.0</b>	V	
On (Operation) Voltage	Von	$V_0 \le 0.1V$ , $I_{OL} \le$	20µA	-	-	2.4 <b>2.3</b>	V	
Off (Dropout) Voltage	V <sub>off</sub>	V <sub>O</sub> ≥ V <sub>CC</sub> - 0.1V I <sub>OH</sub> ≤ -20µA		0.8 <b>0.9</b>	-	-	V	
Output Low Voltage	V <sub>OL</sub>	I <sub>L</sub> = 3.2mA, V <sub>CC</sub> V <sub>IH</sub> = 2.4V	= 4.5V,	-	-	0.3 <b>0.4</b>	V	
Output High Voltage	V <sub>OH</sub>	I <sub>OH</sub> = -1.0mA, V V <sub>IL</sub> = 0.8V	<sub>CC</sub> = 4.5V,	3.6 <b>3.5</b>	-	-	V	
Input Resistance	Ri	V <sub>CC</sub> = 5.0V		3.0 <b>3.0</b>	-	7.0 <b>7.0</b>	kΩ	
Transmitter Parameters								
Output Low Voltage	V <sub>OL</sub>	$V_{CC}$ = 4.5V, $V_{IH}$ R <sub>L</sub> = 3.0k $\Omega$	= 2.0V,	-	-	-5.2 <b>-5.0</b>	V	
Output High Voltage	V <sub>OH</sub>	$V_{CC}$ = 4.5V, $V_{IL}$ = $R_L$ = 3.0k $\Omega$	V <sub>CC</sub> = 4.5V, V <sub>IL</sub> = 0.8V,		-	-	V	
Input Low Current	IIL	V <sub>CC</sub> = 5.5V, V <sub>IL</sub> = 0V		-	-	-1.0 <b>-10.0</b>	μA	
Input High Current	IIH	V <sub>CC</sub> = 5.5V, V <sub>IH</sub> = V <sub>CC</sub>		-	-	1.0 <b>10.0</b>	μA	
Speed Of Output Front Charge	SR		$V_{CC} = 5.0V, C_L = 50 - 1000 pF,$ $R_L = 3.0 - 7.0 k\Omega$		-	30 <b>27</b>	V/µs	
Output Resistance	Ro	$V_{CC} = V + = V - =$ $V_{O} = \pm 2V$	V <sub>CC</sub> = V+ = V- = 0V		-	-	Ω	
		V <sub>CC</sub> = 5.5V	V <sub>I</sub> = V <sub>CC</sub>	-	-	-50 <b>-60</b>		
Short Circuit Output Current	I <sub>SC</sub>	V <sub>O</sub> = 0V	V <sub>1</sub> = 0	-	-	50 <b>60</b>	— mA	
Speed Of Information Transmission	ST	$V_{CC} = 4.5V, C_L = 1000pF,$ $R_L = 3.0k\Omega, t_W = 7\mu s \text{ (for}$ extreme, $t_W = 8\mu s$ )		140 <b>120</b>	-	-	kbit/s	
Dynamic Parameters								
Signal Propagation Delay Time When Switching On (Off)	t <sub>PHLR</sub> (t <sub>PLHR</sub> )	$\label{eq:V_CC} \begin{array}{l} V_{CC} = 4.5 V, \ C_L = 150 p F, \\ V_{IL} = 0 V, \ V_{IH} = 3.0 V, \\ t_{LH} = t_{HL} \leq 10 n s \end{array}$		-	-	9.7 <b>10.0</b>	μs	
Signal Propagation Delay Time When Switching On (Off)	t <sub>рнlт</sub> (t <sub>plht</sub> )	$V_{CC}$ = 4.5V, $C_L$ = 2500pF, $V_{IL}$ = 0V, $V_{IH}$ = 3.0V, $R_L$ = 3k $\Omega$ , $t_{LH}$ = $t_{HL}$ ≤ 10ns		-	-	5.0 <b>6.0</b>	μs	



## Capacitance

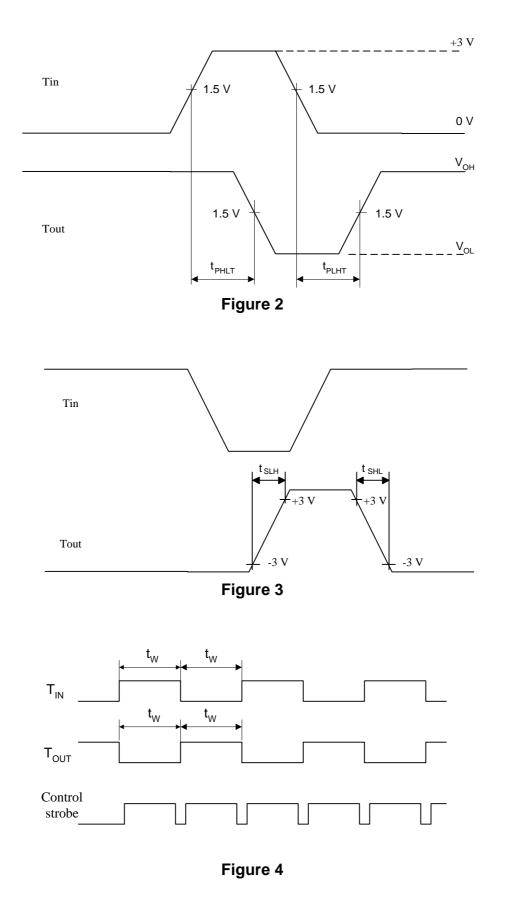
Symbol	Parameter	V <sub>cc</sub> , V	Rate	Unit
C <sub>IN</sub>	Input capacitance	5.0	9.0	pF
$C_{PD}$	Dynamic capacitance		90	

# Timing diagram when measuring IC dynamic parameters





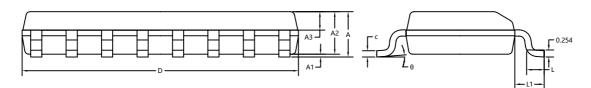


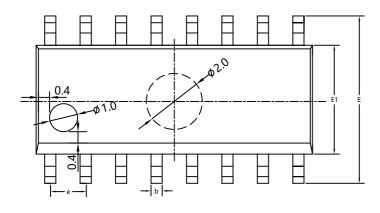




### PACKAGE OUTLINE DIMENSIONS

SOP-16





SYMBOL	MILLIMETER				
	MIN	NOM	МАХ		
A	1.50	1.60	1.70		
A1	0.10	0.15	0.25		
A2	1.40	1.45	1.50		
A3	0.60	0.65	0.70		
b	0.30	0.40	0.50		
с	0.15	0.20	0.25		
D	9.80	9.90	10.00		
E	5.80	6.00	6.20		
E1	3.85	3.90	3.95		
e	1.27BSC				
L	0.50	0.60 0.70			
L1	1.05BSC				
θ	0°	4° 8°			



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