

Description

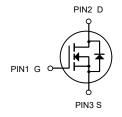
The 16N65F can be used in various power swithching circuit for system miniaturization and higher efficiency. The package form is TO-220/TO-220F, which accords with the RoHS standard.

S

TO-220F

General Features

 $V_{DS} = 650V, I_{D} = 20A$ $R_{DS(ON)} < 0.47\Omega@V_{GS} = 10V$



N-Channel MOSFET

Application

• Power switch circuit of adaptor and charger.

Package Marking and Ordering Information

Product ID	Pack	Marking	Units Tube
16N65F	TO-220F	16N65 XXX YYYY	50

Absolute Maximum Ratings@T =25°C(unless otherwise specified)

Symbol	Parameter	lmit	Unit	
V _{DSS}	Drain-to-Source Voltage ^[1]	650	V	
V _{GSS}	Gate-to-Source Voltage	±30	v	
I _D	Continuous Drain Current	16		
I _{D @ Tc =100} ℃	Continuous Drain Current @ Tc=100℃	10	Α	
I _{DM}	Pulsed Drain Current at V _{GS} =10V ^[2]	64		
E _{AS}	Single Pulse Avalanche Energy	845	mJ	
P _D	Power Dissipation	34	W	
T _L T _{PAK}	Maximum Temperature for Soldering Leads at 0.063in (1.6mm) from Case for 10 seconds, Package Body for 10 seconds	300 260	°C	
T _J & T _{STG}	Operating and Storage Temperature Range	-55 to 150		
R _{θJC}	Thermal Resistance, Junction-to-Case	3.7	°C AA	
$R_{\theta JA}$	Thermal Resistance, Junction-to-Ambient	52	°C/W	

Caution: Stresses greater than those listed in the "Absolute Maximum Ratings" may cause permanent damage to the device.



Electrical Characteristics T_J =25℃ unless otherwise specified

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
V _{(BR)DSS}	Drain-Source Breakdown Voltage	$I_D = 250 \mu A, V_{GS} = 0 V$	650	-	-	V
I _{DSS}	Zero Gate Voltage Drain Current	$V_{DS} = 650 V, V_{GS} = 0 V$	-	-	1.0	μА
I _{GSS}	Gate-Body Leakage Current	$V_{DS} = 0V, V_{GS} = \pm 30V$	-	-	±100	nA
$V_{GS(th)}$	Gate Threshold Voltage	$V_{DS} = V_{GS}, I_{D} = 250 \mu A$	2	3	4	V
R _{DS(ON)}	Static Drain-Source ON-Resistance ⁽⁴⁾	$V_{GS} = 10V, I_D = 8A$	-	0.48	0.62	Ω
C _{iss}	Input Capacitance	$V_{GS} = 0V, V_{DS} = 25V,$ f = 1MHz	-	2747	-	pF
C_{oss}	Output Capacitance		1	224	-	pF
C_{rss}	Reverse Transfer Capacitance		-	27	-	pF
Q_g	Total Gate Charge	$V_{GS} = 0 \text{ to } 10V$ $V_{DS} = 520V, I_D = 16A$	-	62	-	nC
Q_{gs}	Gate Source Charge		-	14	-	nC
Q_{gd}	Gate Drain("Miller") Charge		-	24	-	nC
t _{d(on)}	Turn-On DelayTime		-	38	-	ns
t _r	Turn-On Rise Time	$V_{GS} = 10V, V_{DD} = 310V$ $I_{D} = 16A, R_{GEN} = 24\Omega$	-	52	-	ns
t _{d(off)}	Turn-Off DelayTime		-	176	-	ns
t _f	Turn-Off Fall Time		-	68	-	ns
Is	Maximum Continuous Drain to Source Diode Forward Current		-	-	16	Α
I _{SM}	Maximum Pulsed Drain to Source Diode Forward Current		-	-	64	Α
V_{SD}	Drain to Source Diode Forward Voltage	$V_{GS} = 0V, I_{S} = 16A$	-	-	1.2	V
trr	Body Diode Reverse Recovery Time	1 400 41/44 4000/-	-	476	-	ns
Qrr	Body Diode Reverse Recovery Charge	$I_F = 16A$, di/dt = 100A/us	-	6.9	-	μC

Notes:

- 1. Repetitive Rating: Pulse Width Limited by Maximum Junction Temperature.
- 2. E_{AS} condition: Starting T_J =25C, V_{DD} =50V, V_G =10V, R_G =25ohm, L=10mH, I_{AS} =13A
- 3. $R_{\theta JA}$ is measured with the device mounted on a minimum recommended pad of 2oz copper FR4 PCB
- 4. Pulse Test: Pulse Width≤300µs, Duty Cycle≤0.5%.

Typical Characteristics

5

10

5

0

Figure 1: Output Characteristics

30
25
V_{GS} = 10V
V_{GS} = 5.0V

10

V_{DS}(V)

 $V_{GS} = 4.5V$

 $V_{GS} = 4.0V$

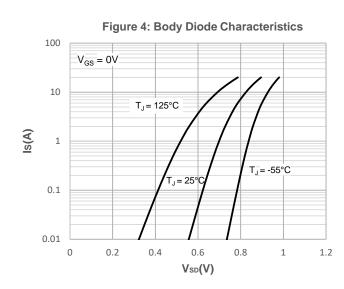
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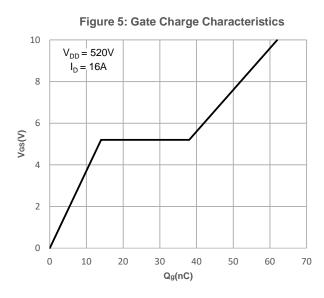
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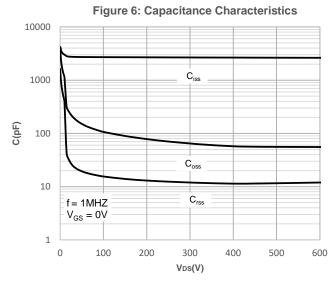
Figure 2: Typical Transfer Characteristics 10 $V_{DS} = 20V$ 8 6 Ib(A) $T_J = -55$ °C 4 T_J = 125°C $T_J = 25^{\circ}C$ 2 0 0 2 3 5 6

Vgs(V)

Figure 3: On-resistance vs. Drain Current 1000 800 RDS(ON)(MQ) 600 400 V_{GS} = 10V 200 0 6 0 3 9 12 15 I_D(A)







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Figure 7: Normalized Breakdown voltage vs. Junction Temperature

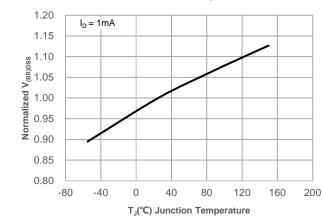


Figure 8: Normalized on Resistance vs. Junction Temperature

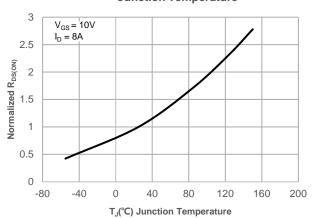


Figure 9: Maximum Safe Operating Area

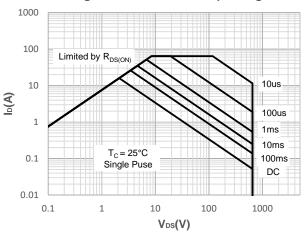


Figure 10: Maximum Continuous Drian Current vs. Case Temperature

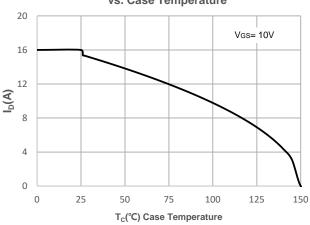


Figure 11: Normalized Maximum Transient Thermal Impedance

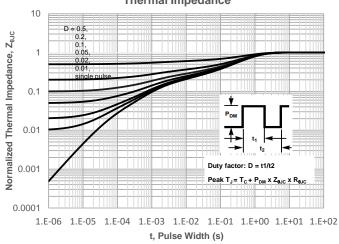
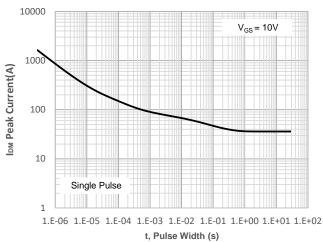


Figure 12: Peak Current Capacity



Test Circuit

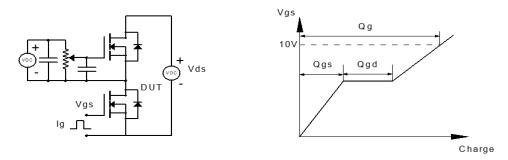


Figure 1: Gate Charge Test Circuit & Waveform

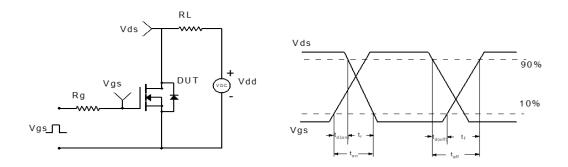


Figure 2: Resistive Switching Test Circuit & Waveform

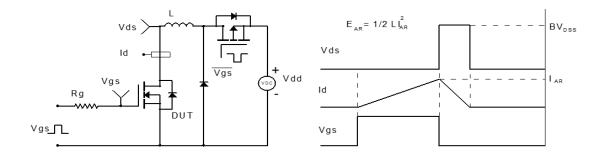


Figure 3: Unclamped Inductive Switching Test Circuit& Waveform

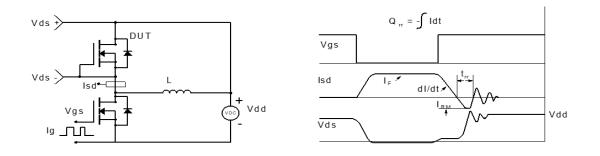
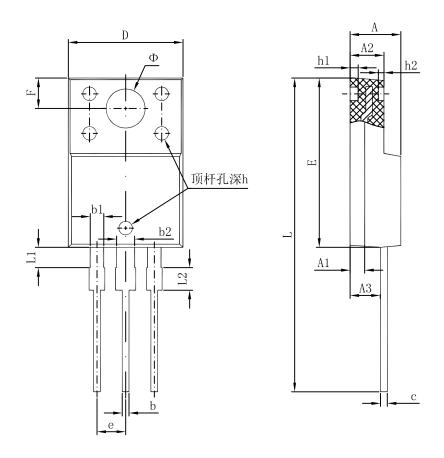


Figure 4: Diode Recovery Test Circuit & Waveform

Package Dimension TO-220F



Symbol	Dimensions In Millimeters		Dimensions In Inches		
	Min.	Max.	Min.	Max.	
Α	4.300	4.700	0.169	0.185	
A1	1.300	1.300 REF. 0.051 REF.		REF.	
A2	2.800	3.200	0.110	0.126	
A3	2.500	2.900	0.098	0.114	
b	0.500	0.750	0.020	0.030	
b1	1.100	1.350	0.043	0.053	
b2	1.500	1.750	0.059	0.069	
С	0.500	0.750	0.020	0.030	
D	9.960	10.360	0.392	0.408	
Е	14.800	15.200	0.583	0.598	
е	2.540 TYP.		0.100 TYP.		
F	2.700 REF.		0.106 REF.		
Φ	3.500 REF.		0.138 REF.		
h	0.000	0.300	0.000	0.012	
h1	0.800 REF.		0.031 REF.		
h2	0.500 REF.		0.020 REF.		
L	28.000	28.400	1.102	1.118	
L1	1.700	1.900	0.067	0.075	
L2	1.900	2.100	0.075	0.083	



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