

**NPN Silicon Epitaxial Planar Transistor**

for switching and amplifier applications.

As complementary types the PNP transistors 2N3905 and 2N3906 are recommended.

On special request, these transistors can be manufactured in different pin configurations.



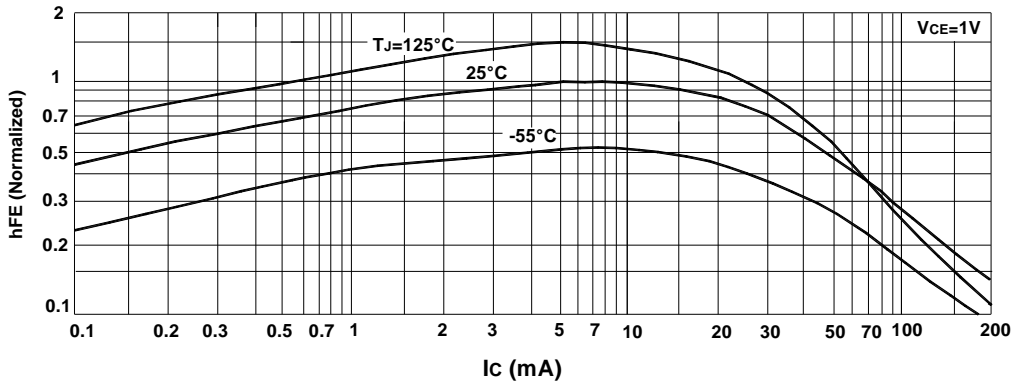
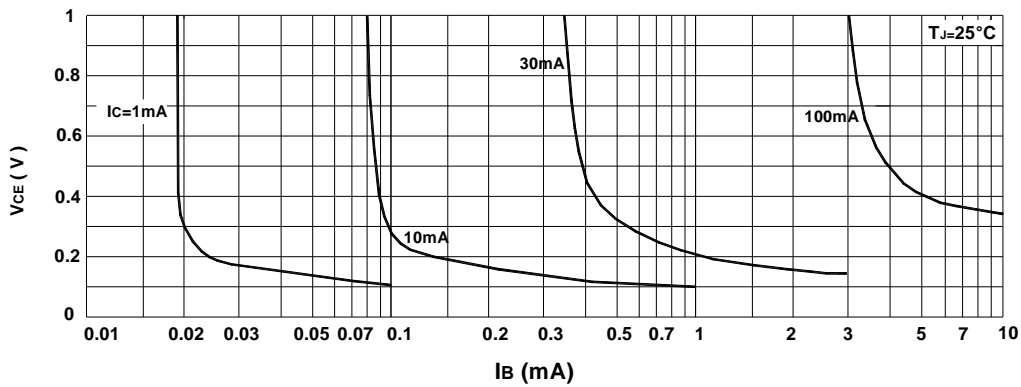
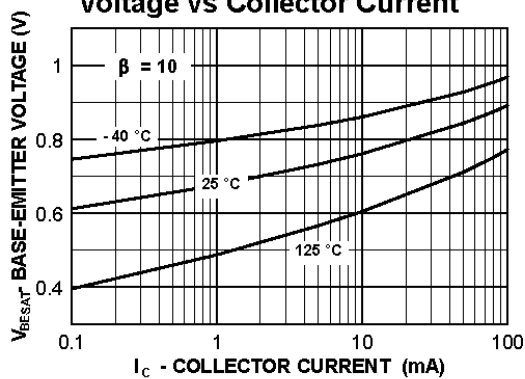
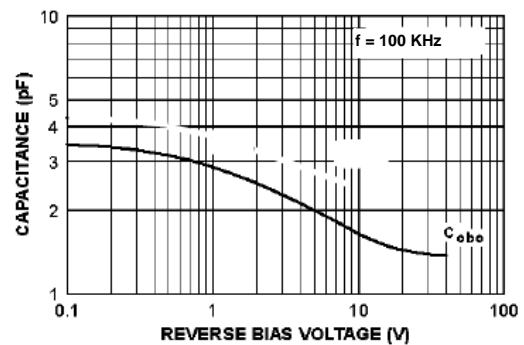
1. Emitter 2. Base 3. Collector  
TO-92 Plastic Package

**Absolute Maximum Ratings ( $T_a = 25\text{ }^\circ\text{C}$ )**

Parameter	Symbol	Value	Unit
Collector Base Voltage	$V_{CBO}$	60	V
Collector Emitter Voltage	$V_{CEO}$	40	V
Emitter Base Voltage	$V_{EBO}$	6	V
Collector Current	$I_C$	200	mA
Power Dissipation	$P_{tot}$	625	mW
Junction Temperature	$T_j$	150	$^\circ\text{C}$
Storage Temperature Range	$T_{stg}$	- 55 to + 150	$^\circ\text{C}$

**Characteristics at T<sub>a</sub> = 25 °C**

Parameter	Symbol	Min.	Max.	Unit
DC Current Gain				
at V <sub>CE</sub> = 1 V, I <sub>C</sub> = 0.1 mA	h <sub>FE</sub>	20	-	-
	h <sub>FE</sub>	40	-	-
at V <sub>CE</sub> = 1 V, I <sub>C</sub> = 1 mA	h <sub>FE</sub>	35	-	-
	h <sub>FE</sub>	70	-	-
at V <sub>CE</sub> = 1 V, I <sub>C</sub> = 10 mA	h <sub>FE</sub>	50	150	-
	h <sub>FE</sub>	100	300	-
at V <sub>CE</sub> = 1 V, I <sub>C</sub> = 50 mA	h <sub>FE</sub>	30	-	-
	h <sub>FE</sub>	60	-	-
at V <sub>CE</sub> = 1 V, I <sub>C</sub> = 100 mA	h <sub>FE</sub>	15	-	-
	h <sub>FE</sub>	30	-	-
Collector Base Cutoff Current at V <sub>CB</sub> = 30 V	I <sub>CBO</sub>	-	50	nA
Emitter Base Cutoff Current at V <sub>EB</sub> = 6 V	I <sub>EBO</sub>	-	50	nA
Collector Base Breakdown Voltage at I <sub>C</sub> = 10 μA	V <sub>(BR)CBO</sub>	60	-	V
Collector Emitter Breakdown Voltage at I <sub>C</sub> = 1 mA	V <sub>(BR)CEO</sub>	40	-	V
Emitter Base Breakdown Voltage at I <sub>E</sub> = 10 μA	V <sub>(BR)EBO</sub>	6	-	V
Collector Emitter Saturation Voltage at I <sub>C</sub> = 10 mA, I <sub>B</sub> = 1 mA	V <sub>CE(sat)</sub>	-	0.2	V
at I <sub>C</sub> = 50 mA, I <sub>B</sub> = 5 mA	V <sub>CE(sat)</sub>	-	0.3	
Base Emitter Saturation Voltage at I <sub>C</sub> = 10 mA, I <sub>B</sub> = 1 mA	V <sub>BE(sat)</sub>	-	0.85	V
at I <sub>C</sub> = 50 mA, I <sub>B</sub> = 5 mA	V <sub>BE(sat)</sub>	-	0.95	
Gain Bandwidth Product at V <sub>CE</sub> = 20 V, I <sub>C</sub> = 10 mA, f = 100 MHz	f <sub>T</sub>	250 300	- -	MHz
Collector Base Capacitance at V <sub>CB</sub> = 5 V, f = 100 KHz	C <sub>ob</sub>	-	4	pF
Delay Time at V <sub>CC</sub> = 3 V, V <sub>BE</sub> = 0.5 V, I <sub>C</sub> = 10 mA, I <sub>B1</sub> = 1 mA	t <sub>d</sub>	-	35	ns
Rise Time at V <sub>CC</sub> = 3 V, V <sub>BE</sub> = 0.5 V, I <sub>C</sub> = 10 mA, I <sub>B1</sub> = 1 mA	t <sub>r</sub>	-	35	ns
Storage Time at V <sub>CC</sub> = 3 V, I <sub>C</sub> = 10 mA, I <sub>B1</sub> = -I <sub>B2</sub> = 1 mA	t <sub>s</sub>	-	200	ns
Fall Time at V <sub>CC</sub> = 3 V, I <sub>C</sub> = 10 mA, I <sub>B1</sub> = -I <sub>B2</sub> = 1 mA	t <sub>f</sub>	-	50	ns

**DC Current Gain**

**Collector Saturation Region**

**Base-Emitter Saturation Voltage vs Collector Current**

**Capacitance vs Reverse Bias Voltage**


TO-92 Package

Package Outline Dimensions (Units: mm)

