

# Shielded Power Inductors – XGL6020



- Industry's lowest DCR & ultra low AC losses
- AEC-Q200 Grade 1 (–40°C to +125°C)
- Superior current handling with soft saturation characteristics
- Wide inductance range from 0.055 – 10  $\mu$ H

**Core material** Composite

**Core and winding loss** See [www.coilcraft.com/coreloss](http://www.coilcraft.com/coreloss)

**Environmental** RoHS compliant, halogen free

**Terminations** RoHS compliant tin-silver (96.5/3.5) over copper. Other terminations available at additional cost.

**Weight:** 0.46 – 0.51 g

**Operating voltage:** 0 – 80 V

**Ambient temperature** –40°C to +125°C with (40°C rise) Irms current.

**Maximum part temperature** +165°C (ambient + temp rise). Derating.

**Storage temperature** Component: –55°C to +165°C.

Tape and reel packaging: –55°C to +80°C

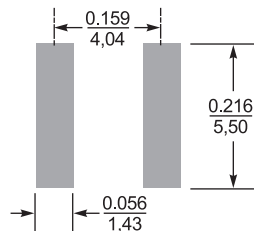
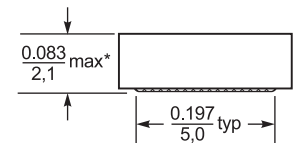
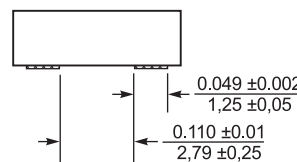
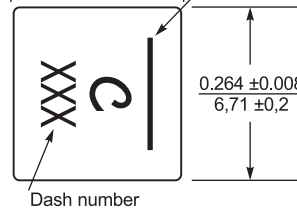
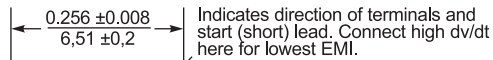
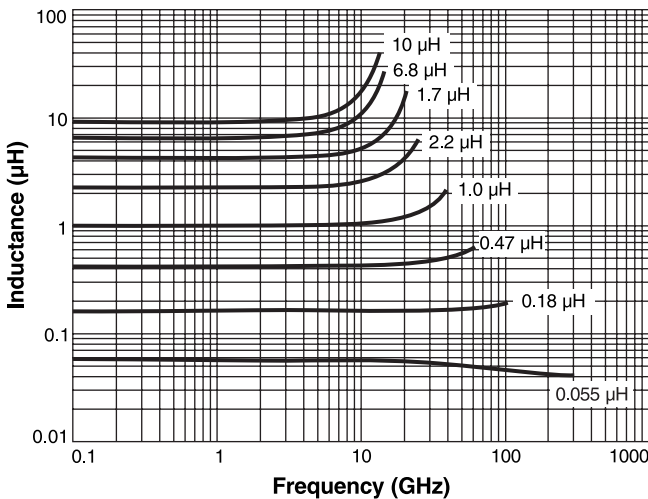
**Resistance to soldering heat** Max three 40 second reflows at +260°C, parts cooled to room temperature between cycles

**Moisture Sensitivity Level (MSL)** 1 (unlimited floor life at <30°C / 85% relative humidity)

**Packaging** 500/7" reel; 2500/13" reel Plastic tape: 16 mm wide, 0.35 mm thick, 12 mm pocket spacing, 2.23 mm pocket depth

**PCB washing** Tested to MIL-STD-202 Method 215 plus an additional aqueous wash. See [Doc787\\_PCB\\_Washing.pdf](#).

## Typical L vs Frequency



**Recommended Land Pattern**

\* For optional tin-lead and tin-silver-copper terminations, dimensions are for the mounted part. Dimensions before mounting can be an additional 0.005 inch / 0.13 mm.

Dimensions are in  $\frac{\text{inches}}{\text{mm}}$



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Part number <sup>1</sup>	Inductance <sup>2</sup> ±20% (µH)	DCR (mOhms) <sup>3</sup>		SRF typ <sup>4</sup> (MHz)	Isat (A) <sup>5</sup>			Irms (A) <sup>6</sup>	
		typ	max		10% drop	20% drop	30% drop	20°C rise	40°C rise
XGL6020-550ME_	0.055	0.75	0.87	485	23.0	37.0	51.5	28.0	38.0
XGL6020-181ME_	0.18	1.5	1.8	150	13.8	24.0	33.0	22.4	30.4
XGL6020-331ME_	0.33	2.6	3.1	85	9.6	16.9	25.0	17.8	23.7
XGL6020-471ME_	0.47	3.5	4.2	75	8.2	13.6	19.3	15.8	21.8
XGL6020-681ME_	0.68	4.6	5.5	58	6.8	11.6	16.7	13.7	19.0
XGL6020-102ME_	1.00	6.7	8.0	45	5.5	9.2	13.4	11.0	13.9
XGL6020-152ME_	1.50	9.2	11.0	38	4.8	7.9	11.4	9.1	12.0
XGL6020-222ME_	2.20	14.0	16.1	27	3.9	6.3	8.8	8.4	11.4
XGL6020-332ME_	3.30	21.1	24.3	23	3.2	5.3	7.6	6.4	8.8
XGL6020-472ME_	4.70	29.4	33.8	18	2.5	4.2	6.0	5.6	7.7
XGL6020-562ME_	5.6	35.1	40.4	17	2.4	4.0	5.7	4.6	6.3
XGL6020-682ME_	6.8	43.3	49.8	15	2.2	3.6	5.1	4.5	6.2
XGL6020-822ME_	8.2	49.0	56.4	14.5	2.0	3.3	4.8	4.1	5.6
XGL6020-103ME_	10	67.4	77.5	14	1.8	3.1	4.4	3.5	4.7

1. When ordering, please specify **termination** and **packaging** codes:

XGL6020-103MEC

**Termination:** E = RoHS compliant tin-silver over copper.

**Special order:** T = RoHS tin-silver-copper (95.5/4/0.5) or S = non-RoHS tin-lead (63/37).

**Packaging:** C = 7" machine-ready reel. EIA-481 embossed plastic tape (500 parts per reel). Quantities less than full reel available: in tape (not machine ready) or with leader and trailer (\$25 charge).

D = 13" machine-ready reel. EIA-481 embossed plastic tape (2500 parts per reel). Factory order only, not stocked.

- Inductance tested at 1 MHz, 0.1 Vrms, 0 Adc.
- DCR measured on a micro-ohmmeter.
- SRF measured using Agilent/HP 4395A or equivalent.
- DC current at 25°C that causes the specified inductance drop from its value without current.  
[Click for temperature derating information.](#)
- Current that causes the specified temperature rise from 25°C ambient. This information is for reference only and does not represent absolute maximum ratings. [Click for temperature derating information.](#)
- Electrical specifications at 25°C.

Refer to Doc 362 "Soldering Surface Mount Components" before soldering.

## Irms Testing

Irms testing was performed on 0.75 inch wide × 0.25 inch thick copper traces in still air.

Temperature rise is highly dependent on many factors including pcb land pattern, trace size, and proximity to other components. Therefore temperature rise should be verified in application conditions.



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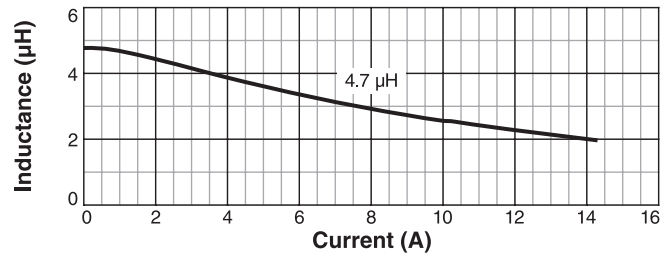
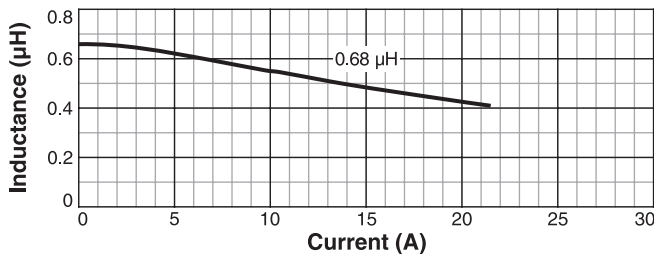
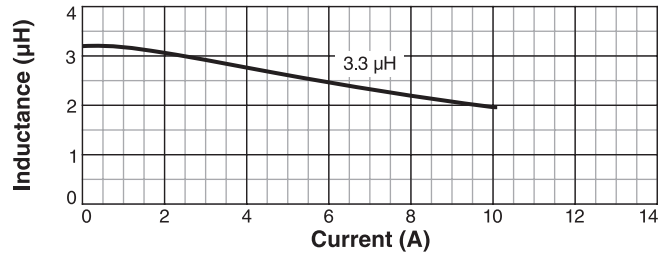
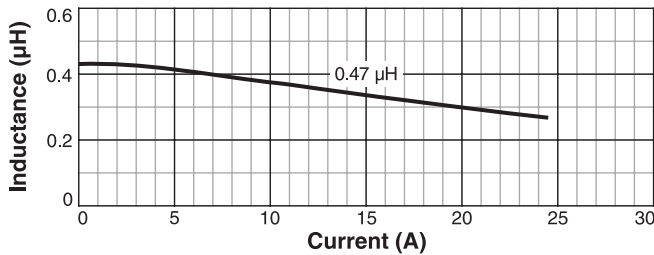
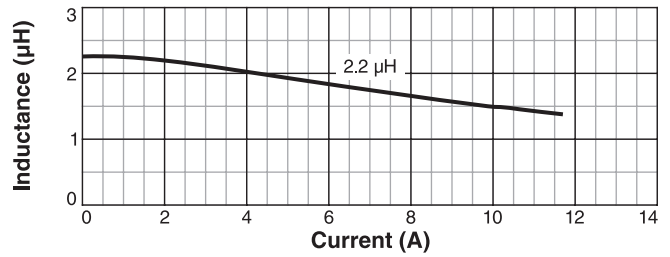
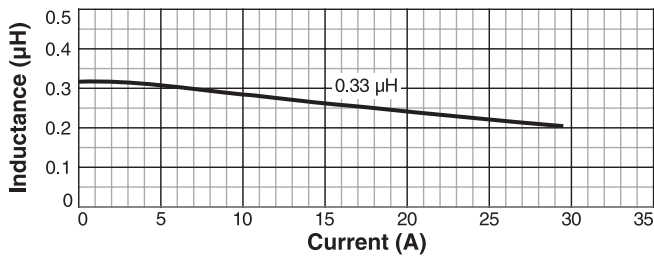
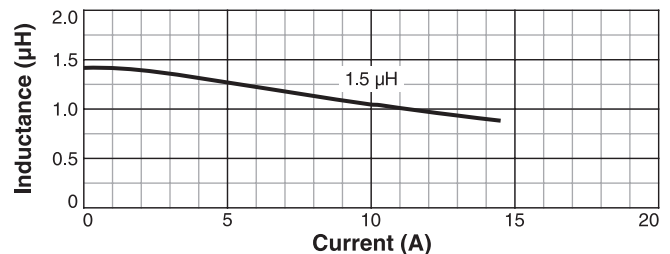
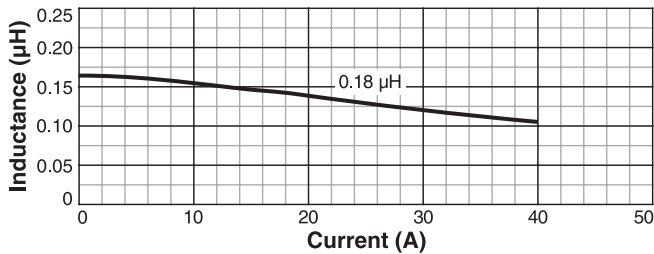
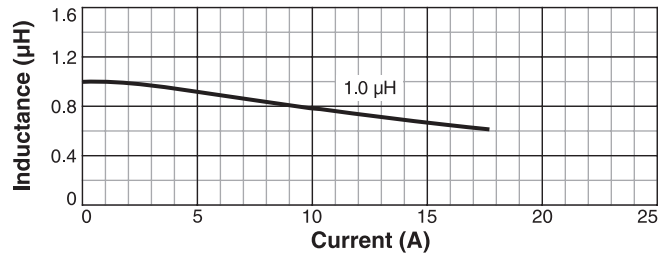
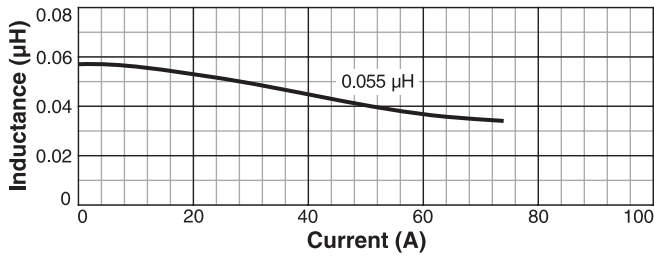
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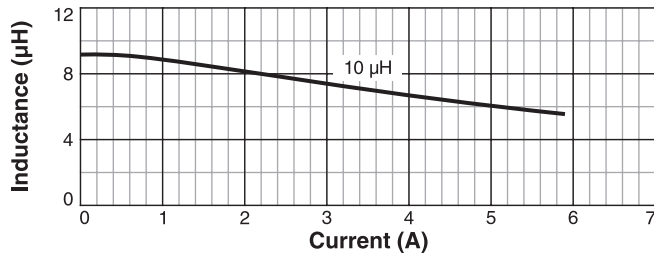
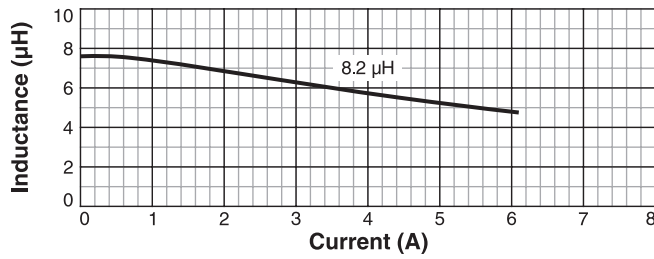
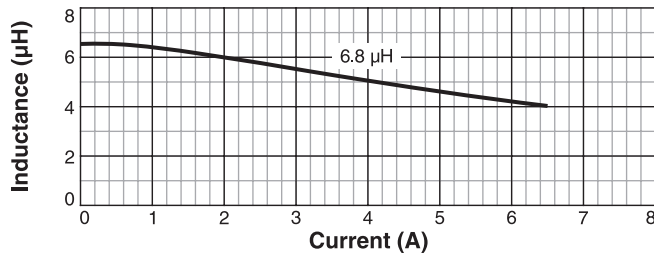
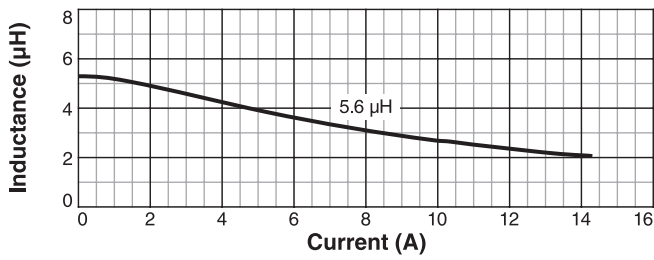
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