

• General Description

The AGM320M combines advanced trench MOSFET technology with a low resistance package to provide extremely low $R_{DS(ON)}$.

This device is ideal for load switch and battery protection applications.

• Features

- Advance high cell density Trench technology
- Low $R_{DS(ON)}$ to minimize conductive loss
- Low Gate Charge for fast switching
- Low Thermal resistance
- 100% Avalanche tested
- 100% DVDS tested

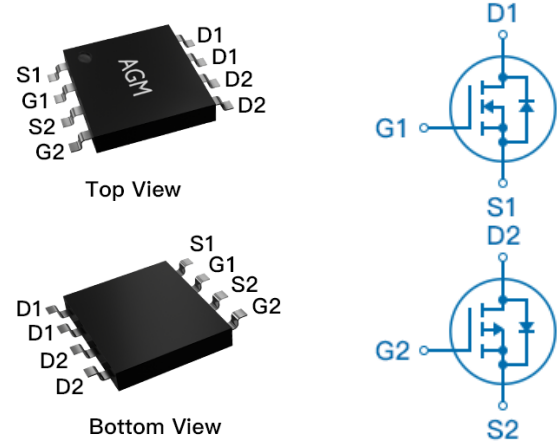
• Application

- MB/VGA Vcore
- SMPS 2nd Synchronous Rectifier
- POL application
- BLDC Motor driver

Product Summary

BVDSS	RDSON	ID
30V	19.5mΩ	8.0A
-30V	48mΩ	-6.8A

SOP8 Pin Configuration



Package Marking and Ordering Information

Device Marking	Device	Device Package	Reel Size	Tape width	Quantity
AGM320M	AGM320M	SOP8	330mm	12mm	3000

Table 1. Absolute Maximum Ratings (TA=25°C)

Symbol	Parameter	Rating		Units
		N-Ch	P-Ch	
V_{DS}	Drain-Source Voltage ($V_{GS}=0V$)	30	-30	V
V_{GS}	Gate-Source Voltage ($V_{DS}=0V$)	±20	±20	V
I_D	Drain Current-Continuous(TA=25°C) (Note 1)	8.0	-6.8	A
	Drain Current-Continuous(TA=100°C)	5.3	-4.5	A
IDM (pluse)	Drain Current-Pulsed (Note 2)	32	-27.2	A
P_D	Total Power Dissipation(TA=25°C)	1.7	1.0	W
EAS	Avalanche energy (Note 3)	25	25	mJ
TJ,TSTG	Operating Junction and Storage Temperature Range	-55 To 150	-55 To 150	°C

Table 2. Thermal Characteristic

Symbol	Parameter	Max(N)	Max(P)	Unit
$R_{\theta JA}$	Thermal Resistance Junction-ambient (Steady State) ¹	73.5	125	°C/W

Table 3. N- Channel Electrical Characteristics (TA=25°C unless otherwise noted)

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
On/Off States						
BVDSS	Drain-Source Breakdown Voltage	VGS=0V ID=250μA	30	--	--	V
IDSS	Zero Gate Voltage Drain Current	VDS=30V,VGS=0V	--	--	1	μA
IGSS	Gate-Body Leakage Current	VGS=±20V,VDS=0V	--	--	±100	nA
VGS(th)	Gate Threshold Voltage	VDS=VGS,ID=250μA	1.2	--	2.2	V
gFS	Forward Transconductance	VDS=5V,ID=3A	--	4	--	S
RDS(on)	Drain-Source On-State Resistance	VGS=10V, ID=3.5A	--	19.5	28	mΩ
		VGS=4.5V, ID=3A	--	30	55	mΩ
Dynamic Characteristics						
Ciss	Input Capacitance	VDS=15V,VGS=0V, F=1MHZ	--	315	--	pF
Coss	Output Capacitance		--	59	--	pF
Crss	Reverse Transfer Capacitance		--	50	--	pF
Rg	Gate resistance	VGS=0V, VDS=0V,f=1.0MHz	--	1.7	--	Ω
Switching Times						
td(on)	Turn-on Delay Time	VGS=10V,VDS=15V, ID=1A, RGEN=3Ω	--	4.0	--	nS
tr	Turn-on Rise Time		--	23	--	nS
td(off)	Turn-Off Delay Time		--	7.0	--	nS
tf	Turn-Off Fall Time		--	19	--	nS
Qg	Total Gate Charge	VGS=10V, VDS=15V, ID=3.6A	--	7.0	--	nC
Qgs	Gate-Source Charge		--	10	--	nC
Qgd	Gate-Drain Charge		--	1.3	--	nC
Source-Drain Diode Characteristics						
ISD	Source-Drain Current(Body Diode)		--	--	8.0	A
VSD	Forward on Voltage	VGS=0V,IS=3.5A	--	--	1.2	V
trr	Reverse Recovery Time	IF=3.5A , dl/dt=100A/μs ,	--	--	--	ns
Qrr	Reverse Recovery Charge	TJ=25°C	--	--	--	nc

Notes 1.The maximum current rating is package limited.

Notes 2.Repetitive Rating: Pulse width limited by maximum junction temperature

Notes 3.EAS condition: TJ=25°C ,VDD=15V,Vgs=10V,ID=10A,L=0.5mH,RG=25ohm

Table 3. P-Channel Electrical Characteristics (TA=25°C unless otherwise noted)

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
On/Off States						
BVDSS	Drain-Source Breakdown Voltage	VGS=0V ID=-250μA	-30	--	--	V
IDSS	Zero Gate Voltage Drain Current	VDS=-30V, VGS=0V	--	--	-1	μA
IGSS	Gate-Body Leakage Current	VGS=±20V, VDS=0V	--	--	±100	nA
VGS(th)	Gate Threshold Voltage	VDS=VGS, ID=-250μA	-1.2	--	-2.2	V
gFS	Forward Transconductance	VDS=-5V, ID=-1.5A	--	3	--	S
RDS(on)	Drain-Source On-State Resistance	VGS=-10V, ID=-2.0A	--	48	68	mΩ
		VGS=-4.5V, ID=-1.5A	--	68	98	mΩ
Dynamic Characteristics						
Ciss	Input Capacitance	VDS=-15V, VGS=0V, F=1MHZ	--	226	--	pF
Coss	Output Capacitance		--	47	--	pF
Crss	Reverse Transfer Capacitance		--	40	--	pF
Rg	Gate resistance	VGS=0V, VDS=0V, f=1.0MHz	--	12	--	Ω
Switching Times						
td(on)	Turn-on Delay Time	VGS=-15V, VDS=-10V, ID=-2A, RGEN=6Ω	--	9.0	--	nS
tr	Turn-on Rise Time		--	9.0	--	nS
td(off)	Turn-Off Delay Time		--	18	--	nS
tf	Turn-Off Fall Time		--	6.0	--	nS
Qg	Total Gate Charge	VGS=-10V, VDS=-15V, ID=-2A	--	9.0	--	nC
Qgs	Gate-Source Charge		--	2.0	--	nC
Qgd	Gate-Drain Charge		--	1.9	--	nC
Source-Drain Diode Characteristics						
ISD	Source-Drain Current(Body Diode)		--	--	-6.8	A
VSD	Forward on Voltage	VGS=0V, IS=-2A	--	--	-1.2	V
trr	Reverse Recovery Time	IF=-2A , dI/dt=100A/μs , TJ=25°C	--	--	--	ns
Qrr	Reverse Recovery Charge		--	--	--	nc

Notes 1.The maximum current rating is package limited.

Notes 2.Repetitive Rating: Pulse width limited by maximum junction temperature

Notes 3.EAS condition: TJ=25°C, VDD=-15V, Vgs=-10V, ID=-10A, L=0.5mH, RG=25ohm

N-Channel Test Circuit and Waveform

Figure A: Gate Charge Test Circuit and Waveform

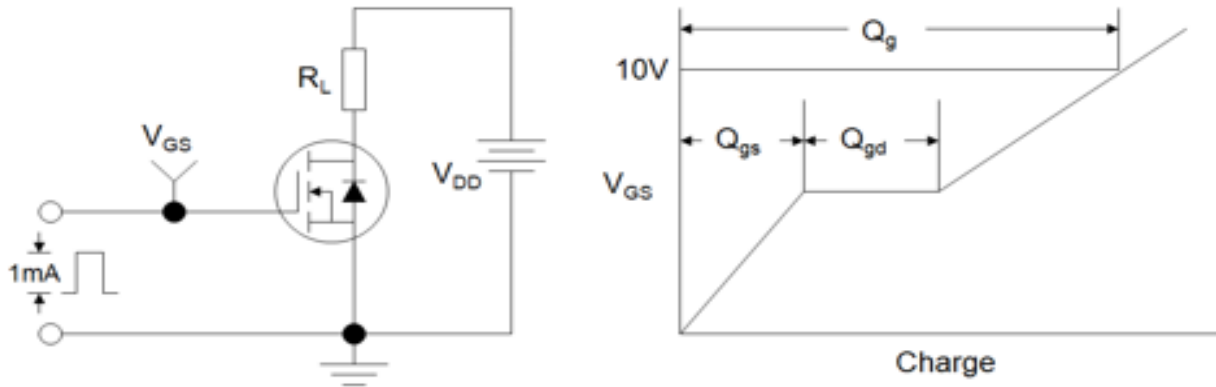


Figure B: Resistive Switching Test Circuit and Waveform

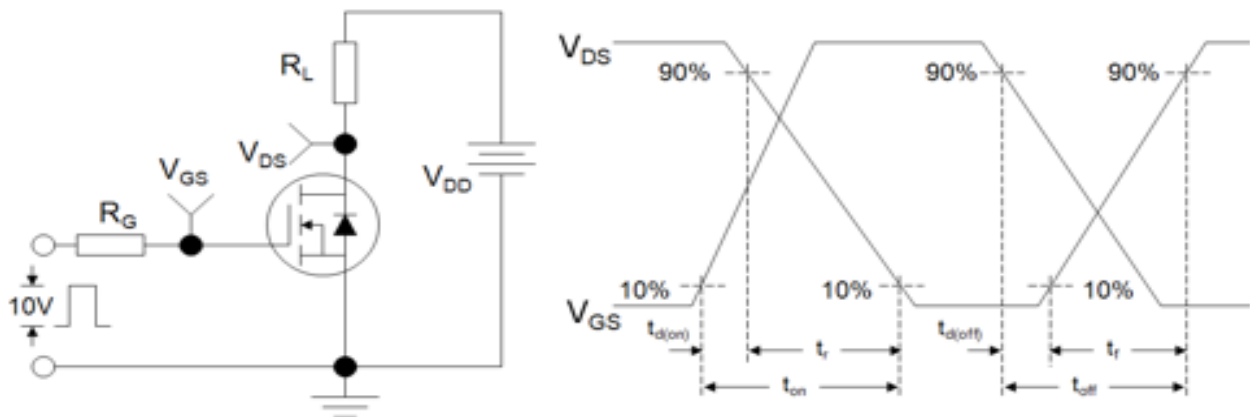
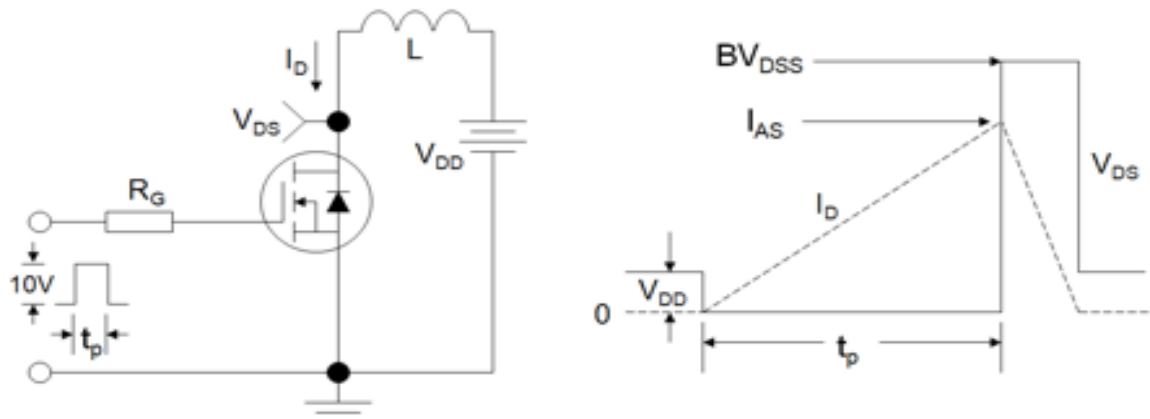


Figure C: Unclamped Inductive Switching Test Circuit and Waveform



N-Channel Characteristics Curve:

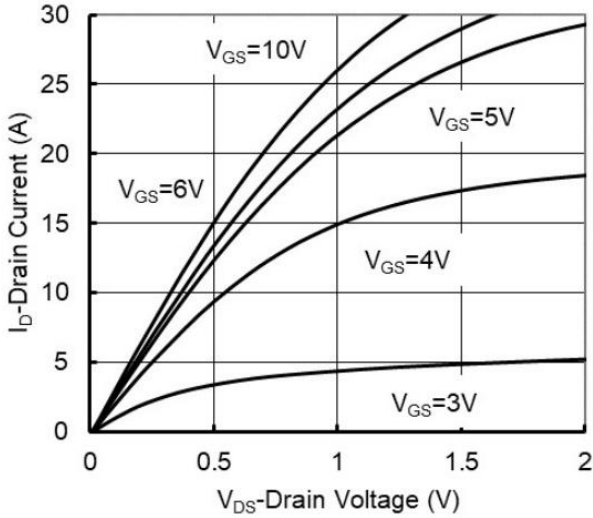


Figure1. Output Characteristics

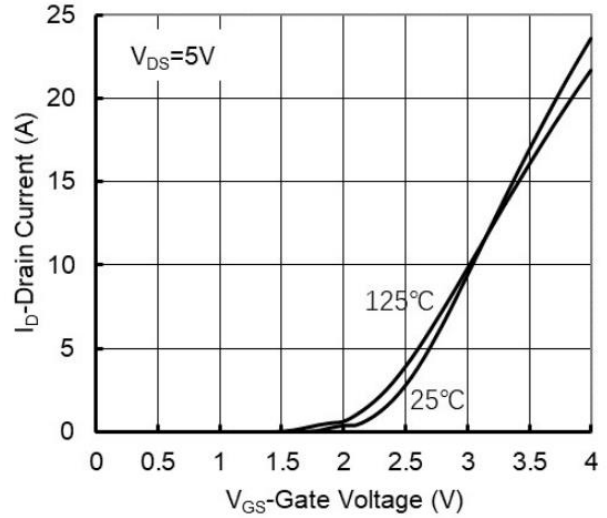


Figure2. Transfer Characteristics

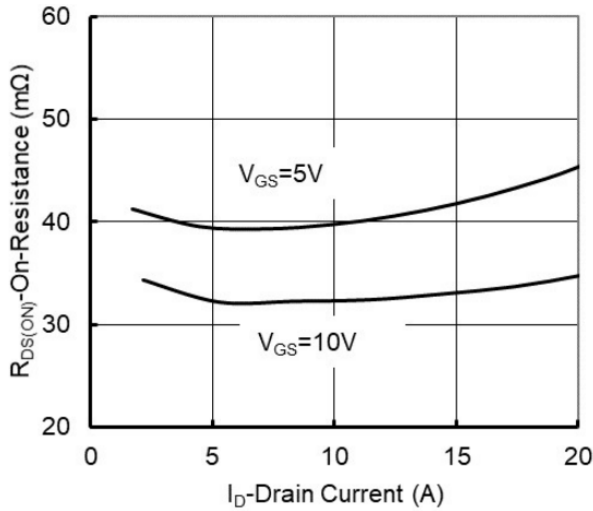


Figure 3: On-Resistance vs. Drain Current and Gate Voltage

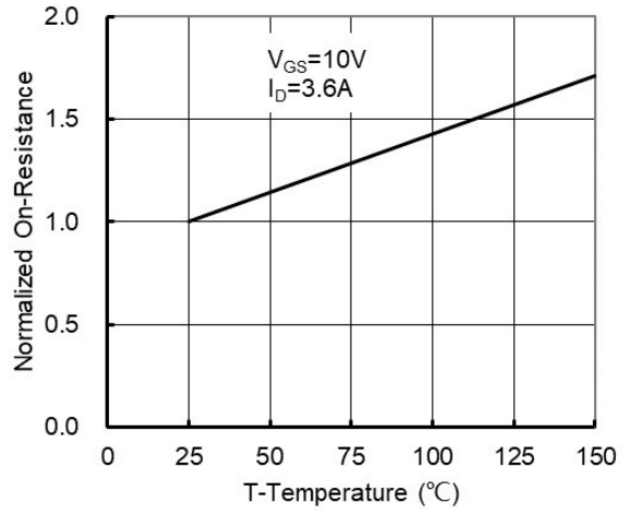


Figure 4: On-Resistance vs. Junction Temperature

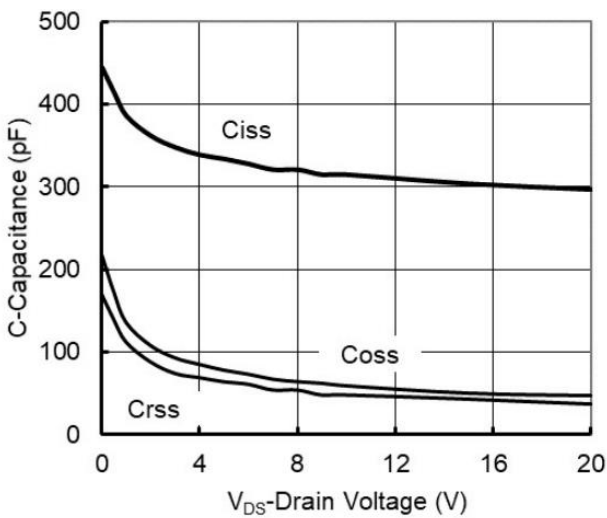


Figure5. Capacitance Characteristics

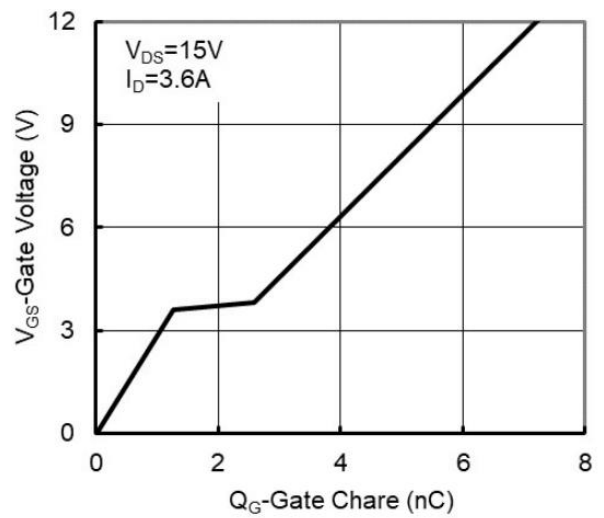


Figure6. Gate Charge

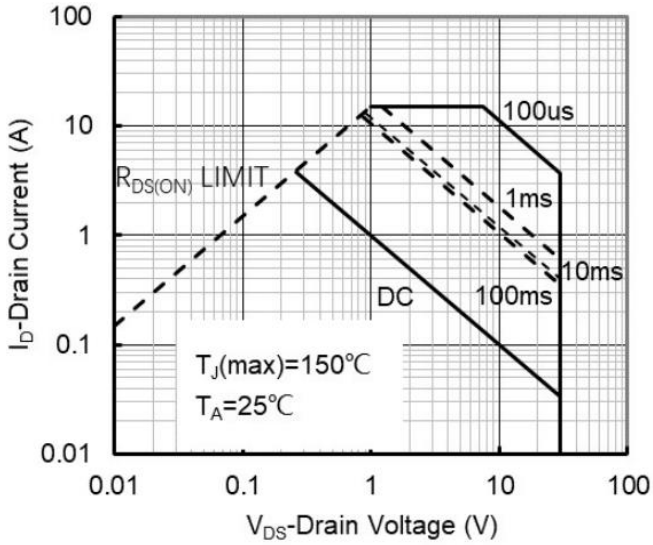


Figure7. Safe Operation Area

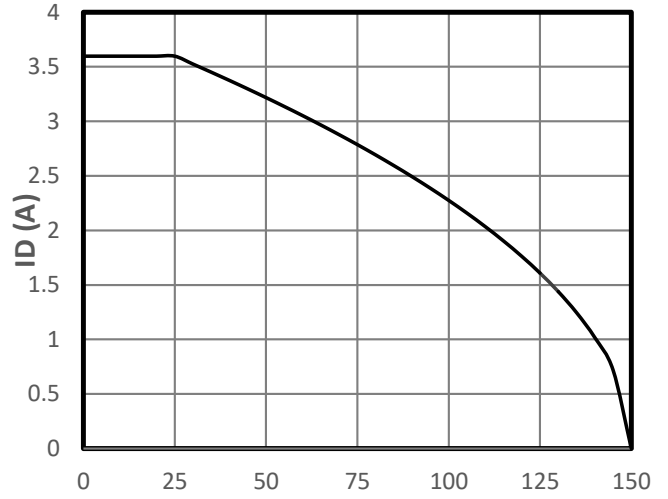


Figure8. Maximum Continuous Drain Current vs Ambient Temperature

Figure 8. Normalized Maximum Transient Thermal Impedance

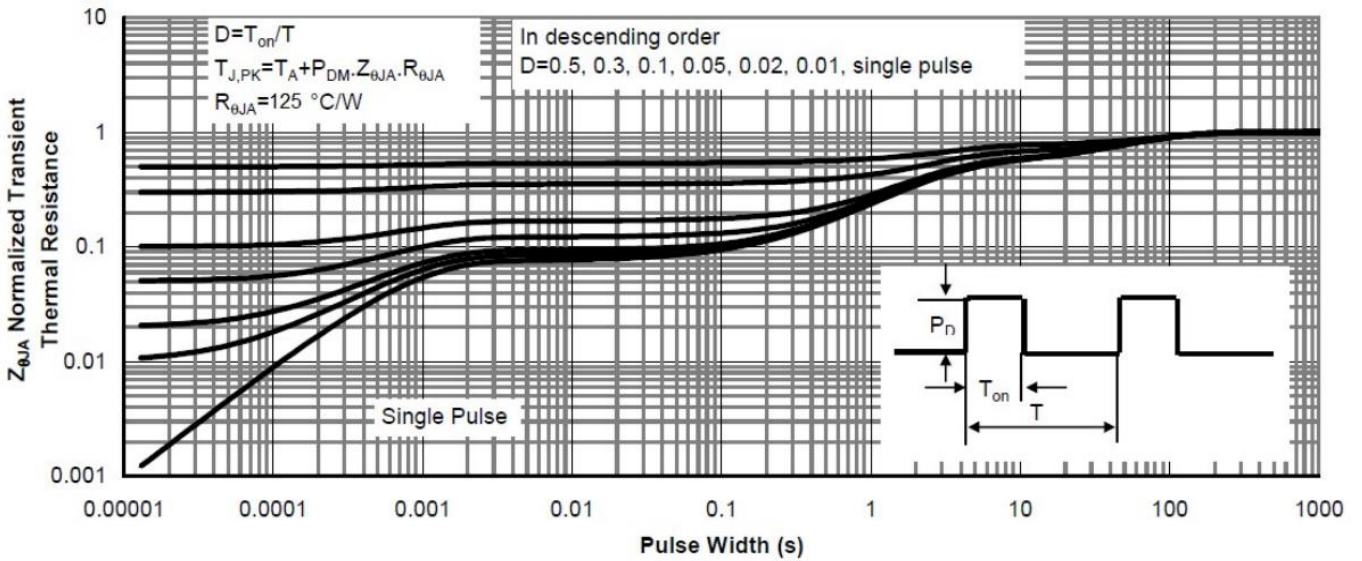


Figure9. Normalized Maximum Transient Thermal Impedance

P Channel Characteristics Curve:

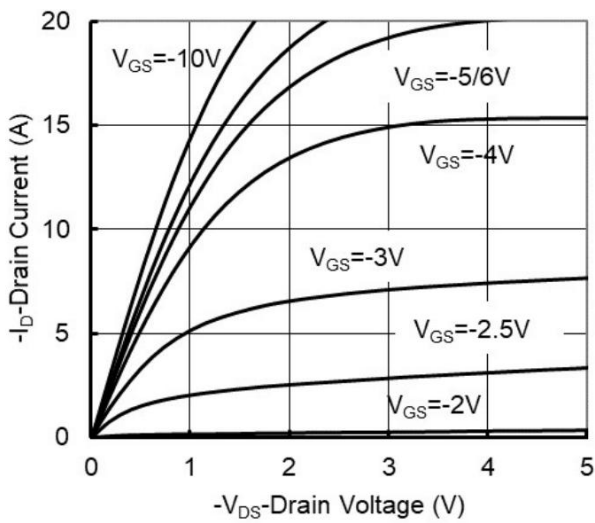
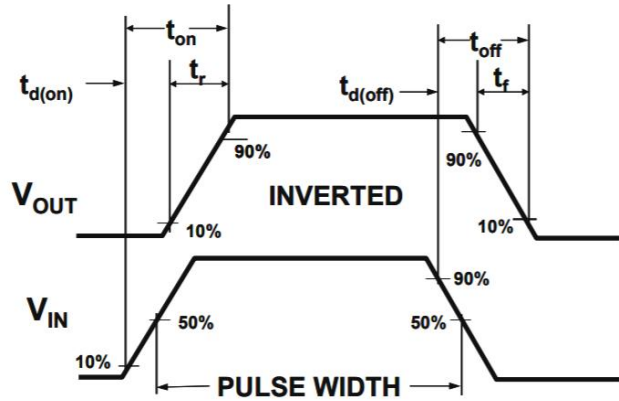
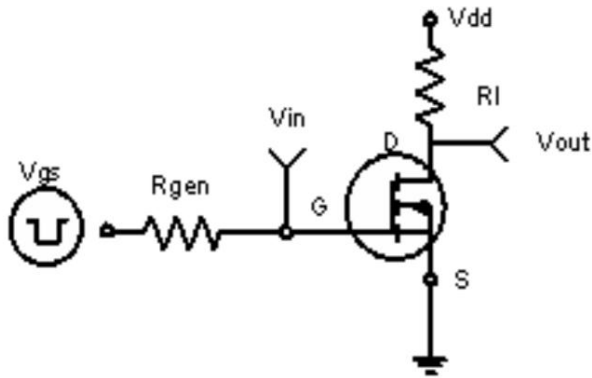


Figure1. Output Characteristics

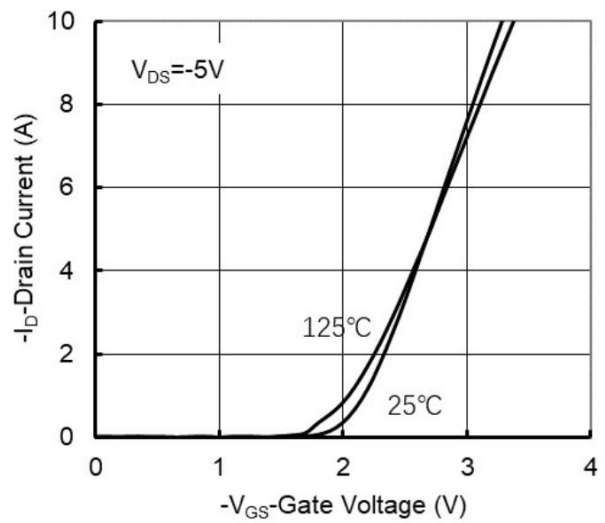


Figure2. Transfer Characteristics

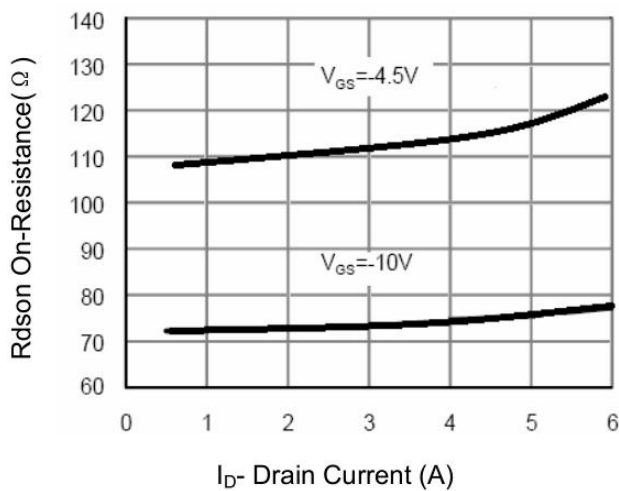


Figure 3: On-Resistance vs. Drain Current and Gate Voltage

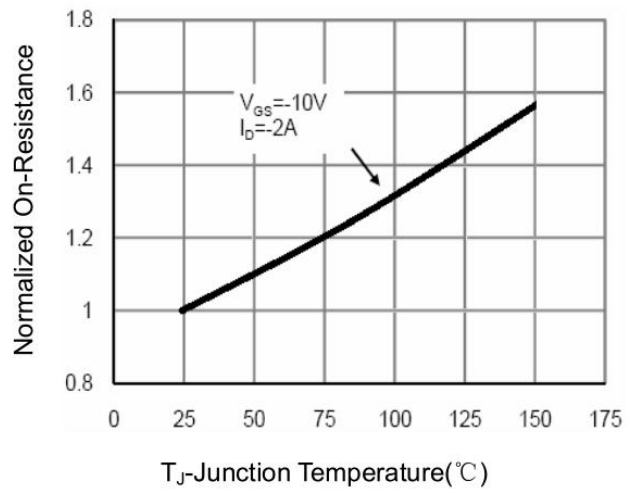


Figure 4: On-Resistance vs. Junction Temperature

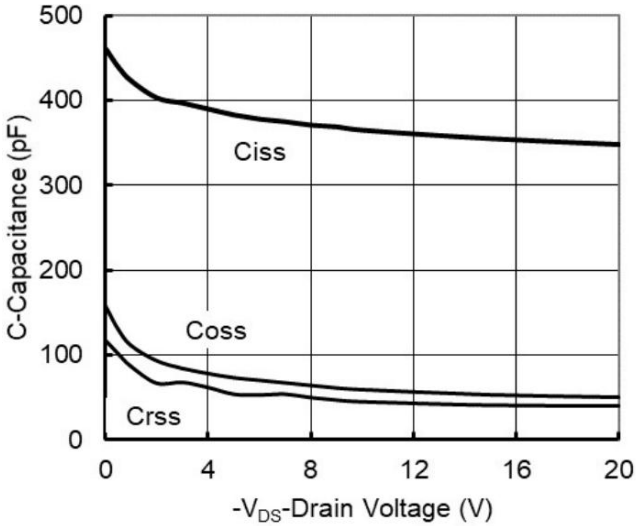


Figure5. Capacitance Characteristics

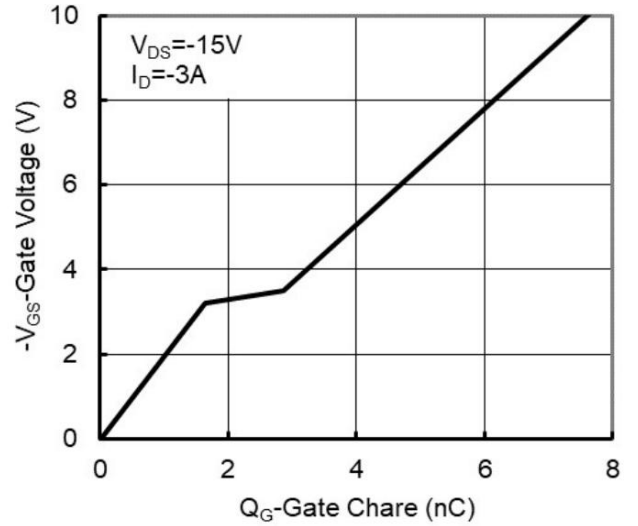


Figure6. Gate Charge

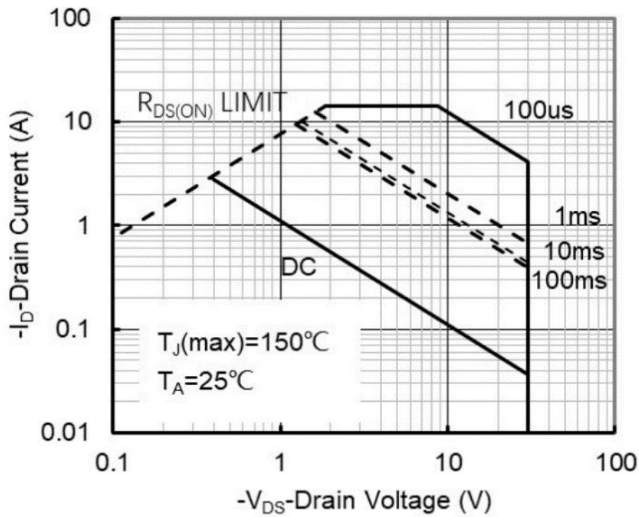


Figure7. Safe Operation Area

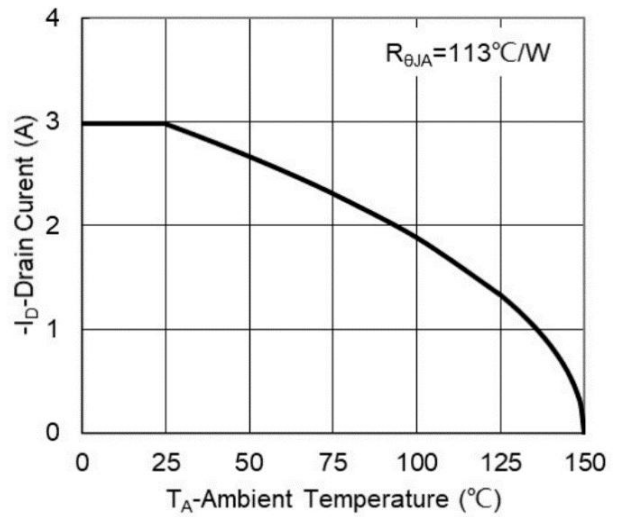


Figure8. Maximum Continuous Drain Current vs Ambient Temperature

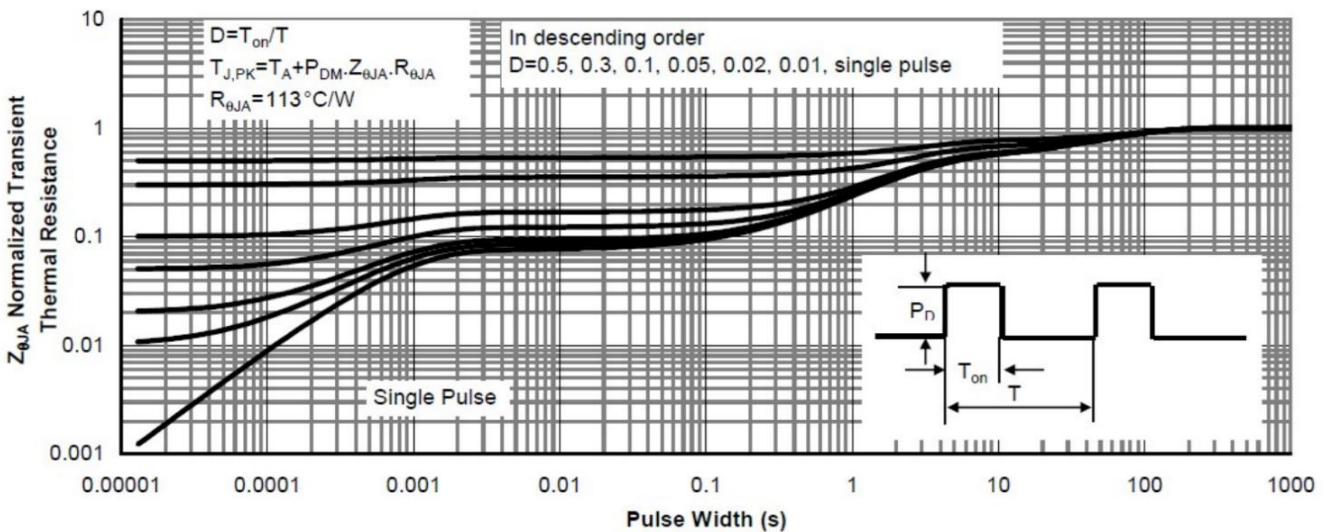
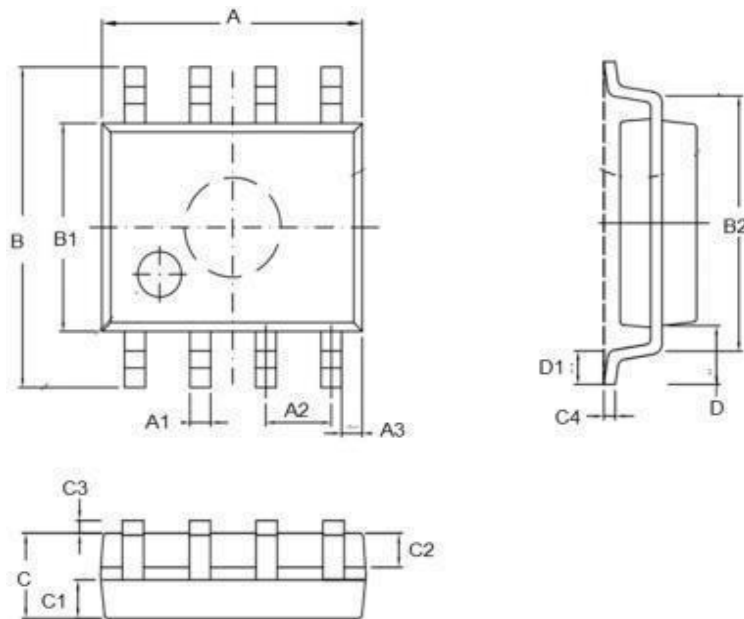


Figure9. Normalized Maximum Transient Thermal Impedance

•Dimensions(SOP8)

SYMBOL	min	TYP	max	SYMBOL	min		max
A	4.80		5.00	C	1.30		1.50
A1	0.37		0.47	C1	0.55		0.75
A2		1.27		C2	0.55		0.65
A3		0.41		C3	0.05		0.20
B	5.80		6.20	C4	0.19	0.20	0.23
B1	3.80		4.00	D		1.05	
B2		5.00		D1	0.40		0.62




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