



OCXO Part No.: 0S400-1005-012

Issue 2; 11th May 2022

Features

- Temperature stability to ±5ppb
- Low phase noise
- Frequency 10.00MHz
- Industry standard package
- The flexible nature of the design means that variations to suit almost any application can be developed to meet individual customer requirements



Option A

- Temperature stability: ±5ppb over (0 to +50)°C
- Output: Sinewave 7dBm nominal Harmonics: -20dBc Voltage: 5.0V Warm Up Current: 470mA Quiescent current: 220mA

Phase Noise (typical)

F0₀+10Hz -130 dBc/Hz F0₀+100Hz -155 dBc/Hz F0₀+1KHz -165 dBc/Hz F0₀+10KHz -170 dBc/Hz F0₀+100KHz -172 dBc/Hz Values based on a 10MHz unit

Voltage / Load change

- ±5% supply voltage change: ±2ppb
- ±10% load change: ±10ppb

Ageing

Bases on 10MHz unit after 30 days continuous operation:

- Per day: ±0.7ppb max. Per year: ±200ppb max.
- Warm up time: 5 minutes to within 0.1ppm

Voltage Trim

- ±0.5ppm minimum
- Trim impedance $50K\Omega$

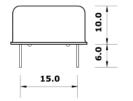
Reference Options

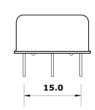
3.0V

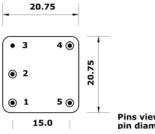
Environmental

- Electrostatic-Sensitive Device (ESD)
- Storage Temperature Range: (-40 to +125)°C

Dimensions (mm)





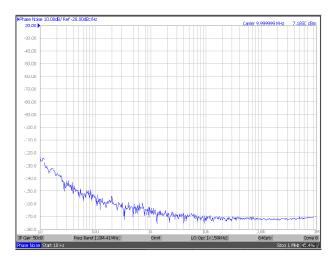


PIN CONNECTIONS #1: Vcc #2: Output #3: Ground/Case

Trim/Tune #5: Vref if fitted

Pins viewed from the bottom pin diameter 0.45 mm

Phase Noise Plot



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- Mechanical shock: MIL standard 202F, method 213, condition J
- Thermal shock: MIL standard 202F, method 107, condition A
- Vibration: MIL standard 202F, method 204, condition B
- Solderability: 5 seconds maximum at 230°C
- 3 seconds maximum at 350°C

Compliance

- RoHS Status (2011/65/EU) Compliant
- **REACH Status Compliant**

Packaging

■ Pack Style: Bulk

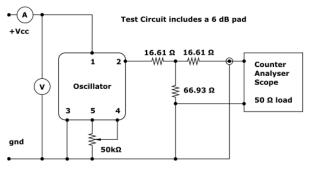
Ordering Information

OCXO Part No.: 0S400-1005-012

Frequency: 10.00MHz

Stability/Output/Voltage: Option A

Test Circuit - Sinewave



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