

REAL TIME CLOCK MODULE (SPI-Bus)

Built-in 32.768 kHz-DTCXO, +105°C operating temperature, Low current consumption, Built-in power supply switching circuit and Time stamp function up to 32 records

RX4901CE

Interface Type
 Current consumption
 3 wire / 4 wire SPI-Bus
 240 nA / 3 V (Typ.)

•Auto power switching function : Automatically switches to backup power supply

by monitoring the VDD / VBAT voltage

• Time stamp function : Maximum 32 time stamps

• Interrupt output : Wake up every hour or every minute or every second

• Alarm interruption : Day, date, hour, minute, second

• Auto repeat wakeup timer interruption

ullet Self-monitoring interruption : Crystal oscillation stop, V_{BAT} low, V_{DD} low

Pb Free



Product Number (2,000 pcs / Reel)

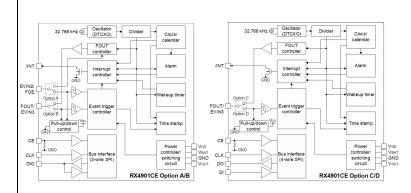
RX4901CE XS A0: X1B000471000115 RX4901CE XB A0: X1B000471000215 RX4901CE XS B0: X1B000471000315 RX4901CE XB B0: X1B000471000415 RX4901CE XS C0: X1B000471000515 RX4901CE XB C0: X1B000471000615 RX4901CE XS D0: X1B000471000715 RX4901CE XB D0: X1B000471000815



RX4901CE

 $(3.2 \times 2.5 \text{ mm}, t = 1.0 \text{ mm Max.})$

Block diagram



Overview

- Interface type : 3 wire / 4 wire SPI-Bus
- High stability

XS: $\pm 3.0 \times 10^{-6}$ / -40 °C to +85 °C (Monthly rate: ± 8 seconds)

: $\pm 5.0 \times 10^{-6}$ / +85 °C to +105 °C (Monthly rate: ± 13.2 seconds) XB : $\pm 5.0 \times 10^{-6}$ / -40 °C to +85 °C (Monthly rate: ± 13.2 seconds)

: ±8.0 x 10⁻⁶ / +85 °C to +105 °C (Monthly rate: ±21 seconds)

Time stamp function

Trigger source: External event (EVIN) input, voltage drop/oscillation stop status detected, command input from the host Record data: 1/1024 seconds to 1 second, seconds, minutes, hours,

Number of recordable events: Maximum 32 events

• Backup power supply switching function

The VDD and VBAT voltages are monitored to switch between Normal mode (VDD operation) and Backup mode (VBAT operation).

Clock output (FOUT)

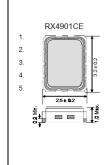
days, months, years

Selectable from 32.768 kHz, 1024 Hz and 1 Hz outputs Output can be controlled by a register or FOE input (selectable with a register).

Pin Function

Signal Name	1/0	Function			
EVIN1,2,3	Input	External event input pins. Detectable even in Backup mode. Pull-up and pull-down is configurable by the resisters			
CE	Input	Slave select input pin A pull-down resistor (Typ. 300 kΩ) is included			
CLK					
DI	Input	Serial data input pin (4 wire)			
DO	Output	Serial data Output pin (4 wire)			
DIO	Input / Output	Serial data input/output pin (3 wire)			
FOUT	Output	Frequency output pin (CMOS). 32.768 kHz (default), 1024 Hz or 1 Hz clock output is selectable. This pin can be switched to the wakeup timer interrupt output (CMOS)			
/INT	Output	Interrupt output pin (N-ch. open drain). The wakeup timer, time update, alarm, and/or event detection interrupt signals can be selected to output from this pin. When two or more signals are selected, they are NORed before being output. This pin is effective even in Backup mode.			
VDD	-	Power-supply pin			
Vout		Internal operating voltage output pin Connect a 1.0 µF bypass capacitor to this pin.			
VBAT	-	Backup power supply pin Connect a backup power supply such as a large-size capacitor, secondary battery, or primary battery. The operating power voltage is supplied from this pin to the internal circuits in Backup mode.			
GND	-	Ground pin			

Terminal connection / External dimensions (Unit: mm)



		Connection					
0.	Pin	Opt. A	Opt. B	Opt. C	Opt. D		
		3 w	/ire	4 wire			
	1	VDD					
-	2	Vouт					
i	3	VBAT					
	4	FOUT	EVIN3				
	5	CLK					
	6	CE					
	7	D	10	DO			
	8	/INT GND					
	9						
	10	EV	IN2	DI			

Specifications (characteristics)

■ Recommended Operating Conditions Symbol Condition Min. Max Item Тур unit Operating voltage VDD 3.0 5.5 Clock supply voltage VCLK Operating Temperature Ta -40 +25 +105 Vpp detection voltage -Vpet1 1.45

■ Frequency Characteristics

Item	Symbol		Condition	Min.	Тур.	Max.	unit
		XS	Ta = -40 to +85 °C	-3	-	+3	10-6
Frequency			Ta = +85 to +105 °C	-5	-	+5	
tolerance	Δf/f	VD	XB Ta = -40 to +85 °C -5	-	+5	× 10 ⁻⁶	
		ΛD	Ta = +85 to +105 °C	-8	-	+8	
start-up time	ne tsta	Ta = +25 °C, VDD = 1.6 V \sim 5.5 V		- 0.5	1.0	s	
ctart up time	LOIA				0.0	1.0	3

* Refer to application manual for details

■ Cui	a = -40 °C to +105 °C					
Item	Symbol	Condition	Min.	Тур.	Max.	unit
IDD	Іват	VBAT = 3.0 V, /INT= Hi-Z, FOUT: Output OFF (Hi-Z), Temperature compensation interval: 2 s, FSEL1= FSEL0 = 1, INIEN = 1, CHGEN = 0, CE = L	-	240	1500	nA
	32k	VDD = 3.0 V, /INT= Hi-Z, FOUT: 32 kHz output, C _L = 0 pF, Temperature compensation interval: 2 s, FSEL1 = FSEL0 = 0, INIEN = 1, CHGEN = 0, CE = L		1.0	3.0	μА

■ Option

I/F	Option	EVIN pin Number	/INTpin Number	FOUT	Number of time stamps recorded by EVIN terminal trigge		
					FIFO Mode	Direct Mode	
SPI	Α	1	1	Yes	32 times	12 times	
3 wire	В	2	1	-	32 times	22 times	
SPI	С	0	1	Yes	0 time	0 time	
4 wire	D	1	1	-	32 times	10 times	



Product name

 $\begin{array}{ccc} \underline{\mathsf{RX4901CE}} & \underline{\mathsf{XS}} & \underline{\mathsf{A0}} \\ \hline & \boxed{3} & \end{array}$

- ① Model CE type package 3.2 x 2.5 x 1.0 mm
- 2 Frequency tolerance

XS: $\pm 3.0 \times 10^{-6}$ / -40 °C to +85 °C (Monthly rate: ± 8 seconds)

 $\pm 5.0 \times 10^{-6} / +85$ °C to +105 °C (Monthly rate: ± 13.2 seconds)

XB: ± 5.0 x 10^{-6} / -40 °C to +85 °C (Monthly rate: ± 13.2 seconds)

±8.0 x 10⁻⁶ / +85 °C to +105 °C (Monthly rate: ±21 seconds)

3 Pin Option

A: Option A

B: Option B

C: Option C

D: Option D

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At Seiko Epson, all environmental initiatives operate under the Plan-Do-Check-Action (PDCA) cycle designed to achieve continuous improvements. The environmental management system (EMS) operates under the ISO 14001 environmental management standard.

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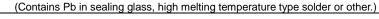
►Pb free.



► Complies with EU RoHS directive.

*About the products without the Pb-free mark.

Contains Pb in products exempted by EU RoHS directive.





▶ Designed for automotive applications such as Car Multimedia, Body Electronics, Remote Keyless Entry etc.



▶ Designed for automotive applications related to driving safety (Engine Control Unit, Air Bag, ESC etc).

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