

ARTESYN DS650/DS850

Distributed Power Bulk Front-End



Advanced Energy's Artesyn DS650 and DS850 series of bulk front end AC-DC power supplies offers a compact 1U x 2U solution for systems with distributed power architectures. Rated at 650 and 850 watts respectively, the power supplies provide a choice of 12 V, 24 V or 48 V output. Each model also provides a 3.3 V or optional 5.5 V standby output. DS650 and DS850 series power supplies accept a wide 90–264 Vac input and have a typical conversion efficiency of 82%. Standard features include active current sharing, internal ORing FETs and an EEPROM for storing service data to facilitate efficient field replacement. An I2C communication interface is provided for the FRU EEPROM data.

■ EEPROM for FRU data

One year warranty

SAFETY

■ EN60950

■ CE Mark

■ China CCC

Internal fan speed control

■ Fan Fail Tach output signal

■ Amber/Green bi-color LED status

■ UL/cUL 60950 (UL Recognized)

■ NEMKO+ CB Report EN60950

SPECIAL FEATURES

- Active power factor correction
- EN61000-3-2 harmonic compliance
- Inrush control
- 1U X 2U form factor
- 15.4 W/ in³
- 12 Vdc, 24 Vdc and 48 Vdc output
- Available in +3.3 V and +5.0 V standby output versions
- No minimum load required
- Hot plug operation
- N + 1 redundant
- Internal OR'ing fets
- Active current sharing (10 - 100% load)
- Built-in cooling fans (40 mm x 28 mm)
- I²C communication interface bus

DATA SHEET

650/850 Watts

Total Input

Power:

+3.3 Vdc Standby Output

Wide Range

90 - 264 Vac

Output Voltage:

12, 24 and 48 V

ELECTRICAL SPECIFICATIONS

Input	
Input range	90 - 264 Vac (wide range)
Frequency	47 - 63 Hz, single phase AC
Inrush current	55 A maximum inrush current
Efficiency	> 82% typical at full load, high line
Conducted EMI	FCC Subpart J EN55022 Class B
Radiated EMI	FCC Subpart J EN55022 Class B
Power factor	0.99 typical
Leakage current	1.40 mA @ 240 Vac
Hold up time	20 ms minimum
Output	
Main DC voltage	+12 V @ 52.5 A/70.0 A +24 V @ 26.3 A/35.0 A +48 V @ 13.1 A/17.5 A
Standby	+3.3 vsb @ 6 A (5 V @ 4 A available)
Adjustment range	Factory Set, no pot adjustments
Regulation	Main output; +5%/-5% +3.3 vsb; +5%/-5%
Overcurrent	110% - 150% of nominal Latches off if overcurrent lasts over 1 second, otherwise it is auto recovery. +3.3 vsb, 9 A max (hiccup mode)
Overvoltage	110% - 120% of nominal +3.3 vsb; 3.76 - 4.30 Vdc
Undervoltage	75% - 90% of nominal
Turn-on delay	2 Second max, 5 - 50 mS, Monotonic Rise
Main output rise time	5 - 50 mS, Monotonic Rise

LOGIC CONTROL

PS_SEATED	TTL logic LOW if power supply is seated into system connector. This is a short pin. A logic HIGH if the PSU is removed.
PWR GOOD	Active TTL HiIGH when output is within regulation limits.
AC OK	A LOW logic level if the input voltage is within allowable limits. A TTL logic HIGH level, and a 5mS early warningsignal before main output loss of regulation.
Temp OK	A TTL logic HIGH, when operating within allowable temperature range.
PS_INHIBIT/PS_KILL	This signal is connected to a short pin on the PSU When left open power supply operation will be inhibited. When the power supply is inserted into the system, this pin will be pull low by the system and turn the power supply on only after all other power supply pins have seated.



ENVIRONMENTAL SPECIFICATIONS

Operating temperature:	0 to 50 °C, unimpeded airflow	
Storage temperature:	-40 °C to +85 °C	
Altitude, operating 10,000ft.		
Electromagnetic susceptibility/Input transients:	-EN61000-3-2, -3-3 -EN61000-4-2, 4.3, 4-4, -4-5, 4-11 -EN55024: 1998	
RoHS & lead-free compliant (no tantalum caps.)		
Humidity:	20 to 90% RH, non-condensing	
Shock and vibration specificatons complies with Astec Std. Specifications.		
MTBF (Demonstrated)	500K Hrs at full load, 40 °C	

ORDERING INFORMATION

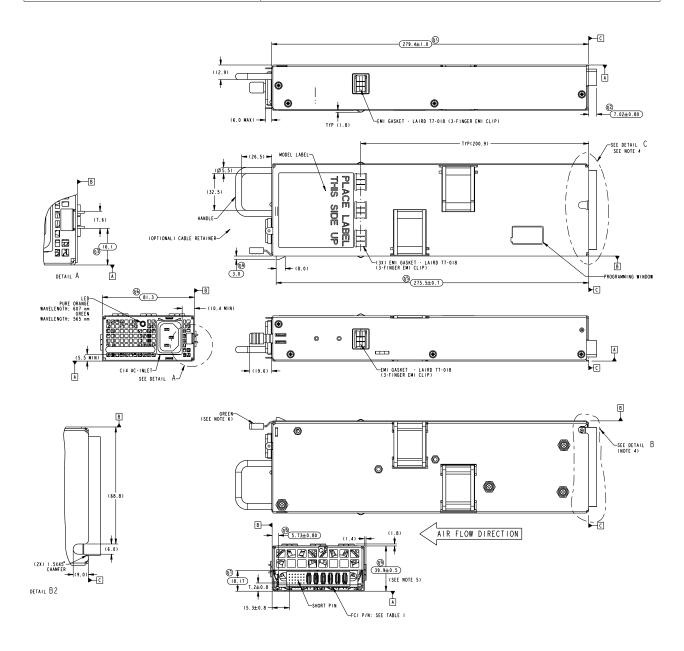
Output	Nominal Output Voltage Set Point	Set Point Tolerance**	Total Regulation	Minimum Current	Maximum Current	Output Ripple P/P
DS650-3	12.0 Vdc	±0.2%	±5%	0 A	52.5 A	120 mV
	3.3 vsb*	±1%	±5%	0 A	6.0 A	50 mV
DS650-5	24.0 Vdc	±0.2%	±5%	0 A	26.3 A	240 mV
	3.3 vsb*	±1%	±5%	0 A	6.0 A	50 mV
DS650-9	48.0 Vdc	±0.2%	±5%	0 A	13.1 A	480 mV
	3.3 vsb*	±1%	±5%	0 A	6.0 A	50 mV
DS850-3	12.0 Vdc	±0.2%	±5%	0 A	70.0 A	120 mV
	3.3 vsb*	±1%	±5%	0 A	6.0 A	50 mV
DS850-5	24.0 Vdc	±0.2%	±5%	0 A	35.0 A	240 mV
	3.3 vsb*	±1%	±5%	0 A	6.0 A	50 mV
DS850-9	48.0 Vdc	±0.2%	±5%	0 A	17.5 A	480 mV
	3.3 vsb*	±1%	±5%	0 A	6.0 A	50 mV



^{*}For 5 vsb, please contact marketing department.
**Set point tolerance is measured at nominal voltage, 50% load and room temperature.

MECHANICAL DRAWING

Power Supply Condition	LED Green/Amber
No AC power to all PSU	OFF
AC present/Standby outpus ON, Main output OFF	Blinking Green
Power supply DC outputs ON and OK	Solid Green
Main output failure (OCP, OVP, UVP)	Blinking Amber
Fan Fail, OTP, Standby output OCP/UVP	Solid Amber



DC OUTPUT CONNECTOR PINOUT ASSIGNMENT

Male co	Male connector as viewed from the rear of the supply:										
D1	D2	D3	D4	D5	D6						
C1	C2	C3	C4	C5	C6	DD1	DDO	DDO	DD4	DDE	DDG
B1	B2	В3	B4	B5	В6	PB1	PB2	PB3	PB4	PB5	PB6
A1	A2	А3	A4	A5	A6						

P1 - POWER SUPPLY SIDE

1	FCI Power Blade 51721 series 51721-10002406AA
	Molex Power Connector
	SD-87667 series 87667-7002

MATING CONNECTOR (SYSTEM SIDE)

1	FCI Power Blade 51741-10002406CC Strait Pins
2	FCI Power Blade 51761-10002406AA Right Angle



PIN ASSIGNMENTS

P8 1	Pin	Signal Name
PB 3 MAIN O/P PB 4 + MAIN O/P PB 5 + MAIN O/P PB 6 + MAIN O/P A1 PS_ON A2 MAIN O/P V RMT SENSE RETURN A3 TEMP_OK A4 PS_SEATED (Power Supply Seated) A5 +393 STAND-BY A6 +303SB RETURN B1 AC_OK (AC Input Present) B2 MAIN O/P RMT SENSE B3 MAIN O/P CURRENT SHARE B4 PS_INHIBIT B5 +393SB RETURN B6 +393SB RETURN C1 SDA (I°C Data Signal) C2 SCL (I°C Clock Signal)* C3 POWER GOOD C4 FAN FAIL (Fan Fail Signal) C5 +393S RETURN D1 A0 (I°C Address BIT O Signal) C5 +394S STAND-BY D1 A0 (I°C Address BIT O Signal) D2 A1 (I°C Address BIT O Signal) D3 S_INT (Alarm) D4 +394S STAND-BY	PB 1	MAIN O/P RETURN
PB 4 + MAIN O/P PB 5 + MAIN O/P PB 6 + MAIN O/P A1 PS_ON A2 MAIN O/P V RMT SENSE RETURN A3 TEMP_OK A4 PS_SEATED (Power Supply Seated) A5 +3V3 STAND-BY A6 +3V3SS RETURN B1 AC_OK (AC Input Present) B2 MAIN O/P RMT SENSE B3 MAIN O/P CURRENT SHARE B4 PS_INHIBIT B5 +3V3 STAND-BY B6 +3V3SS RETURN C1 SDA (I°C Data Signal) C2 SCL (I°C Clock Signal)* C3 POWER GOOD C4 FAN FAIL (Fan Fail Signal) C5 +3V3 STAND-BY C6 +3V3S RETURN D1 A0 (I°C Address BIT 0 Signal) D2 A1 (I°C Address BIT 1 Signal) D3 S_INT (Alarm) D4 +3V3 STAND-BY RMT SENSE D5 +3V3 STAND-BY RMT SENSE	PB 2	MAIN O/P RETURN
PB 5	PB 3	MAIN O/P RETURN
PB 6	PB 4	+ MAIN O/P
A1	PB 5	+ MAIN O/P
A2 MAIN O/P V RMT SENSE RETURN A3 TEMP_OK A4 PS_SEATED (Power Supply Seated) A5 +3V3 STAND-BY A6 +3V3SB RETURN B1 AC_OK (AC Input Present) B2 MAIN O/P RMT SENSE B3 MAIN O/P CURRENT SHARE B4 PS_INHIBIT B5 +3V3 STAND-BY B6 +3V3SB RETURN C1 SDA (PC Data Signal) C2 SCL (PC Clock Signal)* C3 POWER GOOD C4 FAN FAIL (Fan Fail Signal) C5 +3V3 STAND-BY C6 +3V3SB RETURN D1 A0 (PC Address BIT 0 Signal) D2 A1 (PC Address BIT 1 Signal) D3 S_INT (Alarm) D4 +3V3 STAND-BY RMT SENSE D5 +3V3 STAND-BY	PB 6	+ MAIN O/P
A3	A1	PS_ON
A4 PS_SEATED (Power Supply Seated) A5 +3V3 STAND-BY A6 +3V3SB RETURN B1 AC_OK (AC Input Present) B2 MAIN O/P RMT SENSE B3 MAIN O/P CURRENT SHARE B4 PS_INHIBIT B5 +3V3 STAND-BY B6 +3V3SB RETURN C1 SDA (I°C Data Signal) C2 SCL (I°C Clock Signal)* C3 POWER GOOD C4 FAN FAIL (Fan Fail Signal) C5 +3V3 STAND-BY C6 +3V3SB RETURN D1 A0 (I°C Address BIT 0 Signal) D2 A1 (I°C Address BIT 1 Signal) D3 S_INT (Alarm) D4 +3V3 STAND-BY RMT SENSE D5 +3V3 STAND-BY RMT SENSE	A2	MAIN O/P V RMT SENSE RETURN
A5 +3V3 STAND-BY A6 +3V3SB RETURN B1 AC_OK (AC Input Present) B2 MAIN O/P RMT SENSE B3 MAIN O/P CURRENT SHARE B4 PS_INHIBIT B5 +3V3 STAND-BY B6 +3V3SB RETURN C1 SDA (I°C Data Signal) C2 SCL (I°C Clock Signal)* C3 POWER GOOD C4 FAN FAIL (Fan Fail Signal) C5 +3V3 STAND-BY C6 +3V3SB RETURN D1 A0 (I°C Address BIT 0 Signal) D2 A1 (I°C Address BIT 1 Signal) D3 S_INT (Alarm) D4 +3V3 STAND-BY RMT SENSE D6 +3V3 STAND-BY	A3	TEMP_OK
### A66 ###############################	A4	PS_SEATED (Power Supply Seated)
B1	A5	+3V3 STAND-BY
B2 MAIN O/P RMT SENSE B3 MAIN O/P CURRENT SHARE B4 PS_INHIBIT B5 +3V3 STAND-BY B6 +3V3SB RETURN C1 SDA (I°C Data Signal) C2 SCL (I°C Clock Signal)* C3 POWER GOOD C4 FAN FAIL (Fan Fail Signal) C5 +3V3 STAND-BY C6 +3V3SB RETURN D1 A0 (I°C Address BIT 0 Signal) D2 A1 (I°C Address BIT 1 Signal) D3 S_INT (Alarm) D4 +3V3 STAND-BY RMT SENSE D5 +3V3 STAND-BY	A6	+3V3SB RETURN
B3 MAIN O/P CURRENT SHARE B4 PS_INHIBIT B5 +3V3 STAND-BY B6 +3V3SB RETURN C1 SDA (I°C Data Signal) C2 SCL (I°C Clock Signal)* C3 POWER GOOD C4 FAN FAIL (Fan Fail Signal) C5 +3V3 STAND-BY C6 +3V3SB RETURN D1 A0 (I°C Address BIT 0 Signal) D2 A1 (I°C Address BIT 1 Signal) D3 S_INT (Alarm) D4 +3V3 STAND-BY RMT SENSE D5 +3V3 STAND-BY	B1	AC_OK (AC Input Present)
B4 PS_INHIBIT B5 +3V3 STAND-BY B6 +3V3SB RETURN C1 SDA (I°C Data Signal) C2 SCL (I°C Clock Signal)* C3 POWER GOOD C4 FAN FAIL (Fan Fail Signal) C5 +3V3 STAND-BY C6 +3V3SB RETURN D1 A0 (I°C Address BIT 0 Signal) D2 A1 (I°C Address BIT 1 Signal) D3 S_INT (Alarm) D4 +3V3 STAND-BY RMT SENSE D5 +3V3 STAND-BY	B2	MAIN O/P RMT SENSE
B5	B3	MAIN O/P CURRENT SHARE
### ##################################	B4	PS_INHIBIT
C1 SDA (I²C Data Signal) C2 SCL (I²C Clock Signal)* C3 POWER GOOD C4 FAN FAIL (Fan Fail Signal) C5 +3V3 STAND-BY C6 +3V3SB RETURN D1 A0 (I²C Address BIT 0 Signal) D2 A1 (I²C Address BIT 1 Signal) D3 S_INT (Alarm) D4 +3V3 STAND-BY RMT SENSE D5 +3V3 STAND-BY	B5	+3V3 STAND-BY
C2 SCL (I²C Clock Signal)* C3 POWER GOOD C4 FAN FAIL (Fan Fail Signal) C5 +3V3 STAND-BY C6 +3V3SB RETURN D1 A0 (I²C Address BIT 0 Signal) D2 A1 (I²C Address BIT 1 Signal) D3 S_INT (Alarm) D4 +3V3 STAND-BY RMT SENSE D5 +3V3 STAND-BY	B6	+3V3SB RETURN
C3 POWER GOOD C4 FAN FAIL (Fan Fail Signal) C5 +3V3 STAND-BY C6 +3V3SB RETURN D1 A0 (I²C Address BIT 0 Signal) D2 A1 (I²C Address BIT 1 Signal) D3 S_INT (Alarm) D4 +3V3 STAND-BY RMT SENSE D5 +3V3 STAND-BY	C1	SDA (I ² C Data Signal)
C4 FAN FAIL (Fan Fail Signal) C5 +3V3 STAND-BY C6 +3V3SB RETURN D1 A0 (I²C Address BIT 0 Signal) D2 A1 (I²C Address BIT 1 Signal) D3 S_INT (Alarm) D4 +3V3 STAND-BY RMT SENSE D5 +3V3 STAND-BY	C2	SCL (I ² C Clock Signal)*
C5 +3V3 STAND-BY C6 +3V3SB RETURN D1 A0 (I²C Address BIT 0 Signal) D2 A1 (I²C Address BIT 1 Signal) D3 S_INT (Alarm) D4 +3V3 STAND-BY RMT SENSE D5 +3V3 STAND-BY	C3	POWER GOOD
C6 +3V3SB RETURN D1 A0 (I²C Address BIT 0 Signal) D2 A1 (I²C Address BIT 1 Signal) D3 S_INT (Alarm) D4 +3V3 STAND-BY RMT SENSE D5 +3V3 STAND-BY	C4	FAN FAIL (Fan Fail Signal)
D1 A0 (I²C Address BIT 0 Signal) D2 A1 (I²C Address BIT 1 Signal) D3 S_INT (Alarm) D4 +3V3 STAND-BY RMT SENSE D5 +3V3 STAND-BY	C5	+3V3 STAND-BY
D2 A1 (I²C Address BIT 1 Signal) D3 S_INT (Alarm) D4 +3V3 STAND-BY RMT SENSE D5 +3V3 STAND-BY	C6	+3V3SB RETURN
D3 S_INT (Alarm) D4 +3V3 STAND-BY RMT SENSE D5 +3V3 STAND-BY	D1	A0 (I ² C Address BIT 0 Signal)
D4 +3V3 STAND-BY RMT SENSE D5 +3V3 STAND-BY	D2	A1 (I ² C Address BIT 1 Signal)
D5 +3V3 STAND-BY	D3	S_INT (Alarm)
	D4	+3V3 STAND-BY RMT SENSE
D6 +3V3SB RETURN	D5	+3V3 STAND-BY
	D6	+3V3SB RETURN

^{*}Supports I²C standard mode (100 kHz) only





ABOUT ADVANCED ENERGY

Advanced Energy (AE) has devoted more than three decades to perfecting power for its global customers. AE designs and manufactures highly engineered, precision power conversion, measurement and control solutions for mission-critical applications and processes.

Our products enable customer innovation in complex applications for a wide range of industries including semiconductor equipment, industrial, manufacturing, telecommunications, data center computing, and medical. With deep applications know-how and responsive service and support across the globe, we build collaborative partnerships to meet rapid technological developments, propel growth for our customers, and innovate the future of power.

PRECISION | POWER | PERFORMANCE

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For international contact information, visit advancedenergy.com.

powersales@aei.com (Sales Support) productsupport.ep@aei.com (Technical Support) +1 888 412 7832