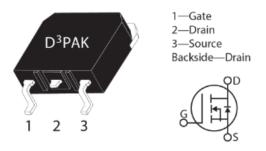


MSC750SMA170S Silicon Carbide N-Channel Power MOSFET

Product Overview

The silicon carbide (SiC) power MOSFET product line from Microsemi increases the performance over silicon MOSFET and silicon IGBT solutions while lowering the total cost of ownership for high-voltage applications. The MSC750SMA170S device is a 1700 V, 750 m Ω SiC MOSFET in a TO-268 (D3PAK) package.



Features

The following are key features of the MSC750SMA170S device:

- Low capacitances and low gate charge
- Fast switching speed due to low internal gate resistance (ESR)
- Stable operation at high junction temperature, T_{J(max)} = 175 °C
- · Fast and reliable body diode
- Superior avalanche ruggedness
- RoHS compliant

Benefits

The following are benefits of the MSC750SMA170S device:

- High efficiency to enable lighter, more compact system
- Simple to drive and easy to parallel
- Improved thermal capabilities and lower switching losses
- Eliminates the need for external freewheeling diode
- Lower system cost of ownership

Applications

The MSC750SMA170S device is designed for the following applications:

- PV inverter, converter, and industrial motor drives
- Smart grid transmission and distribution
- · Induction heating and welding
- H/EV powertrain and EV charger
- Power supply and distribution



Device Specifications

This section shows the specifications of the MSC750SMA170S device.

Absolute Maximum Ratings

The following table shows the absolute maximum ratings of the MSC750SMA170S device.

Table 1 • Absolute Maximum Ratings

Symbol	Characteristic	Ratings	Unit
V _{DSS}	Drain source voltage	1700	V
I _D	Continuous drain current at T _C = 25 °C	6	А
	Continuous drain current at T _C = 100 °C	4	
I _{DM}	Pulsed drain current ¹	12	
V _{GS}	Gate-source voltage	23 to -10	V
P _D	Total power dissipation at T _C = 25 °C	63	W
	Linear derating factor	0.42	W/°C

Note:

1. Repetitive rating: pulse width and case temperature limited by maximum junction temperature.

The following table shows the thermal and mechanical characteristics of the MSC750SMA170S device.

Table 2 • Thermal and Mechanical Characteristics

Symbol	Characteristic	Min	Тур	Max	Unit
$R_{\theta JC}$	Junction-to-case thermal resistance		1.6	2.39	°C/W
T _J	Operating junction temperature	- 55		175	°C
T _{STG}	Storage temperature	- 55		150	
T _L	Soldering temperature for 10 seconds (1.6 mm from case)			300	
Wt	Package weight		0.14		OZ
			4.0		g



Electrical Performance

The following table shows the static characteristics of the MSC750SMA170S device. T_J = 25 °C unless otherwise specified.

Table 3 • Static Characteristics

Symbol	Characteristic	Test Conditions	Min	Тур	Max	Unit
V _{(BR)DSS}	Drain-source breakdown voltage	V_{GS} = 0 V, I $_{D}$ = 100 μA	1700			V
R _{DS(on)}	Drain-source on resistance ¹	V _{GS} = 20 V, I _D = 2.5 A		750	940	mΩ
V _{GS(th)}	Gate-source threshold voltage	$V_{GS} = V_{DS}, I_{D} = 100 \mu A$	1.9	3.25		V
$\Delta V_{GS(th)}/\Delta T_J$	Threshold voltage coefficient	$V_{GS} = V_{DS}, I_{D} = 100 \mu A$		-5.7		mV/°C
I _{DSS}	Zero gate voltage drain current	V _{DS} = 1700 V, V _{GS} = 0 V			100	μА
		V _{DS} = 1700 V, V _{GS} = 0 V T _J = 125 °C			500	
I _{GSS}	Gate-source leakage current	V _{GS} = 20 V/–10 V			±100	nA

Note:

1. Pulse test: pulse width $< 380 \mu s$, duty cycle < 2%.

The following table shows the dynamic characteristics of the MSC750SMA170S device. $T_J = 25$ °C unless otherwise specified.

Table 4 • Dynamic Characteristics

Symbol	Characteristic	Test Conditions	Min	Тур	Max	Unit
C _{iss}	Input capacitance	$V_{GS} = 0 \text{ V}, V_{DD} = 1000 \text{ V}$ $V_{AC} = 25 \text{ mV}, f = 1 \text{ MHz}$		184		pF
C _{rss}	Reverse transfer capacitance	AC 25/		2		
C _{oss}	Output capacitance			14		
Q _g	Total gate charge	$V_{GS} = -5 \text{ V/20 V}, V_{DD} = 850 \text{ V}$ $I_D = 2.5 \text{ A}$		11		nC
Q _{gs}	Gate-source charge			2.9		
Q _{gd}	Gate-drain charge			2.1		
t _{d(on)}	Turn-on delay time	$V_{DD} = 1200 \text{ V}, V_{GS} = -5 \text{ V}/20 \text{ V}$ $I_D = 5 \text{ A}, R_{G(ext)} = 8 \Omega,$		13		ns
t _f	Voltage fall time	Freewheeling diode = MSC750SMA170S (Vg = -5 V)		12		
t _{d(off)}	Turn-off delay time	INISCI 2003INIMITIOS (AB2 A)		7		



Symbol	Characteristic	Test Conditions	Min	Тур	Max	Unit
t _r	Voltage rise time			8		
E _{on}	Turn-on switching energy			107		μ
E _{off}	Turn-off switching energy			17		
t _{d(on)}	Turn-on delay time	$V_{DD} = 1200 \text{ V}, V_{GS} = -5 \text{ V}/20 \text{ V}$ $I_D = 5 \text{ A}, R_{G(ext)} = 8 \Omega, T_J = 150 ^{\circ}\text{C}$		13		ns
t _f	Voltage fall time	Freewheeling diode = MSC750SMA170S		12		
t _{d(off)}	Turn-off delay time	WISC/SUSIVIAT/US		7		
t _r	Voltage rise time			8		
E _{on}	Turn-on switching energy			185		μЈ
E _{off}	Turn-off switching energy			20		
ESR	Equivalent series resistance	f = 1 MHz, 25 mV, drain short		2.89		Ω
SCWT	Short circuit withstand time	V _{DS} = 1200 V, V _{GS} = 20 V		2.5		μs
E _{AS}	Avalanche energy, single pulse	$V_{DS} = 150 \text{ V}, V_{GS} = 20 \text{ V}, I_{D} = 2.5 \text{ A}$		360		mJ

The following table shows the body diode characteristics of the MSC750SMA170S device. T_J = 25 °C unless otherwise specified.

Table 5 • Body Diode Characteristics

Symbol	Characteristic	Test Conditions	Min	Тур	Max	Unit
V _{SD}	Diode forward voltage	$I_{SD} = 2.5 \text{ A, V}_{GS} = 0 \text{ V}$		3.7		V
		$I_{SD} = 2.5 \text{ A, } V_{GS} = -5 \text{ V}$		3.9		V
t _{rr}	Reverse recovery time	I _{SD} = 5 A, V _{GS} = -5 V,		18		ns
Q _{rr}	Reverse recovery charge	V_{DD} = 1200 V, dI/dt = -2000 A/ μ s Drive Rg = 8 Ω		120		nC
I _{RRM}	Reverse recovery current			3.0		А



Typical Performance Curves

This section shows the typical performance curves of the MSC750SMA170S device.

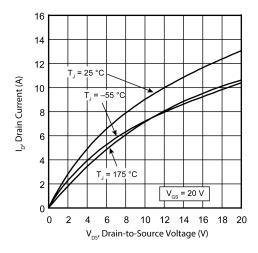


Figure 1 • Drain Current vs. V_{DS}

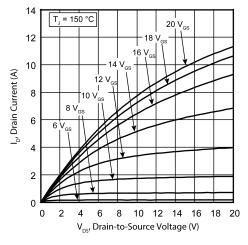


Figure 3 • Drain Current vs. V_{DS}

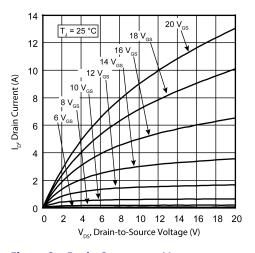


Figure 2 • Drain Current vs. V_{DS}

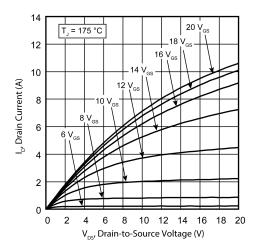


Figure 4 • Drain Current vs. V_{DS}



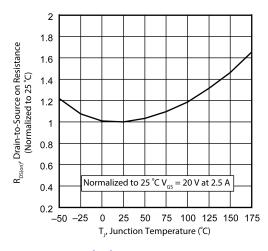


Figure 5 • RDS(on) vs. Junction Temperature

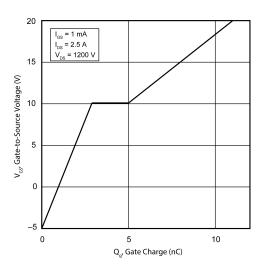


Figure 6 • Gate Charge Characteristics

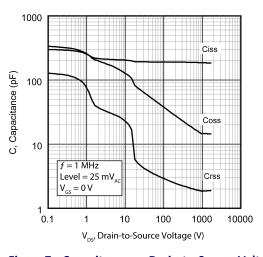


Figure 7 • Capacitance vs. Drain-to-Source Voltage

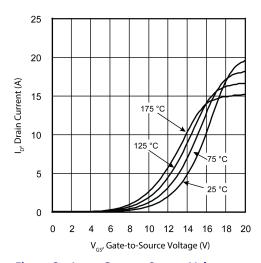


Figure 8 • I_D vs. Gate-to-Source Voltage



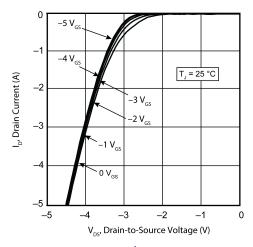


Figure 9 • I_D vs. V_{DS} 3rd Quadrant Conduction

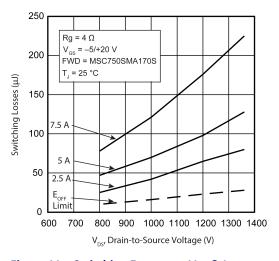


Figure 11 • Switching Energy vs. $V_{DS} \& I_{D}$

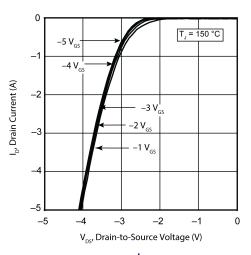


Figure 10 • I_D vs. V_{DS} 3rd Quadrant Conduction

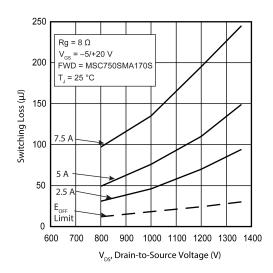


Figure 12 • Switching Energy vs. V_{DS} & I_D



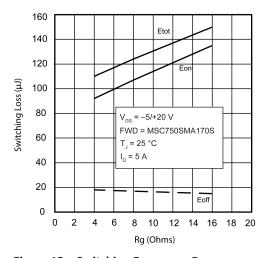


Figure 13 • Switching Energy vs. Rg

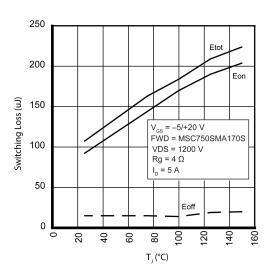


Figure 14 • Switching Energy vs. Temperature

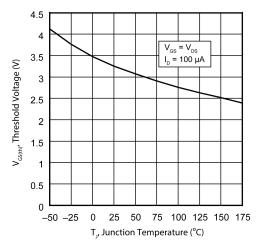


Figure 15 • Threshold Voltage vs. Junction Temp.

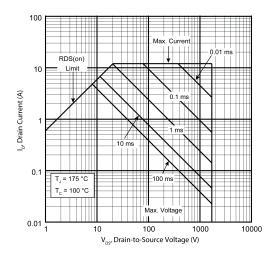


Figure 16 • Forward Safe Operating Area



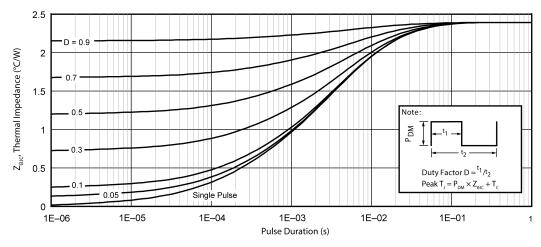


Figure 17 • Maximum Transient Thermal Impedance



Package Specification

This section shows the package specification of the MSC750SMA170S device.

Package Outline Drawing

The following figure illustrates the TO-268 package outline of the MSC750SMA170S device.

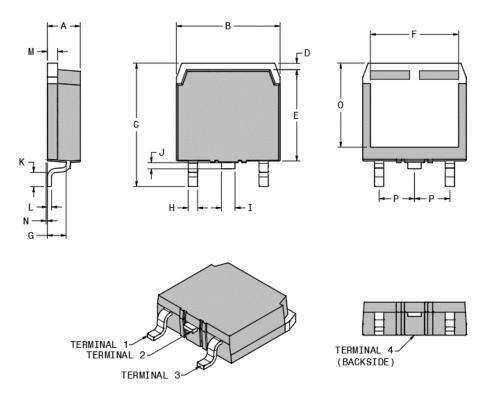


Figure 18 • Package Outline Drawing

The following table shows the TO-268 dimensions and should be used in conjunction with the package outline drawing.

Table 6 • TO-268 Dimensions

Symbol	Min (mm)	Max (mm)	Min (in.)	Max (in.)
А	4.90	5.10	0.193	0.201
В	15.85	16.20	0.624	0.638
С	18.70	19.10	0.736	0.752
D	1.00	1.25	0.039	0.049
Е	13.80	14.00	0.543	0.551
F	13.30	13.60	0.524	0.535



Symbol	Min (mm)	Max (mm)	Min (in.)	Max (in.)		
G	2.70	2.90	0.106	0.114		
Н	1.15	1.45	0.045	0.057		
1	1.95	2.21	0.077	0.087		
J	0.94	1.40	0.037	0.055		
К	2.40	2.70	0.094	0.106		
L	0.40	0.60	0.016	0.024		
М	1.45	1.60	0.057	0.063		
N	0.00	0.18	0.000	0.007		
0	12.40	12.70	0.488	0.500		
Р	5.45 BSC (nom.)		0.215 BSC (nom.)			
Terminal 1	Gate					
Terminal 2	Drain					
Terminal 3	Source					
Terminal 4	Drain					





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