

To our customers,

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## Old Company Name in Catalogs and Other Documents

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# M62342GP

## 8-Bit, 2-Channel D/A Converter (Buffered)

REJ03F0074-0300

Rev.3.00

Mar 25, 2008

### Description

The M62342GP is a CMOS-structure semiconductor integrated circuit incorporating two 8-bit D/A converter channels with output buffer op-amps.

Serial data transfer type input can easily be used through a combination of three lines: DI, CLK, and LD.

Outputs incorporate buffer op-amps that have a drive capacity of 1 mA or above for both sink and source, and can operate over the entire voltage range from almost ground to  $V_{CC}$  (0 to 5 V), making peripheral elements unnecessary and enabling configuration of a system with few component parts.

This product is currently under development, and specifications and other details may be modified at a future date.

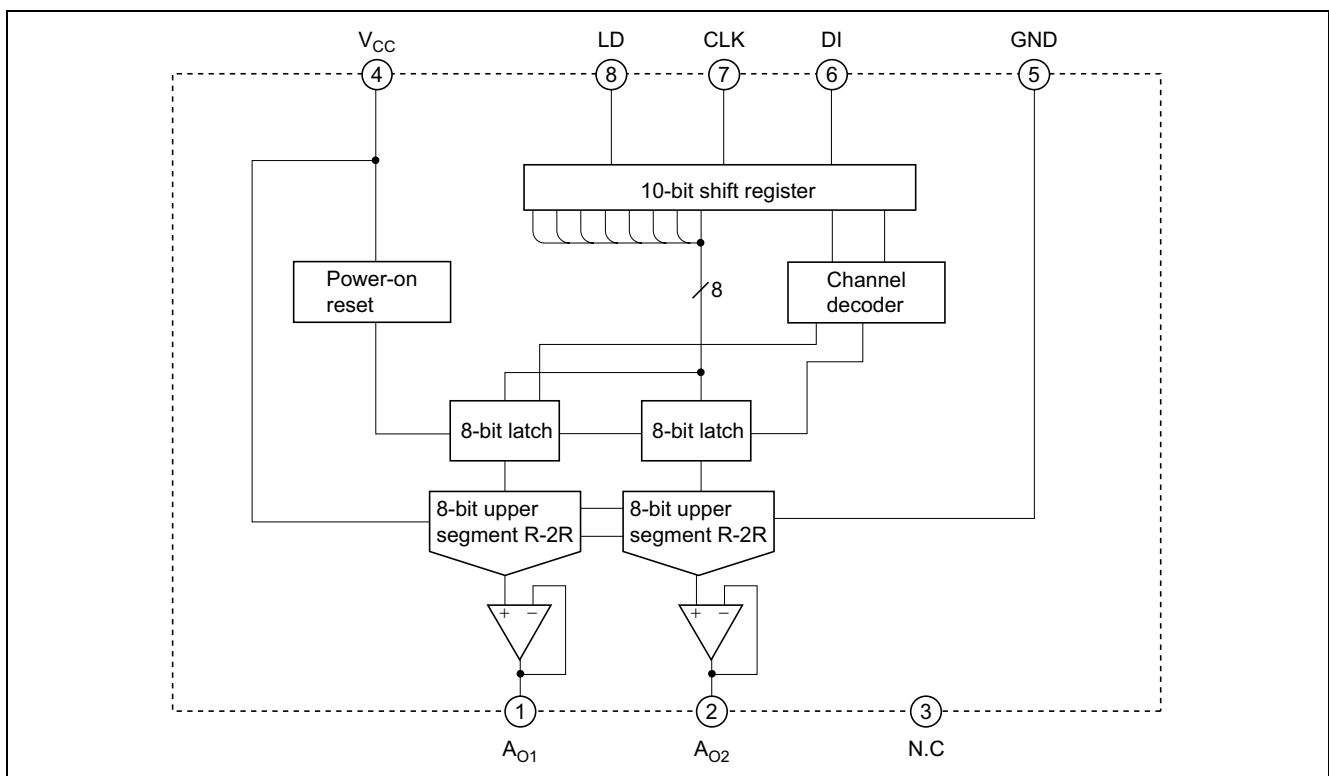
### Features

- Data transfer format  
10-bit serial data input type
- Output buffer op-amps  
Operable over entire voltage range from almost ground to  $V_{CC}$  (0 to 5 V)
- High output current capacity  
 $\pm 1$  mA or higher

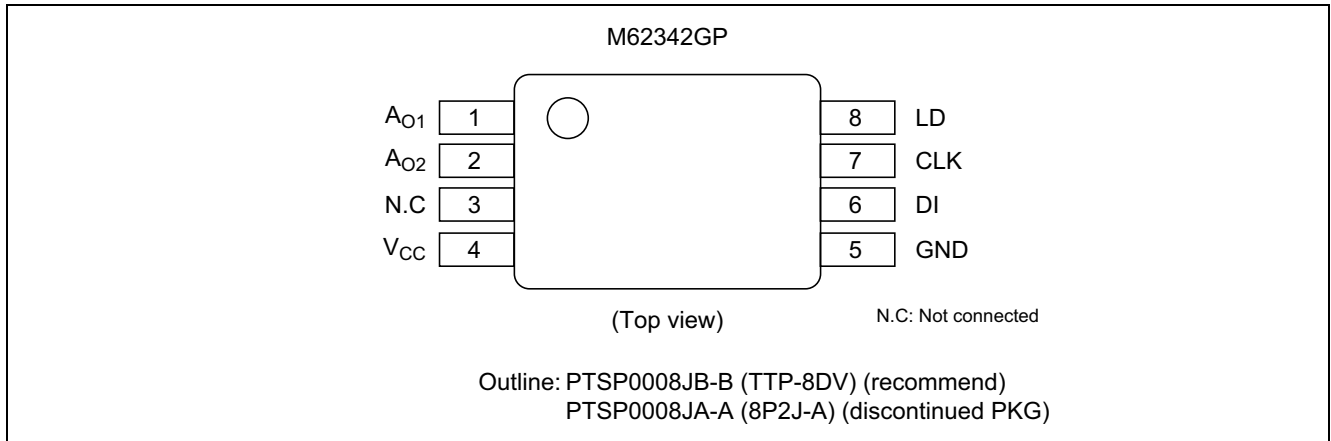
### Application

Signal gain setting and automatic adjustment in DSC, CTV, and display monitors, conversion from digital data to analog data in consumer and industrial products

### Block Diagram



## Pin Arrangement



## Pin Description

| Pin No. | Symbol          | Function  |
|---------|-----------------|---|
| 6       | DI              | Serial data input pin. Inputs serial data with a 10-bit data length.  |
| 7       | CLK             | Serial clock input pin. Input signal from DI pin is input to 10-bit shift register at rise of shift clock.                  |
| 8       | LD              | Load pin. When "H" level is input to LD pin, value in 10-bit shift register is loaded into decoder and D/A output register. |
| 1       | A <sub>O1</sub> | 8-bit resolution D/A converter output pins<br>(After power-on, all channels are reset and DAC data 00h is output.)          |
| 2       | A <sub>O2</sub> |   |
| 3       | N.C.            | (Not connected)   |
| 4       | V <sub>CC</sub> | Power supply voltage pin  |
| 5       | GND             | GND pin   |

## Absolute Maximum Ratings

| Item                          | Symbol    | Rated Value                   | Unit |
|-------------------------------|-----------|-------------------------------|------|
| Power supply voltage          | $V_{CC}$  | -0.3 to 7.0                   | V    |
| Input voltage                 | $V_{in}$  | -0.3 to $V_{CC}+0.3 \leq 7.0$ | V    |
| Output voltage                | $V_o$     | -0.3 to $V_{CC}+0.3 \leq 7.0$ | V    |
| Internal power consumption    | $P_d$     | 200                           | mW   |
| Operating ambient temperature | $T_{opr}$ | -20 to +85                    | °C   |
| Storage temperature           | $T_{stg}$ | -40 to +125                   | °C   |

## Electrical Characteristics

(Unless specified otherwise,  $V_{CC} = +5\text{ V} \pm 10\%$ ,  $GND = 0\text{ V}$ ,  $T_a = -20^\circ\text{C}$  to  $85^\circ\text{C}$ )

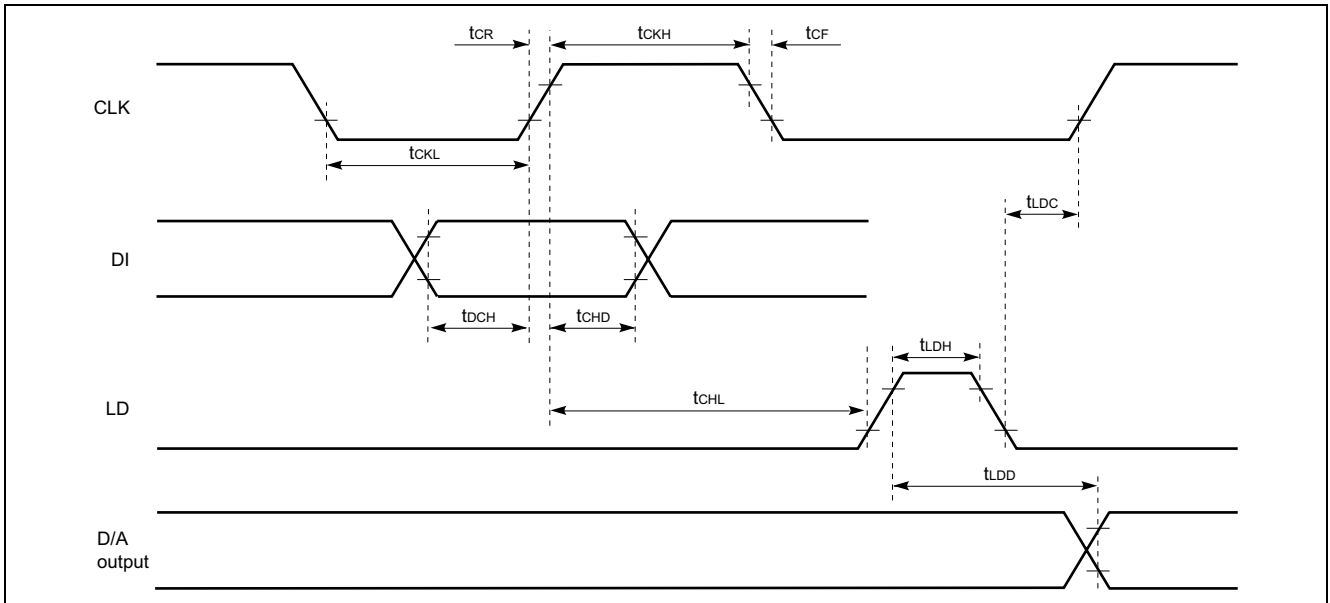
| Item                                 | Symbol     | Specification Values |     |              | Unit          | Test Conditions  |
|--------------------------------------|------------|----------------------|-----|--------------|---------------|--|
|                                      |            | Min                  | Typ | Max          |               |  |
| Power supply voltage                 | $V_{CC}$   | 2.7                  | 5.0 | 5.5          | V             |  |
| Power supply current                 | $I_{CC}$   | —                    | 0.7 | 2.5          | mA            | At CLK = 1 MHz operation, $I_{AO} = 0\ \mu\text{A}$<br>D/A data: 6 Ah (at maximum current) |
|                                      |            | —                    | 0.5 | 1.6          |               | DI = CLK = LD = GND, $I_{AO} = 0\ \mu\text{A}$   |
| Input leakage current                | $I_{ILK}$  | -10                  | —   | 10           | $\mu\text{A}$ | $V_{IN} = 0$ to $V_{CC}$   |
| Input voltage "L"                    | $V_{IL}$   | 0                    | —   | $0.2V_{CC}$  | V             |  |
| Input voltage "H"                    | $V_{IH}$   | $0.5V_{CC}$          | —   | $V_{CC}$     | V             |  |
| Buffer amp output voltage range      | $V_{AO}$   | 0.1                  | —   | $V_{CC}-0.1$ | V             | $I_{AO} = \pm 100\ \mu\text{A}$  |
|                                      |            | 0.1                  | —   | $V_{CC}-0.2$ |               | $I_{AO} = \pm 500\ \mu\text{A}$  |
| Buffer amp output drive range        | $I_{AO}$   | -1.0                 | —   | 1.0          | mA            | Upper saturation voltage = 0.3 V<br>Lower saturation voltage = 0.2 V                       |
| Differential nonlinearity error      | $S_{DL}$   | -1.0                 | —   | 1.0          | LSB           | $V_{CC} = 5.12\text{ V}$ (20 mV/LSB)<br>No load ( $I_{AO} = 0$ )                           |
| Nonlinearity error                   | $S_L$      | -1.5                 | —   | 1.5          | LSB           |  |
| Zero point error                     | $S_{ZERO}$ | -2.0                 | —   | 2.0          | LSB           |  |
| Full-scale error                     | $S_{FULL}$ | -2.0                 | —   | 2.0          | LSB           |  |
| Oscillation limit output capacitance | $C_O$      | —                    | —   | 0.1          | $\mu\text{F}$ |  |
| Buffer amp output impedance          | $R_O$      | —                    | 5.0 | —            | $\Omega$      |  |

## AC Characteristics

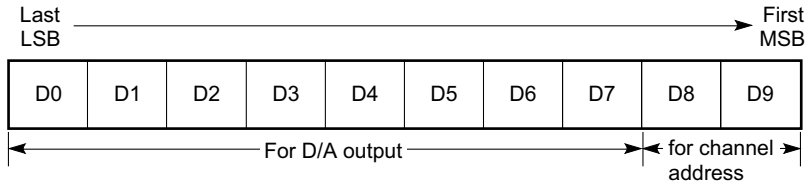
(Unless specified otherwise,  $V_{CC} = +5\text{ V} \pm 10\%$ ,  $GND = 0\text{ V}$ ,  $T_a = -20^\circ\text{C}$  to  $85^\circ\text{C}$ )

| Item                     | Symbol    | Specification Values |     |     | Unit          | Test Conditions                   |
|--------------------------|-----------|----------------------|-----|-----|---------------|-----------------------------------|
|                          |           | Min                  | Typ | Max |               |                                   |
| Clock "L" pulse width    | $t_{CKL}$ | 200                  | —   | —   | ns            |                                   |
| Clock "H" pulse width    | $t_{CKH}$ | 200                  | —   | —   | ns            |                                   |
| Clock rise time          | $t_{CR}$  | —                    | —   | 200 | ns            |                                   |
| Clock fall time          | $t_{CF}$  | —                    | —   | 200 | ns            |                                   |
| Data setup time          | $t_{DCH}$ | 30                   | —   | —   | ns            |                                   |
| Data hold time           | $t_{CHD}$ | 60                   | —   | —   | ns            |                                   |
| Load setup time          | $t_{CHL}$ | 200                  | —   | —   | ns            |                                   |
| Load hold time           | $t_{LDC}$ | 100                  | —   | —   | ns            |                                   |
| Load "H" pulse width     | $t_{LDH}$ | 100                  | —   | —   | ns            |                                   |
| D-A output settling time | $t_{LDD}$ | —                    | —   | 300 | $\mu\text{s}$ | Until output reaches last 1/2 LSB |

### Timing Chart



## Digital Data Format



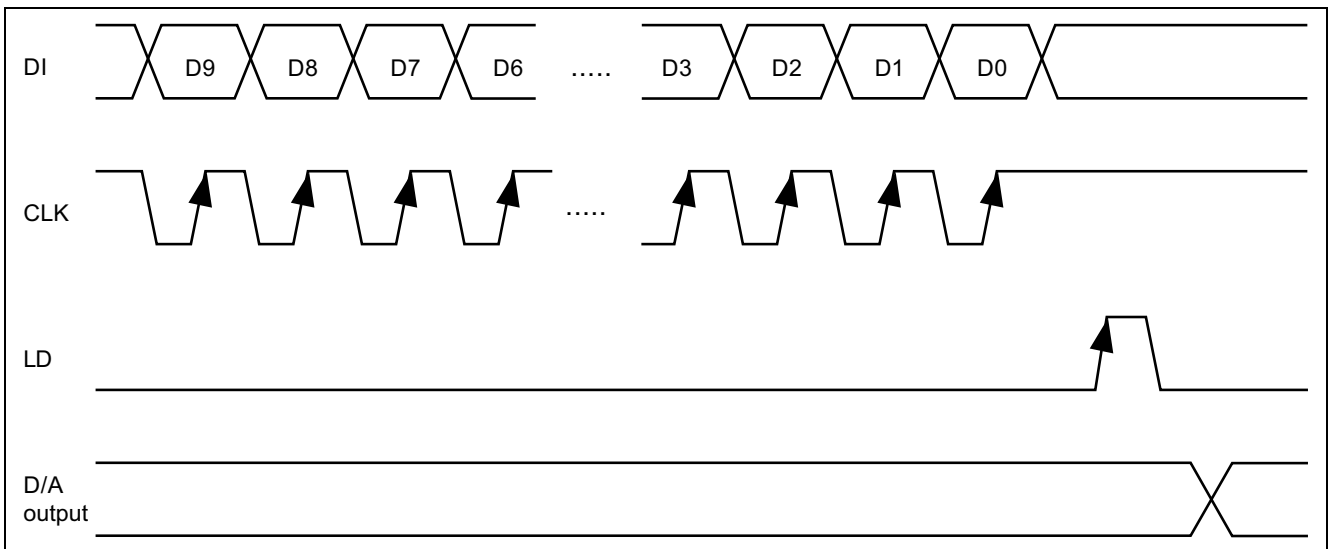
### D/A Data

| D0 | D1 | D2 | D3 | D4 | D5 | D6 | D7 | D-A output              |
|----|----|----|----|----|----|----|----|-------------------------|
| 0  | 0  | 0  | 0  | 0  | 0  | 0  | 0  | $V_{CC}/256 \times 1$   |
| 1  | 0  | 0  | 0  | 0  | 0  | 0  | 0  | $V_{CC}/256 \times 2$   |
| 0  | 1  | 0  | 0  | 0  | 0  | 0  | 0  | $V_{CC}/256 \times 3$   |
| 1  | 1  | 0  | 0  | 0  | 0  | 0  | 0  | $V_{CC}/256 \times 4$   |
| ⋮  | ⋮  | ⋮  | ⋮  | ⋮  | ⋮  | ⋮  | ⋮  | ⋮                       |
| 0  | 1  | 1  | 1  | 1  | 1  | 1  | 1  | $V_{CC}/256 \times 255$ |
| 1  | 1  | 1  | 1  | 1  | 1  | 1  | 1  | $V_{CC}/256 \times 256$ |

### Channel Select Data

| D8 | D9 | Channel Selection |
|----|----|-------------------|
| 0  | 0  | AO1 selected      |
| 1  | 0  | AO2 selected      |
| 0  | 1  | Don't care        |
| 1  | 1  | Don't care        |

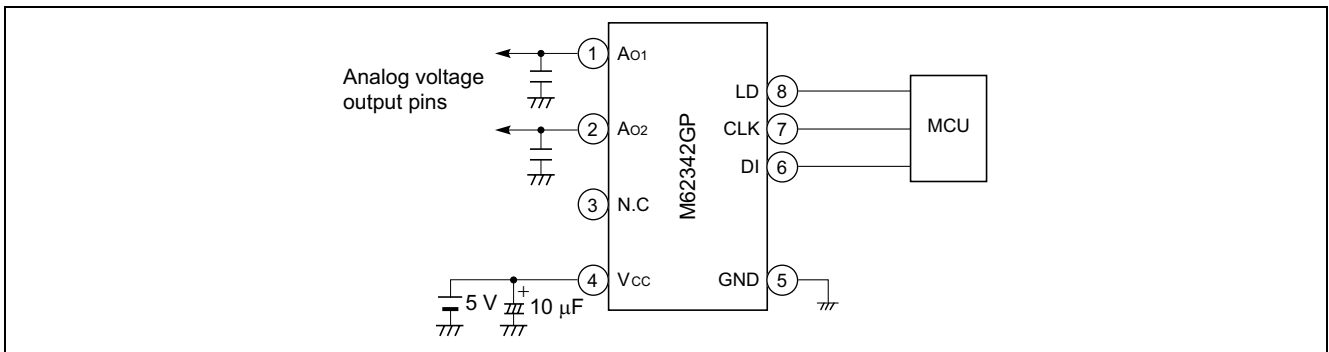
### Data Timing Chart (Model)



## Usage Notes

1. With this IC, D/A converter upper reference voltage setting is performed by means of the power supply voltage. If ripples or spikes are imposed on this pin, conversion accuracy may fall. When using this IC, a capacitor must be inserted between the power supply pin and GND in order to ensure stable D/A conversion.
2. The output buffer amps of this IC are highly tolerant of capacitive loads. Therefore, connecting capacitors (0.1  $\mu$ F max.) between the output pins and ground in order to eliminate jitter or noise due to output line wiring presents no problems whatever in terms of operation.

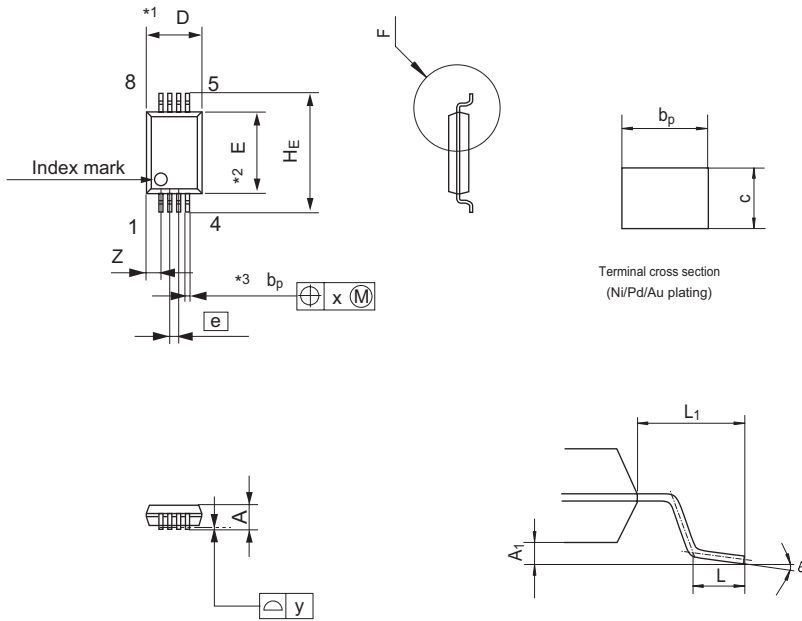
## Sample Standard Application Circuit





Package Dimensions

|              |                       |              |               |            |
|--------------|-----------------------|--------------|---------------|------------|
| Package Name | JEITA Package Code    | RENESAS Code | Previous Code | MASS[Typ.] |
| TSSOP-8      | P-TSSOP8-4.4 x 3-0.65 | PTSP0008JB-B | TTP-8DV       | 0.034g     |

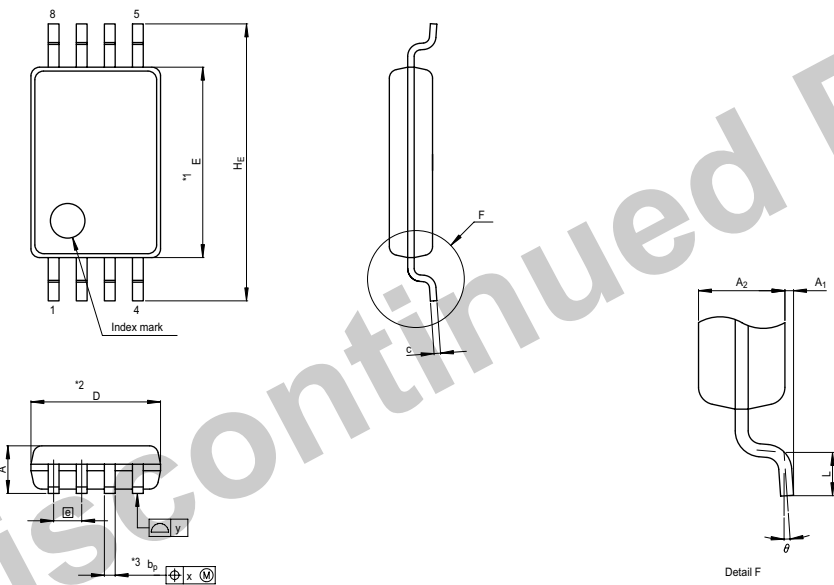


Terminal cross section (Ni/Pd/Au plating)

NOTE)  
 1. DIMENSIONS \*\*1(Nom)\*\* AND \*\*2\*\* DO NOT INCLUDE MOLD FLASH.  
 2. DIMENSION \*\*3\*\* DOES NOT INCLUDE TRIM OFFSET.

| Reference Symbol | Dimension in Millimeters |      |       |
|------------------|--------------------------|------|-------|
|                  | Min                      | Nom  | Max   |
| D                | —                        | 3.00 | 3.30  |
| E                | —                        | 4.40 | —     |
| A <sub>2</sub>   | —                        | —    | —     |
| A <sub>1</sub>   | 0.03                     | 0.07 | 0.10  |
| A                | —                        | —    | 1.10  |
| b <sub>p</sub>   | 0.15                     | 0.20 | 0.25  |
| b <sub>1</sub>   | —                        | —    | —     |
| c                | 0.10                     | 0.15 | 0.20  |
| c <sub>1</sub>   | —                        | —    | —     |
| θ                | 0°                       | —    | 8°    |
| H <sub>E</sub>   | 6.20                     | 6.40 | 6.60  |
| Ⓜ                | —                        | 0.65 | —     |
| x                | —                        | —    | 0.13  |
| y                | —                        | —    | 0.10  |
| Z                | —                        | —    | 0.805 |
| L                | 0.40                     | 0.50 | 0.60  |
| L <sub>1</sub>   | —                        | 1.0  | —     |

|              |                     |              |               |            |
|--------------|---------------------|--------------|---------------|------------|
| Package Name | JEITA Package Code  | RENESAS Code | Previous Code | MASS[Typ.] |
| TSSOP-8      | P-TSSOP8-4.4x3-0.65 | PTSP0008JA-A | 8P2J-A        | 0.04g      |



Detail F

NOTE)  
 1. DIMENSIONS \*\*1\*\* AND \*\*2\*\* DO NOT INCLUDE MOLD FLASH.  
 2. DIMENSION \*\*3\*\* DOES NOT INCLUDE TRIM OFFSET.

| Reference Symbol | Dimension in Millimeters |      |      |
|------------------|--------------------------|------|------|
|                  | Min                      | Nom  | Max  |
| D                | 2.9                      | 3.0  | 3.1  |
| E                | 4.3                      | 4.4  | 4.5  |
| A <sub>2</sub>   | —                        | 1.0  | —    |
| A                | —                        | —    | 1.2  |
| A <sub>1</sub>   | 0                        | 0.1  | 0.2  |
| b <sub>p</sub>   | 0.2                      | 0.25 | 0.32 |
| c                | 0.14                     | 0.15 | 0.2  |
| θ                | 0°                       | —    | 8°   |
| H <sub>E</sub>   | 6.2                      | 6.4  | 6.6  |
| Ⓜ                | —                        | 0.65 | —    |
| x                | —                        | —    | 0.13 |
| y                | —                        | —    | 0.10 |
| L                | 0.3                      | 0.5  | 0.7  |

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