

National Semiconductor is now part of  
Texas Instruments.

Search <http://www.ti.com/> for the latest technical  
information and details on our current products and services.

# LM6161/LM6261/LM6361 High Speed Operational Amplifier

## General Description

The LM6161 family of high-speed amplifiers exhibits an excellent speed-power product in delivering 300 V/ $\mu$ s and 50 MHz unity gain stability with only 5 mA of supply current. Further power savings and application convenience are possible by taking advantage of the wide dynamic range in operating supply voltage which extends all the way down to +5V.

These amplifiers are built with National's VIPTM (Vertically Integrated PNP) process which provides fast PNP transistors that are true complements to the already fast NPN devices. This advanced junction-isolated process delivers high speed performance without the need for complex and expensive dielectric isolation.

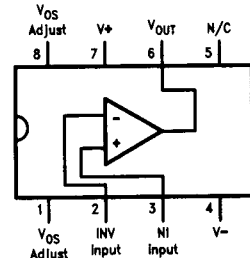
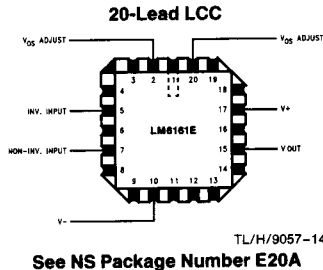
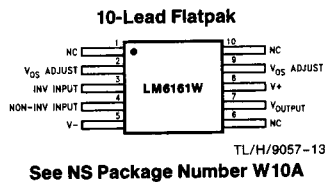
## Features

- High slew rate 300 V/ $\mu$ s
- High unity gain freq 50 MHz
- Low supply current 5 mA
- Fast settling 120 ns to 0.1%
- Low differential gain <0.1%
- Low differential phase 0.1°
- Wide supply range 4.75V to 32V
- Stable with unlimited capacitive load
- Well behaved; easy to apply

## Applications

- Video amplifier
- High-frequency filter
- Wide-bandwidth signal conditioning
- Radar
- Sonar

## Connection Diagrams



Temperature Range			Package	NSC Drawing
Military -55°C ≤ TA ≤ +125°C	Industrial -25°C ≤ TA ≤ +85°C	Commercial 0°C ≤ TA ≤ +70°C		
	LM6261N	LM6361N	8-Pin Molded DIP	N08E
LM6161J LM6161J/883 5962-8962101PA	LM6261J	LM6361J	8-Pin Ceramic DIP	J08A
	LM6261M	LM6361M	8-Pin Molded Surface Mt.	M08A
LM6161E/883 5962-89621012A			20-Lead LCC	E20A
LM6161W/883 5962-8962101HA			10-Pin Ceramic Flatpak	W10A

## Absolute Maximum Ratings (Note 12)

If Military/Aerospace specified devices are required, please contact the National Semiconductor Sales Office/Distributors for availability and specifications.

Supply Voltage ( $V^+ - V^-$ )	36V
Differential Input Voltage (Note 8)	$\pm 8V$
Common-Mode Voltage Range (Note 10)	$(V^+ - 0.7V)$ to $(V^- + 7V)$
Output Short Circuit to GND (Note 1)	Continuous
Soldering Information	
Dual-In-Line Package (N, J)	
Soldering (10 sec.)	260°C
Small Outline Package (M)	
Vapor Phase (60 sec.)	215°C
Infrared (15 sec.)	220°C

See AN-450 "Surface Mounting Methods and Their Effect on Product Reliability" for other methods of soldering surface mount devices.

Storage Temp Range	$-65^\circ\text{C}$ to $+150^\circ\text{C}$
Max Junction Temperature	150°C
ESD Tolerance (Notes 6 and 7)	$\pm 700V$

## Operating Ratings (Note 12)

Temperature Range (Note 2)	
LM6161	$-55^\circ\text{C} \leq T_J \leq +125^\circ\text{C}$
LM6261	$-25^\circ\text{C} \leq T_J \leq +85^\circ\text{C}$
LM6361	$0^\circ\text{C} \leq T_J \leq +70^\circ\text{C}$
Supply Voltage Range	4.75V to 32V

## DC Electrical Characteristics

The following specifications apply for Supply Voltage =  $\pm 15V$ ,  $V_{CM} = 0$ ,  $R_L \geq 100\text{ k}\Omega$  and  $R_S = 50\Omega$  unless otherwise noted. **Boldface** limits apply for  $T_J = T_{MIN}$  to  $T_{MAX}$ ; all other limits  $T_J = 25^\circ\text{C}$ .

Symbol	Parameter	Conditions	Typ	LM6161	LM6261	LM6361	Units
				Limit (Notes 3, 11)	Limit (Note 3)	Limit (Note 3)	
$V_{OS}$	Input Offset Voltage		5	7 <b>10</b>	7 <b>9</b>	20 <b>22</b>	mV Max
$V_{OS}$ Drift	Input Offset Voltage Average Drift		10				$\mu\text{V}/^\circ\text{C}$
$I_b$	Input Bias Current		2	3 <b>6</b>	3 <b>5</b>	5 <b>6</b>	$\mu\text{A}$ Max
$I_{OS}$	Input Offset Current		150	350 <b>800</b>	350 <b>600</b>	1500 <b>1900</b>	nA Max
$I_{OS}$ Drift	Input Offset Current Average Drift		0.4				nA/ $^\circ\text{C}$
$R_{IN}$	Input Resistance	Differential	325				k $\Omega$
$C_{IN}$	Input Capacitance	$A_V = +1$ @ 10 MHz	1.5				pF
$A_{VOL}$	Large Signal Voltage Gain	$V_{OUT} = \pm 10V$ , $R_L = 2\text{ k}\Omega$ (Note 9)	750	550 <b>300</b>	550 <b>400</b>	400 <b>350</b>	V/V Min
		$R_L = 10\text{ k}\Omega$ (Note 9)	2900				V/V
$V_{CM}$	Input Common-Mode Voltage Range	Supply = $\pm 15V$	+14.0	+13.9 <b>+13.8</b>	+13.9 <b>+13.8</b>	+13.8 <b>+13.7</b>	Volts Min
			-13.2	-12.9 <b>-12.7</b>	-12.9 <b>-12.7</b>	-12.8 <b>-12.7</b>	Volts Min
		Supply = +5V (Note 4)	4.0	3.9 <b>3.8</b>	3.9 <b>3.8</b>	3.8 <b>3.7</b>	Volts Min
			1.8	2.0 <b>2.2</b>	2.0 <b>2.2</b>	2.1 <b>2.2</b>	Volts Max
CMRR	Common-Mode Rejection Ratio	$-10V \leq V_{CM} \leq +10V$	94	80 <b>74</b>	80 <b>76</b>	72 <b>70</b>	dB Min
PSRR	Power Supply Rejection Ratio	$\pm 10V \leq V^\pm \leq \pm 16V$	90	80 <b>74</b>	80 <b>76</b>	72 <b>70</b>	dB Min
$V_O$	Output Voltage Swing	Supply = $\pm 15V$ and $R_L = 2\text{ k}\Omega$	+14.2	+13.5 <b>+13.3</b>	+13.5 <b>+13.3</b>	+13.4 <b>+13.3</b>	Volts Min
			-13.4	-13.0 <b>-12.7</b>	-13.0 <b>-12.8</b>	-12.9 <b>-12.8</b>	Volts Min

**DC Electrical Characteristics** (Continued)

The following specifications apply for Supply Voltage =  $\pm 15V$ ,  $V_{CM} = 0$ ,  $R_L \geq 100\text{ k}\Omega$  and  $R_S = 50\Omega$  unless otherwise noted.

**Boldface** limits apply for  $T_J = T_{MIN}$  to  $T_{MAX}$ ; all other limits  $T_J = 25^\circ\text{C}$ .

Symbol	Parameter	Conditions	Typ	LM6161	LM6261	LM6361	Units
				Limit (Notes 3, 11)	Limit (Note 3)	Limit (Note 3)	
$V_O$ (Continued)	Output Voltage Swing (Continued)	Supply = +5V and $R_L = 2\text{ k}\Omega$ (Note 4)	4.2	3.5 <b>3.3</b>	3.5 <b>3.3</b>	3.4 <b>3.3</b>	Volts Min
			1.3	1.7 <b>2.0</b>	1.7 <b>1.9</b>	1.8 <b>1.9</b>	Volts Max
	Output Short Circuit Current	Source	65	30 <b>20</b>	30 <b>25</b>	30 <b>25</b>	mA Min
		Sink	65	30 <b>20</b>	30 <b>25</b>	30 <b>25</b>	mA Min
$I_S$	Supply Current		5.0	6.5 <b>6.8</b>	6.5 <b>6.7</b>	6.8 <b>6.9</b>	mA Max

**AC Electrical Characteristics**

The following specifications apply for Supply Voltage =  $\pm 15V$ ,  $V_{CM} = 0$ ,  $R_L \geq 100\text{ k}\Omega$  and  $R_S = 50\Omega$  unless otherwise noted.

**Boldface** limits apply for  $T_J = T_{MIN}$  to  $T_{MAX}$ ; all other limits  $T_J = 25^\circ\text{C}$ .

Symbol	Parameter	Conditions	Typ	LM6161	LM6261	LM6361	Units
				Limit (Notes 3, 11)	Limit (Note 3)	Limit (Note 3)	
GBW	Gain-Bandwidth Product	@ $f = 20\text{ MHz}$	50	40 <b>30</b>	40 <b>35</b>	35 <b>32</b>	MHz Min
		Supply = $\pm 5V$	35				MHz
SR	Slew Rate	$A_V = +1$ (Note 8)	300	200 <b>180</b>	200 <b>180</b>	200 <b>180</b>	$V/\mu\text{s}$ Min
		Supply = $\pm 5V$ (Note 8)	200				$V/\mu\text{s}$
PBW	Power Bandwidth	$V_{OUT} = 20\text{ V}_{PP}$	4.5				MHz
$t_S$	Settling Time	10V Step to 0.1% $A_V = -1$ , $R_L = 2\text{ k}\Omega$	120				ns
$\phi_m$	Phase Margin		45				Deg
$A_D$	Differential Gain	NTSC, $A_V = +4$	<0.1				%
$\phi_D$	Differential Phase	NTSC, $A_V = +4$	0.1				Deg
$e_{np-p}$	Input Noise Voltage	$f = 10\text{ kHz}$	15				$nV/\sqrt{\text{Hz}}$
$i_{np-p}$	Input Noise Current	$f = 10\text{ kHz}$	1.5				$pA/\sqrt{\text{Hz}}$

**Note 1:** Continuous short-circuit operation at elevated ambient temperature can result in exceeding the maximum allowed junction temperature of  $150^\circ\text{C}$ .

**Note 2:** The typical junction-to-ambient thermal resistance of the molded plastic DIP (N) is  $105^\circ\text{C}/\text{W}$ , the molded plastic SO (M) package is  $155^\circ\text{C}/\text{W}$ , and the cerdip (J) package is  $125^\circ\text{C}/\text{W}$ . All numbers apply for packages soldered directly into a printed circuit board.

**Note 3:** Limits are guaranteed by testing or correlation.

**Note 4:** For single supply operation, the following conditions apply:  $V^+ = 5V$ ,  $V^- = 0V$ ,  $V_{CM} = 2.5V$ ,  $V_{OUT} = 2.5V$ . Pin 1 & Pin 8 ( $V_{OS}$  Adjust) are each connected to Pin 4 ( $V^-$ ) to realize maximum output swing. This connection will degrade  $V_{OS}$ ,  $V_{OS}$  Drift, and Input Voltage Noise.

**Note 5:**  $C_L \leq 5\text{ pF}$ .

**Note 6:** In order to achieve optimum AC performance, the input stage was designed without protective clamps. Exceeding the maximum differential input voltage results in reverse breakdown of the base-emitter junction of one of the input transistors and probable degradation of the input parameters (especially  $V_{OS}$ ,  $I_{OS}$ , and Noise).

**Note 7:** The average voltage that the weakest pin combinations (those involving Pin 2 or Pin 3) can withstand and still conform to the datasheet limits. The test circuit used consists of the human body model of  $100\text{ pF}$  in series with  $1500\Omega$ .

**Note 8:**  $V_{IN} = 8V$  step. For supply =  $\pm 5V$ ,  $V_{IN} = 5V$  step.

**Note 9:** Voltage Gain is the total output swing (20V) divided by the input signal required to produce that swing.

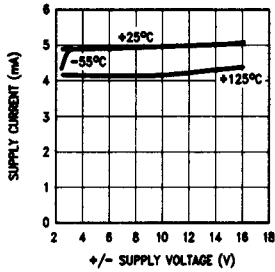
**Note 10:** The voltage between  $V^+$  and either input pin must not exceed 36V.

**Note 11:** A military RETS electrical test specification is available on request. At the time of printing, the RETS6161X specs complied with all **Boldface** limits in this column.

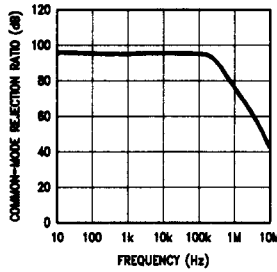
**Note 12:** Absolute Maximum Ratings indicate limits beyond which damage to the device may occur. Operating Ratings indicate conditions for which the device is intended to be functional, but do not guarantee specific performance limits. For guaranteed specifications and test conditions, see the Electrical Characteristics. The guaranteed specifications apply only for the test conditions listed.

**Typical Performance Characteristics** ( $R_L = 10\text{ k}\Omega$ ,  $T_A = 25^\circ\text{C}$  unless otherwise specified)

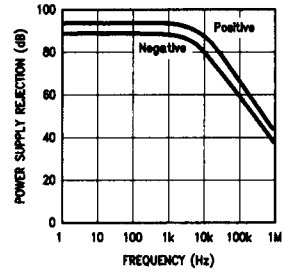
**Supply Current vs Supply Voltage**



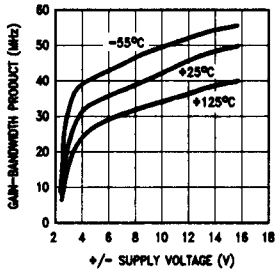
**Common-Mode Rejection Ratio**



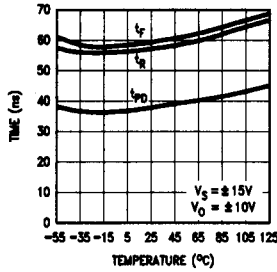
**Power Supply Rejection Ratio**



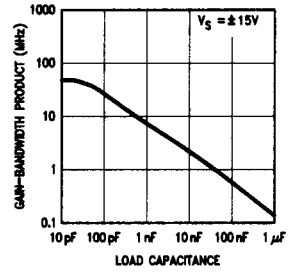
**Gain-Bandwidth Product**



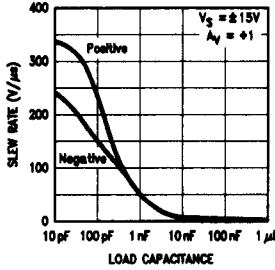
**Propagation Delay Rise and Fall Times**



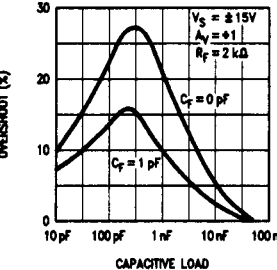
**Gain-Bandwidth Product vs Load Capacitance**



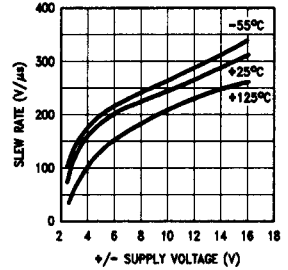
**Slew Rate vs Load Capacitance**



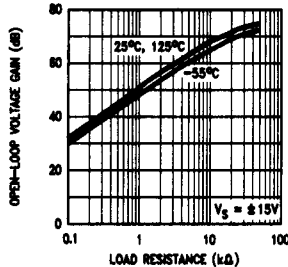
**Overshoot vs Capacitive Load**



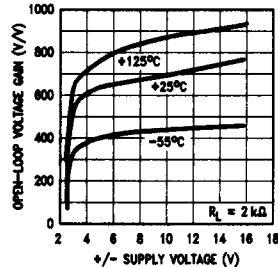
**Slew Rate**



**Voltage Gain vs Load Resistance**



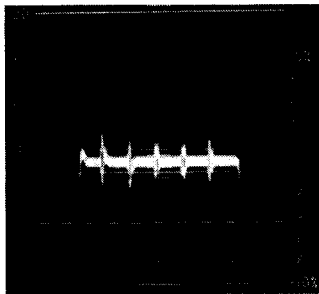
**Gain vs Supply Voltage**



# Typical Performance Characteristics

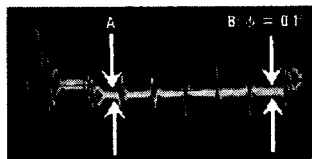
( $R_L = 10\text{ k}\Omega$ ,  $T_A = 25^\circ\text{C}$  unless otherwise specified) (Continued)

## Differential Gain (Note)



TL/H/9057-7

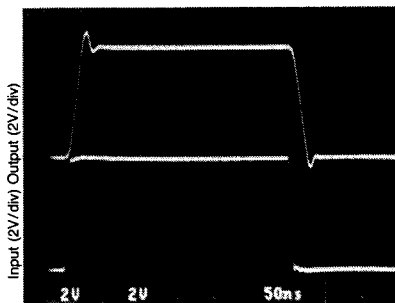
## Differential Phase (Note)



TL/H/9057-8

**Note:** Differential gain and differential phase measured for four series LM6361 op amps configured as unity-gain followers, in series with an LM6321 buffer. Error added by LM6321 is negligible. Test performed using Tektronix Type 520 NTSC test system.

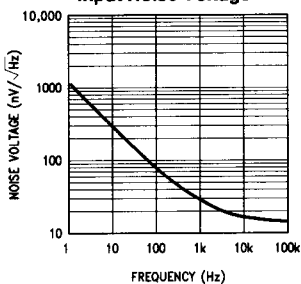
## Step Response; $A_v = +1$



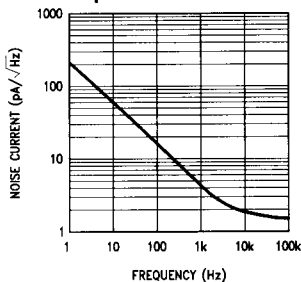
(50 ns/div)

TL/H/9057-1

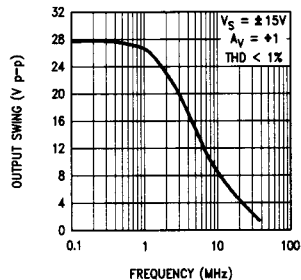
## Input Noise Voltage



## Input Noise Current



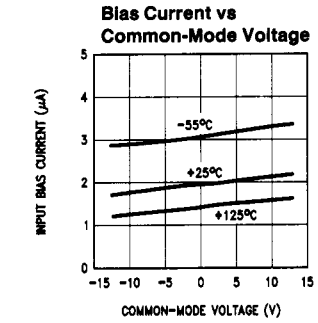
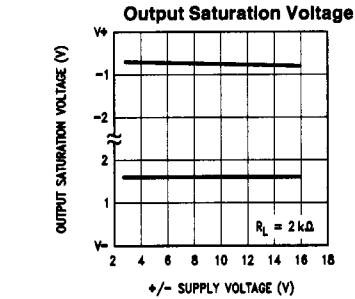
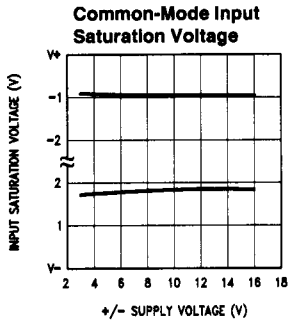
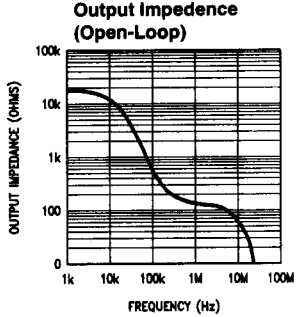
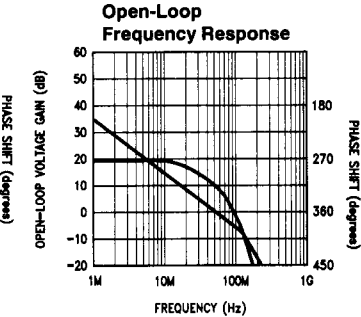
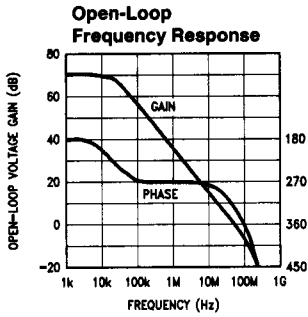
## Power Bandwidth



TL/H/9057-9

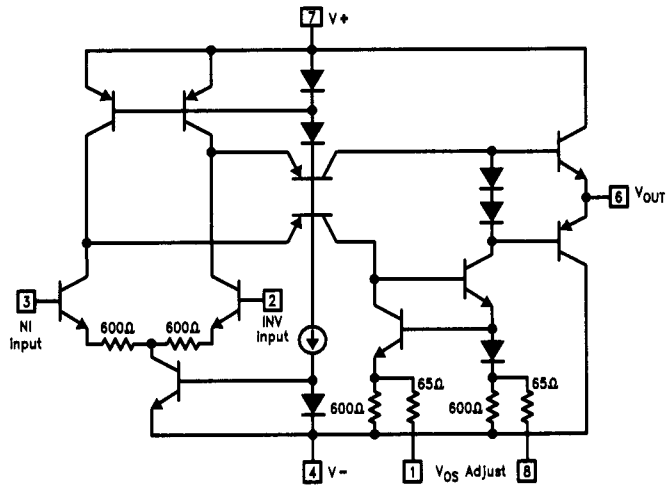
# Typical Performance Characteristics

( $R_L = 10\text{ k}\Omega$ ,  $T_A = 25^\circ\text{C}$  unless otherwise specified) (Continued)



TL/H/8057-12

## Simplified Schematic



TL/H/8057-3

