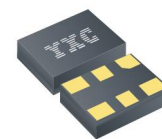


# Temperature Compensated Crystal Oscillator

## YSO512ET High Precision-TCXO



### Applications

- High-Precision Intelligent Electronic Devices

### Features

- Frequency Stability as Tight as  $\pm 0.1$  ppm Over  $-40^{\circ}\text{C}$  to  $85^{\circ}\text{C}$
- Package Size :  $5.0 \times 3.2$ ,  $7.0 \times 5.0\text{mm}$
- Wide Frequency Range

## Specifications

Item/Type		Clipped Sine wave or CMOS	
Package Size		5.0 x 3.2; 7.0 x 5.0	
Frequency Range		10, 12, 12.8, 19.2, 20, 24.576, 25, 30, 32, 40, 48, 50MHz, or specify	
Supply Voltage		3.3V, or specify	
Frequency Tolerance at $25^{\circ}\text{C}$		$\pm 1$ ppm, or specify	
Frequency Stability	vs Aging at $T_a = +25^{\circ}\text{C}$	$\pm 1.0$ ppm / year ( max.)	
	vs Voltage Change	$\pm 0.2$ ppm ( max.), for a $\pm 5\%$ input voltage change.	
	vs Load Change	$\pm 0.2$ ppm ( max.), for a $\pm 10\%$ load condition change.	
	vs Temperature Change	$\pm 0.1$ ppm / $\pm 0.28$ ppm, or specify	
Operating Temperature Range		$-40 \sim +85^{\circ}\text{C}$ , or specify	
Storage Temperature Range		$-55^{\circ}\text{C}$ to $+125^{\circ}\text{C}$	
Output Type		Clipped Sine wave	CMOS
Current Consumption. (max.)		15mA ( max.)	
Rise Time & Fall Time		-	6nsec.( max.)
Output Voltage High " 1 "		-	2.4V( min.)
Output Voltage Low " 0 "		-	0.4V( max.)
Output Voltage Level (peak to peak)		0.8V( min.)	
Duty Cycle at $50\% \pm 5\%$ output swing		45 ~ 55%	
Start-up Time		10msec.( max.)	
Output Load		$10\text{K}\Omega // 10\text{pF}$	15pF

# Temperature Compensated Crystal Oscillator YSO512ET TCXO-High Precision



## Dimensions and Recommended Land Pattern (外观尺寸及推荐焊盘)

<p>5.0*3.2</p>	<p>Recommended Land Pattern</p> <table border="1"> <tbody> <tr> <td>Pin1</td> <td>NC</td> </tr> <tr> <td>Pin2</td> <td>GND</td> </tr> <tr> <td>Pin3</td> <td>Output</td> </tr> <tr> <td>Pin4</td> <td>VDD</td> </tr> </tbody> </table>	Pin1	NC	Pin2	GND	Pin3	Output	Pin4	VDD
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Pin1	NC								
Pin2	GND								
Pin3	Output								
Pin4	VDD								
<p>Notes:</p> <p>1. A capacitor of value 0.01uf~0.1uf or higher between VDD and GND is required.</p>									