

SS12 THRU SS120

1.0AMP Surface Mount Schottky Barrier Rectifier

Features

- · Schottky Brrier Chip
- Low Power Loss, High Efficiency
- · Ideally Suited for Automatic Assembly
- Surge Overload Rating to 30A Peak
- Plastic Case Material has UL Flammability Classification Rating 94V-0

Mechanical Data

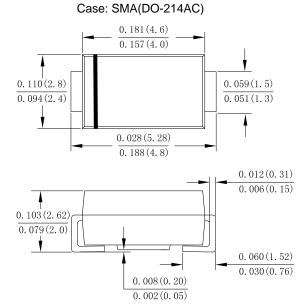
· Case: Molded plastic SMA

 Terminals: Plated leads solderable per MIL-STD-750, Method 2026 guaranteed

· Polarity: Color band denotes cathode end

Mounting Position: Any

· Making: Type Number



Dimensions in inches and (millimeters)

Maximum Ratings and Electrical Characteristics

Rating at 25°C ambient temperature unless otherwise specified

Single phase, half wave, 60Hz, resistive or inductive load

For capacitive load derate current by 20%

| Type Number | SYMBOL | SS12 | SS13 | SS14 | SS145 | SS15 | SS16 | SS18 | SS110 | SS115 | SS120 | Unit |
|--|------------------|-------------|------|------|-------|------|------|------|-------|-------|--|---------|
| Maximum Recurrent Peak Reverse Voltage | VRRM | 20 | 30 | 40 | 45 | 50 | 60 | 80 | 100 | 150 | 200 | V |
| Maximum RMS Voltage | VRMS | 14 | 21 | 28 | 31 | 35 | 42 | 56 | 70 | 105 | 140 | V |
| Maximum DC Blocking Voltage | V _{DC} | 20 | 30 | 40 | 45 | 50 | 60 | 80 | 100 | 150 | 200 | V |
| Average Rectified Output Current @TL =100 °C | IF(AV) | 1.0 | | | | | | | | | | Α |
| Peak Forward Surge Current 8.3ms Single half sine-wave superimposed on rated load (JEDEC Method) | İfsm | 30 | | | | | | | | | Α | |
| Rating for fusing (t<8.3ms) | l ² t | 3.74 | | | | | | | | | | $A^2 s$ |
| Forward Voltage @IF=1.0A | V _{FM} | 0.55 | | | 0 | .7 | (|).85 | 0. | 92 | V | |
| Peak Reverse Current @TA =25°C | | 0.1 0.05 | | | | | | | | | | A |
| At Rated DC Blocking Voltage @TA =100°C | l _R | 10 | | | | | | 5 | | | mA | |
| Typical Junction Capacitance (Note 1) | Сл | 35 20 | | | | | | pF | | | | |
| Typical Thermal Resistance perleg | RθJA | 110 | | | | | | | | | $^{\circ}\!$ | |
| Operating Temperature Range | TJ | -55 to+150 | | | | | | | | | $^{\circ}$ C | |
| Storage Temperature Range | Tstg | -55 to +150 | | | | | | | | | $^{\circ}$ | |

Note:

1. Measured at 1.0 MHz and Applied reverse Voltage of 4.0V D.C

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Fig. 1 Forward Current Derating Curve

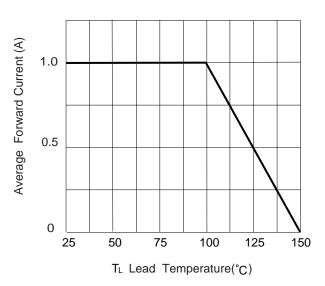


Fig. 3 Max Non-Repetitive Peak Fwd Surge Current

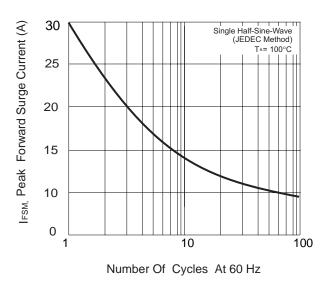


Fig.5 Mounting PAD Layout

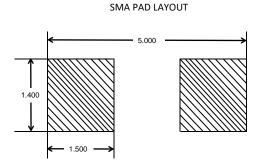
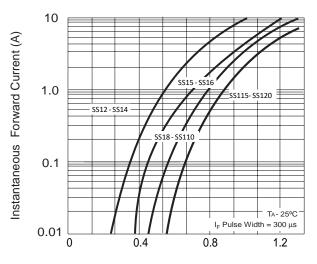
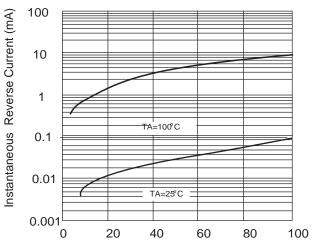


Fig. 2 Typ. Forward Characteristics



V_F, Instantaneous Forward Voltage (V)

Fig.4 Typical Reverse Chracteristics (per element)



Percent Of Rated Peak Reverse Voltage (%)

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