

LESD5L5.0CT1G

Transient Voltage Suppressors

ESD Protection Diodes with Ultra–Low Capacitance

The ESD5L is designed to protect voltage sensitive components that require ultra-low capacitance from ESD and transient voltage events. Excellent clamping capability, low capacitance, low leakage, and fast response time, make these parts ideal for ESD protection on designs where board space is at a premium. Because of its low capacitance, it is suited for use in high frequency designs such as USB 2.0 high speed and antenna line applications.

Specification Features:

- Ultra Low Capacitance 0.5 pF
- Low Clamping Voltage
- Small Body Outline Dimensions: 0.047" x 0.032" (1.20 mm x 0.80 mm)
- Low Body Height: 0.016" (0.4 mm)
- Stand-off Voltage: 5 V
- Low Leakage
- Response Time is Typically < 1.0 ns
- IEC61000-4-2 Level 4 ESD Protection
- This is a Pb–Free Device

Mechanical Characteristics:

CASE: Void-free, transfer-molded, thermosetting plastic Epoxy Meets UL 94 V–0 **LEAD FINISH:** 100% Matte Sn (Tin)

QUALIFIED MAX REFLOW TEMPERATURE: 260°C

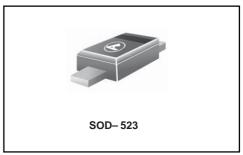
Device Meets MSL 1 Requirements

MAXIMUM RATINGS

Rating	Symbol	Value	Unit	
IEC 61000-4-2 (ESD) Cont	act Air		±10 ±15	kV
Peak Pulse Power (tp= 8/20us)	P _{PP}	100	W	
Storage Temperature Range	T _{stg}	-55 to +150	°C	
Junction Temperature Range		TJ	-55 to +125	°C
Lead Solder Temperature – Maximum (10 Second Duration)	1	ΤL	260	°C

Stresses exceeding Maximum Ratings may damage the device. Maximum Ratings are stress ratings only. Functional operation above the Recommended Operating Conditions is not implied. Extended exposure to stresses above the Recommended Operating Conditions may affect device reliability.

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Ordering information

Device	Marking	Shipping	
LESD5L5.0CT1G	L5	3000/Tape&Reel	
LESD5L5.0CT5G	L5	8000/Tape&Reel	

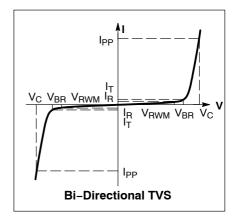


ELECTRICAL CHARACTERISTICS

(T_A = 25°C unless otherwise noted)

Symbol	Parameter					
I _{PP}	Maximum Reverse Peak Pulse Current					
V _C	Clamping Voltage @ IPP					
V _{RWM} Working Peak Reverse Voltage						
I _R	Maximum Reverse Leakage Current @ V _{RWM}					
V _{BR}	Breakdown Voltage @ I _T					
Ι _Τ	Test Current					
١ _F	Forward Current					
V _F	Forward Voltage @ I _F					
P _{pk}	Peak Power Dissipation					
С	Capacitance @ V_R = 0 and f = 1.0 MHz					

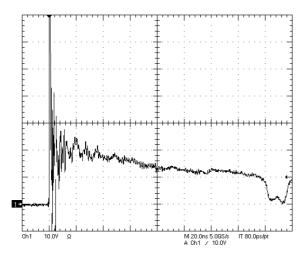
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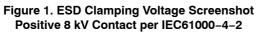


ELECTRICAL CHARACTERISTICS (T_A = 25°C unless otherwise noted)

		V _{RWM} (V)	I _R (μΑ) @ V _{RWM}	V _{BR} (V) @ I _T (Note 1)	ե	C (pF)		V _C (V) @ Ipp = 1 A (Note 2)	v _c
Device	Device Marking	Max	Max	Min	mA	Тур	Max	Max	Per IEC61000-4-2 (Note 3)
LESD5L5.0CT1G	L5	5.0	1.0	5.4	1.0	0.5	0.9	12.9	Figures 1 and 2 See Below

1. V_{BR} is measured with a pulse test current I_T at an ambient temperature of 25°C. 2. Surge current waveform per Figure 5. 3. For test procedure see Figures 3 and 4.





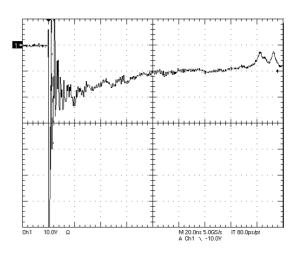


Figure 2. ESD Clamping Voltage Screenshot Negative 8 kV Contact per IEC61000-4-2



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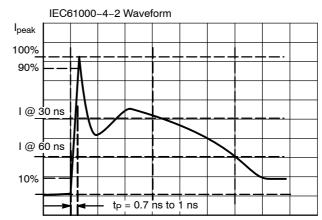
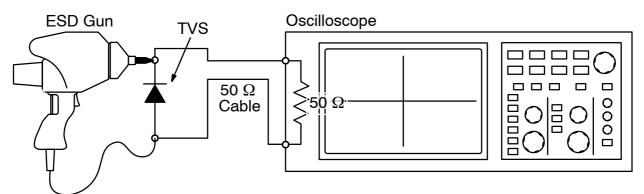
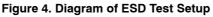
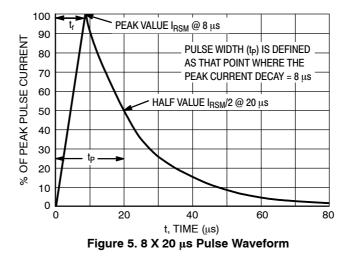


Figure 3. IEC61000-4-2 Spec







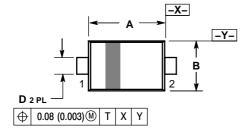
IEC 61000-4-2 Spec.

First Peak Test Voltage Current Current at Current at Level (kV) (A) 30 ns (A) 60 ns (A) 1 2 7.5 4 2 2 4 15 8 4 3 6 22.5 12 6 4 30 8 8 16



LESD5L5.0CT1G

SOD-523



С

K→

S-

J.

-T-

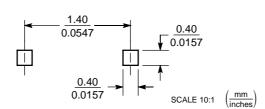
SEATING PLANE



- NOTES: 1. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982. 2. CONTROLLING DIMENSION: MILLIMETER. 3. MAXIMUM LEAD THICKNESS INCLUDES LEAD FINISH THICKNESS. MINIMUM LEAD THICKNESS IS THE MINIMUM THICKNESS OF BASE MATERIAL.

	м	LLIMETE	RS	INCHES			
DIM	MIN	NOM	MAX	MIN	NOM	MAX	
Α	1.10	1.20	1.30	0.043	0.047	0.051	
В	0.70	0.80	0.90	0.028	0.032	0.035	
С	0.50	0.60	0.70	0.020	0.024	0.028	
D	0.25	0.30	0.35	0.010	0.012	0.014	
J	0.07	0.14	0.20	0.0028	0.0055	0.0079	
к	0.15	0.20	0.25	0.006	0.008	0.010	
S	1.50	1.60	1.70	0.059	0.063	0.067	





SOLDERING FOOTPRINT*



DISCLAIMER

- Curve guarantee in the specification. The curve of test items with electric parameter is used as quality guarantee. The curve of test items without electric parameter is used as reference only.
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