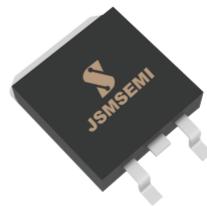


## Description:

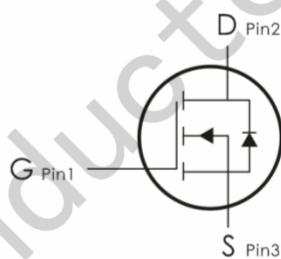
This N-Channel MOSFET uses advanced trench technology and design to provide excellent  $R_{DS(on)}$  with low gate charge. It can be used in a wide variety of applications.



TO-263

## Features:

- 1)  $V_{DS}=100V, I_D=60A, R_{DS(ON)}<17.5\text{ m}\Omega @ V_{GS}=10V$
- 2) Low gate charge.
- 3) Green device available.
- 4) Advanced high cell density trench technology for ultra  $R_{DS(ON)}$ .
- 5) Excellent package for good heat dissipation.



## Absolute Maximum Ratings: ( $T_c=25^\circ\text{C}$ unless otherwise noted)

Symbol	Parameter	Ratings	Units
$V_{DS}$	Drain-Source Voltage	100	V
$V_{GS}$	Gate-Source Voltage	$\pm 20$	V
$I_D$	Continuous Drain Current- $T_C=25^\circ\text{C}$	60	A
	Continuous Drain Current- $T_C=100^\circ\text{C}$	40	
$I_{DM}$	Pulsed Drain Current	160	
$E_{AS}$	Single Pulse Avalanche Energy <sup>5</sup>	580	mJ
$P_D$	Power Dissipation	160	W
$T_J, T_{STG}$	Operating and Storage Junction Temperature Range	-55 to +150	$^\circ\text{C}$

## Thermal Characteristics:

Symbol	Parameter	Max	Units
$R_{eJC}$	Thermal Resistance,Junction to Case <sup>2</sup>	0.94	$^\circ\text{C}/\text{W}$

Electrical Characteristics: ( $T_C=25^\circ\text{C}$  unless otherwise noted)

Symbol	Parameter	Conditions	Min	Typ	Max	Units
<b>Off Characteristics</b>						
$\text{BV}_{\text{DSS}}$	Drain-Source Breakdown Voltage	$V_{\text{GS}}=0\text{V}, I_D=250 \mu\text{A}$	100	110	---	V
$I_{\text{DSS}}$	Zero Gate Voltage Drain Current	$V_{\text{GS}}=0\text{V}, V_{\text{DS}}=100\text{V}$	---	---	1	$\mu\text{A}$
$I_{\text{GSS}}$	Gate-Source Leakage Current	$V_{\text{GS}}=\pm 20\text{V}, V_{\text{DS}}=0\text{A}$	---	---	$\pm 100$	nA
<b>On Characteristics</b>						
$V_{\text{GS}(\text{th})}$	GATE-Source Threshold Voltage <sup>3</sup>	$V_{\text{GS}}=V_{\text{DS}}, I_D=250 \mu\text{A}$	2	3	4	V
$R_{\text{DS}(\text{ON})}$	Drain-Source On Resistance <sup>3</sup>	$V_{\text{GS}}=10\text{V}, I_D=28\text{A}$	---	14.5	17.5	$\text{m}\Omega$
<b>Dynamic Characteristics</b>						
$C_{\text{iss}}$	Input Capacitance <sup>4</sup>	$V_{\text{DS}}=25\text{V}, V_{\text{GS}}=0\text{V}, f=1\text{MHz}$	---	3968	---	pF
$C_{\text{oss}}$	Output Capacitance <sup>4</sup>		---	182.4	---	
$C_{\text{rss}}$	Reverse Transfer Capacitance <sup>4</sup>		---	160	---	
<b>Switching Characteristics</b>						
$t_{\text{d}(\text{on})}$	Turn-On Delay Time <sup>4</sup>	$V_{\text{DD}}=30\text{V}, I_D=2\text{A}, R_{\text{GEN}}=2.5 \Omega, V_{\text{GS}}=10\text{V}$	---	16	---	ns
$t_r$	Rise Time <sup>4</sup>		---	12	---	ns
$t_{\text{d}(\text{off})}$	Turn-Off Delay Time <sup>4</sup>		---	54	---	ns
$t_f$	Fall Time <sup>4</sup>		---	15	---	ns
$Q_g$	Total Gate Charge <sup>4</sup>	$V_{\text{GS}}=10\text{V}, V_{\text{DS}}=30\text{V}, I_D=30\text{A}$	---	146	---	nC
$Q_{\text{gs}}$	Gate-Source Charge <sup>4</sup>		---	29	---	nC
$Q_{\text{gd}}$	Gate-Drain "Miller" Charge <sup>4</sup>		---	57	---	nC
<b>Drain-Source Diode Characteristics</b>						
$V_{\text{SD}}$	Source-Drain Diode Forward Voltage <sup>3</sup>	$V_{\text{GS}}=0\text{V}, I_S=28\text{A}$	---	0.85	1.2	V
$\text{Tr}$	Reverse Recovery Time	$T_j=25^\circ\text{C}, I_{\text{sd}}=28\text{A}, V_{\text{GS}}=0\text{V}$ $dI/dt=500\text{A}/\mu\text{s}$	---	35	---	NS
$Q_{\text{rr}}$	Reverse Recovery Charge		---	58	---	NC
$I_s$	Continuous Drain Current <sup>2</sup>	$V_D=V_G=0\text{V}$	---	60	---	A
$I_{\text{SM}}$	Pulsed Drain Current		---	160	---	A

**Notes:**

1. Repetitive Rating: Pulse width limited by maximum junction temperature.
2. Surface Mounted on FR4 Board,  $t \leq 10$  sec.
3. Pulse Test: Pulse Width  $\leq 300\mu\text{s}$ , Duty Cycle  $\leq 2\%$ .
4. Guaranteed by design, not subject to production
5. EAS condition:  $T_j=25^\circ\text{C}, V_{\text{DD}}=50\text{V}, V_G=10\text{V}, R_g=25\Omega, L=1\text{mH}, I_{\text{AS}}=35\text{A}$

Typical Characteristics: ( $T_c=25^\circ\text{C}$  unless otherwise noted)

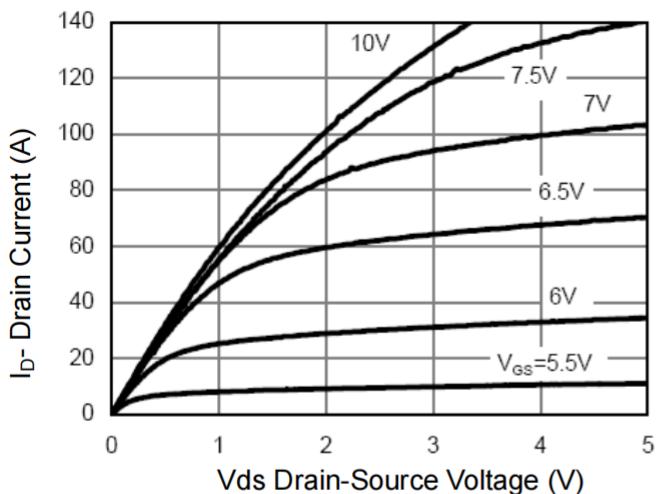


Figure 1 Output Characteristics

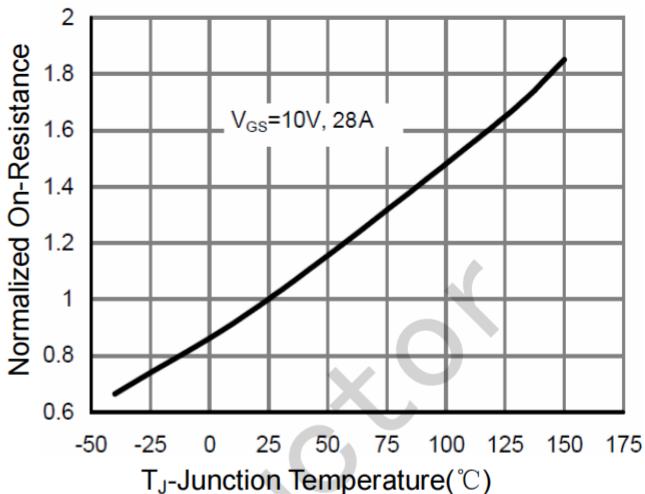


Figure 4 Rdson-Junction Temperature

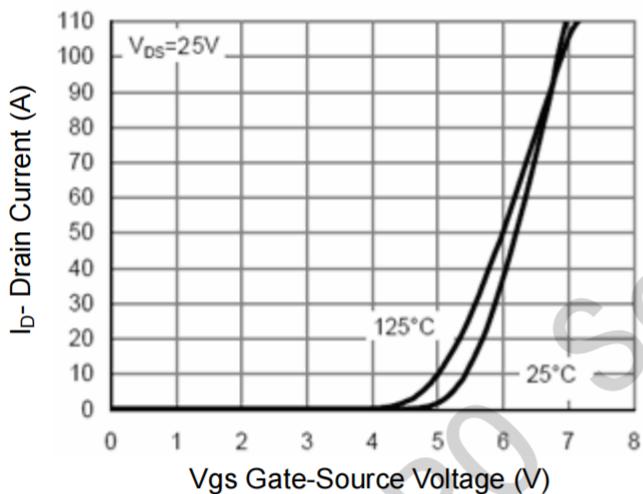


Figure 2 Transfer Characteristics

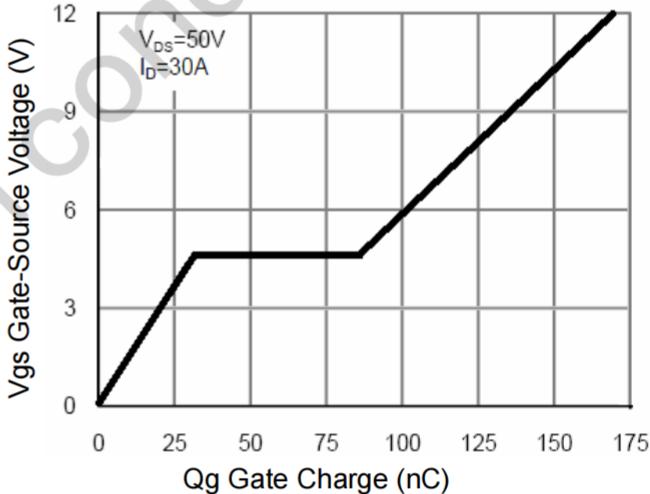


Figure 5 Gate Charge

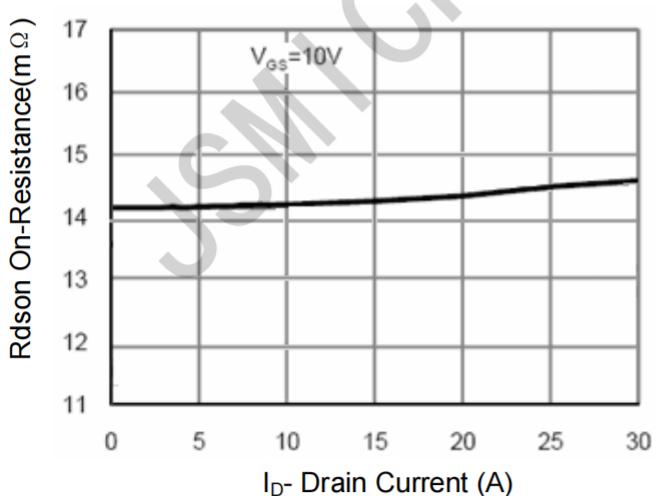


Figure 3 Rdson-Drain Current

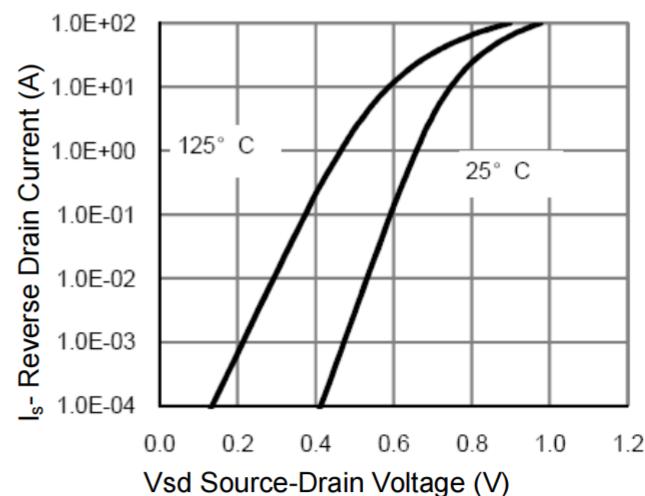
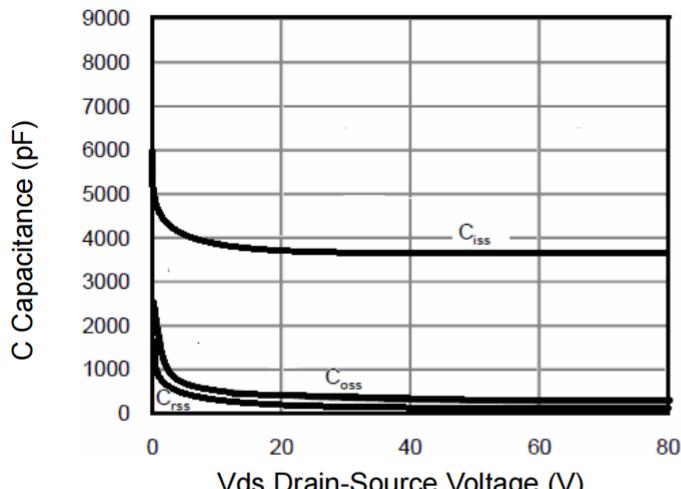
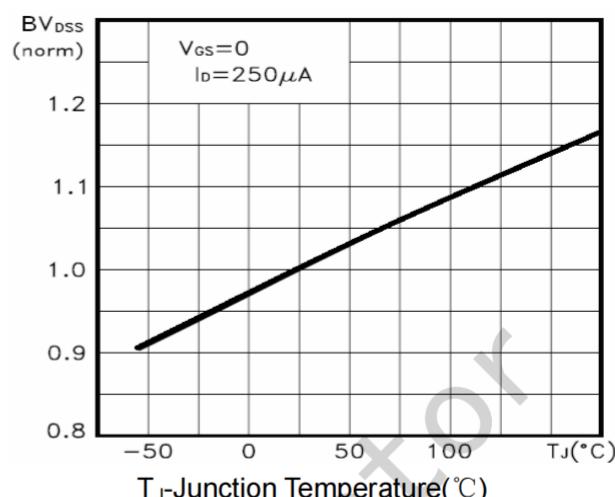


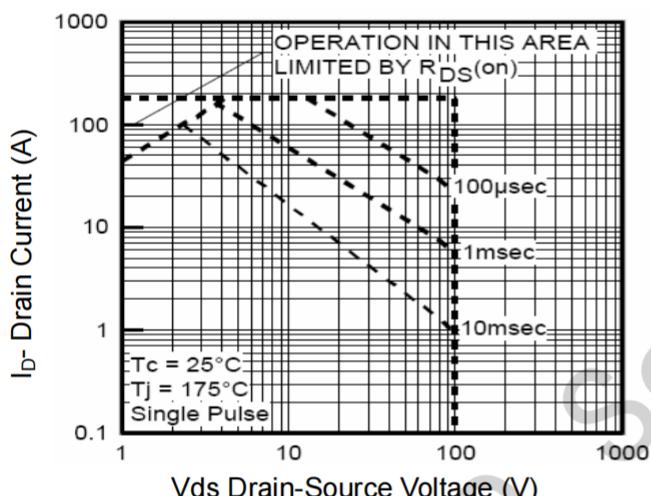
Figure 6 Source-Drain Diode Forward



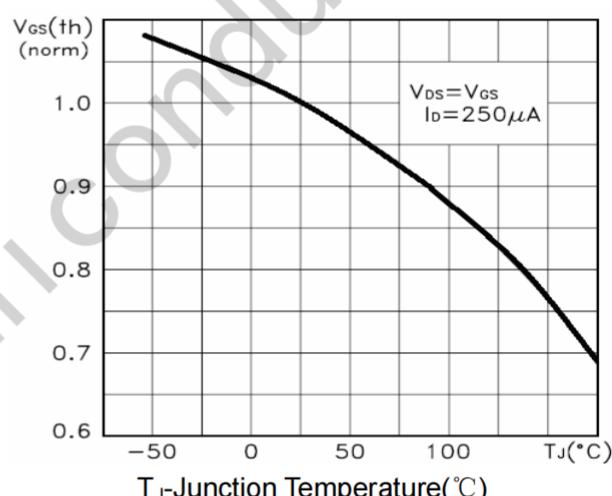
**Figure 7 Capacitance vs Vds**



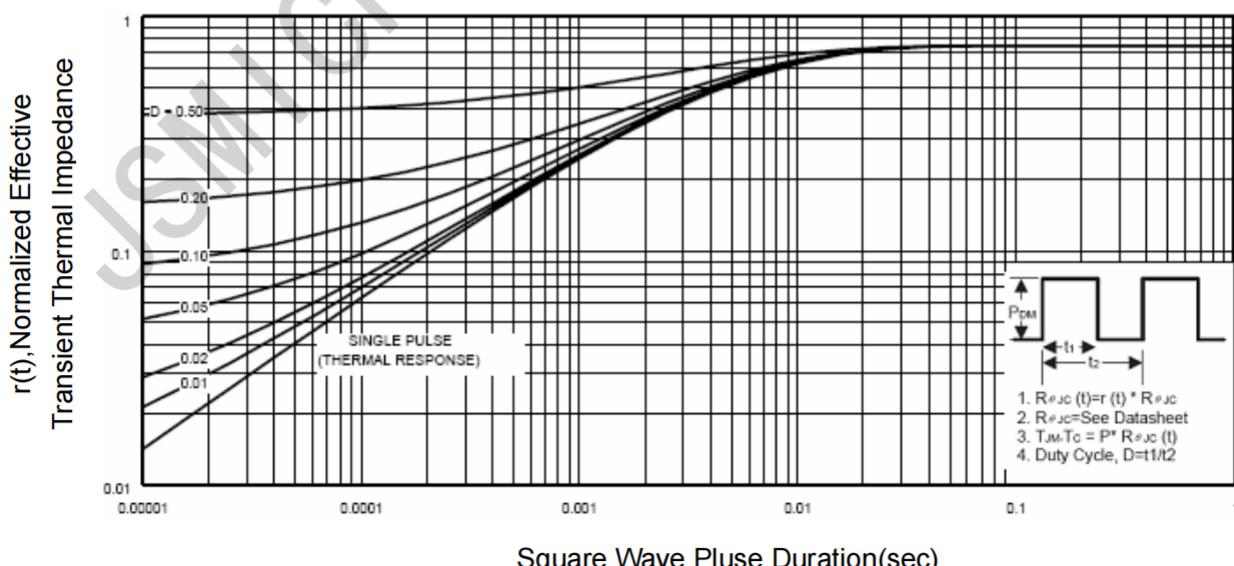
**Figure 9  $BV_{dss}$  vs Junction Temperature**



**Figure 8 Safe Operation Area**



**Figure 10  $V_{GS(th)}$  vs Junction Temperature**



**Figure 11 Normalized Maximum Transient Thermal Impedance**

## Package Information

TO-263

Unit: mm

