

LOC	DIST	REV	DATE	BY	APPD
GP	00	A	29FEB2012	KH	AS

- △ HOUSING: LCP, UL94V0, COLOR: BLACK.
CONTACT: PHOSPHOR BRONZE.
POST: BRASS WIRE
- △ CONTACT: 1.27µm MIN GOLD IN PAD CONTACT AREA
1.27µm MIN TIN-LEAD ON PCB TAIL
OVER 1.27µm MIN NICKEL OVER ALL.
POST 1.27µm MIN NICKEL PLATED.
- △ PLATED THROUGH HOLE REQUIREMENTS
PER 114-13056, FIGURE 4.
- △ SEE TABLE FOR INTERCONNECTIONS TO
BACKPLANE CONNECTOR.

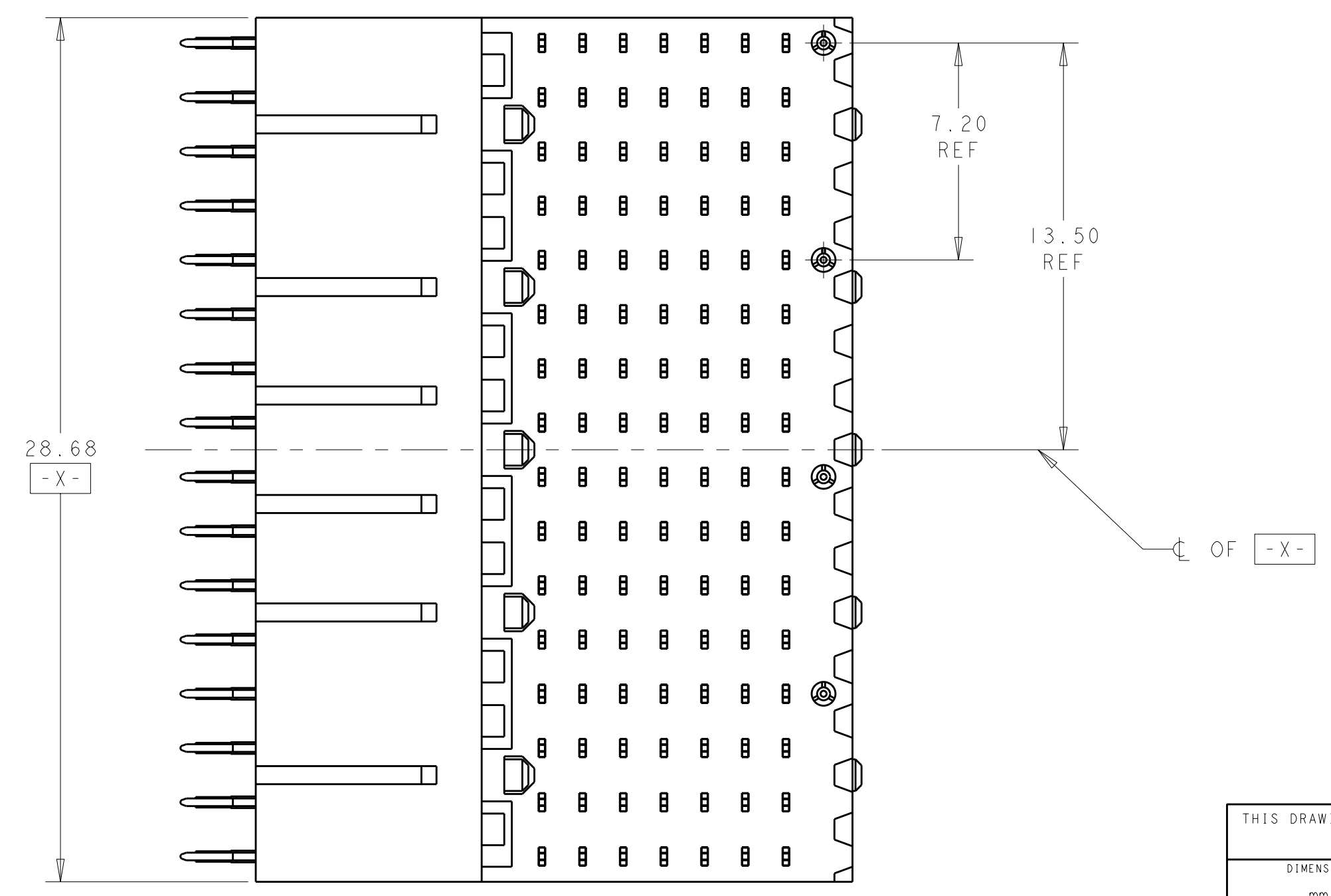
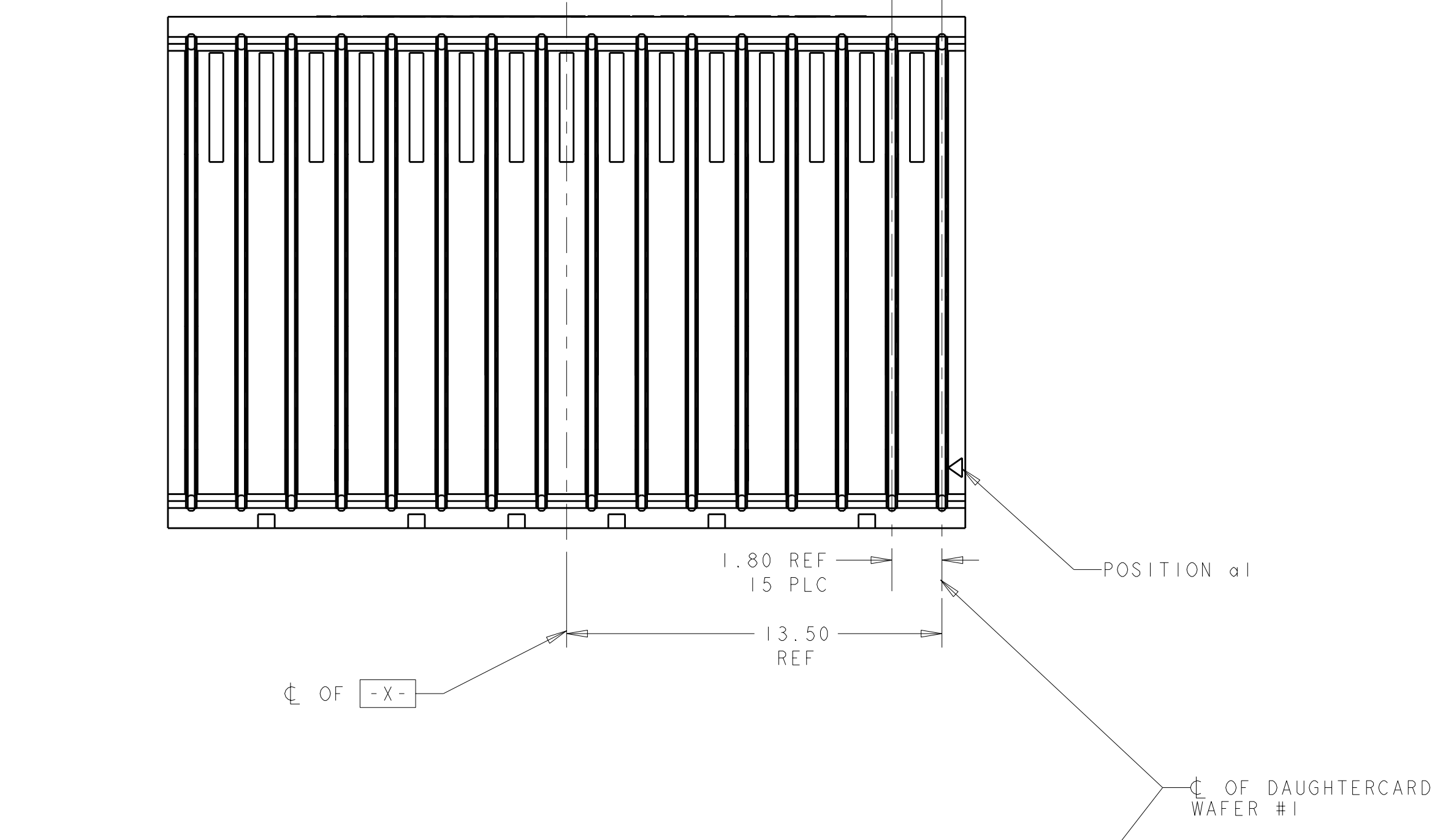
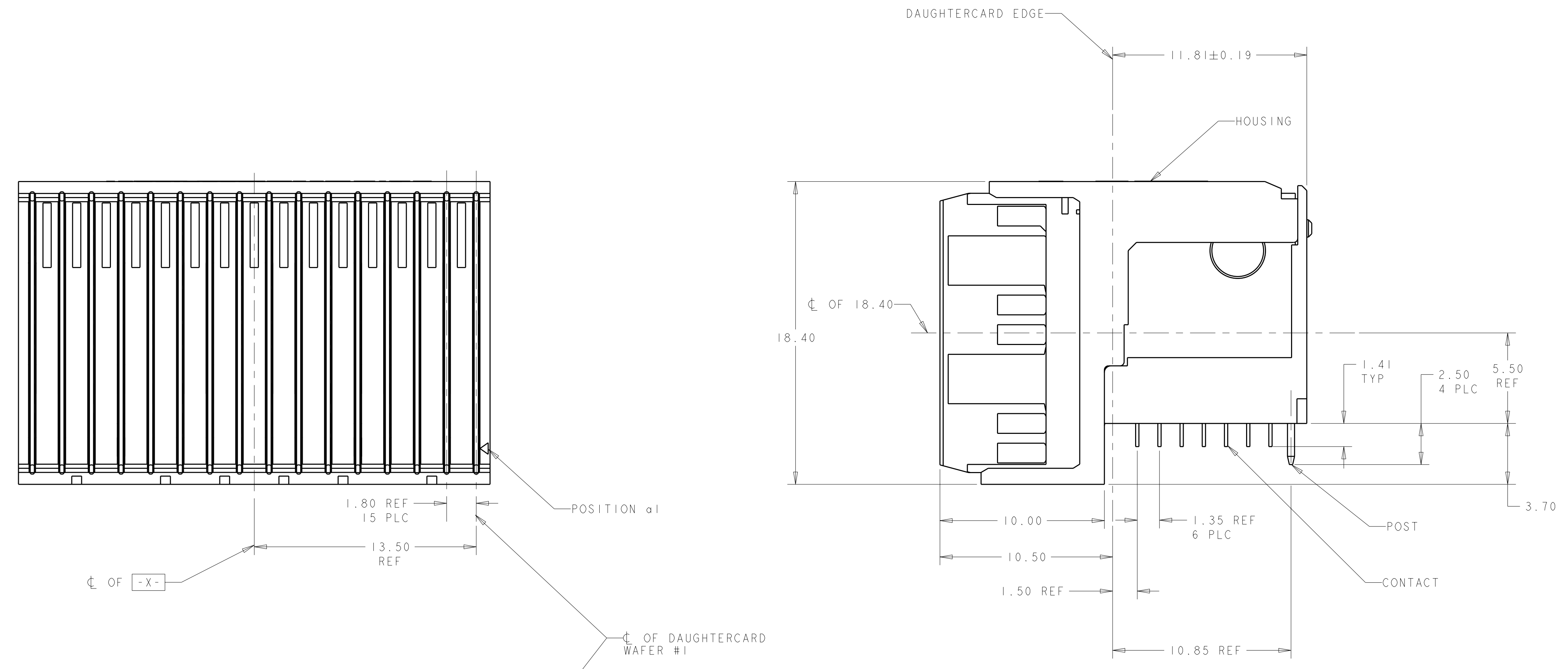
TABLE 1
INTERCONNECTIONS WITH BACKPLANE CONNECTOR 1410140

TYPICAL INTERCONNECTIONS FOR EACH EVEN-NUMBERED COLUMN (WAFER): 2		
CONTACT USAGE	DAUGHTERCARD CONNECTOR PIN	BACKPLANE CONNECTOR PIN
SIGNAL PAIR	bx	cx
SIGNAL PAIR	cx	dx
SIGNAL PAIR	ex	gx
SIGNAL PAIR	fx	hx
GROUND	ax, dx, gx, (ALL COMMONED)	ax, bx, ex, fx, ix

TYPICAL INTERCONNECTIONS FOR EACH ODD-NUMBERED COLUMN (WAFER): 1		
CONTACT USAGE	DAUGHTERCARD CONNECTOR PIN	BACKPLANE CONNECTOR PIN
SIGNAL PAIR	ax	ax
SIGNAL PAIR	bx	bx
SIGNAL PAIR	dx	ex
SIGNAL PAIR	ex	fx
SIGNAL PAIR	gx	ix
GROUND	cx, fx (ALL COMMONED)	cx, dx, gx, hx

TYPICAL INTERCONNECTIONS FOR EACH COLUMN (WAFER): 3-16		
CONTACT USAGE	DAUGHTERCARD CONNECTOR PIN	BACKPLANE CONNECTOR PIN
SIGNAL	ax	bx
SIGNAL	bx	cx
SIGNAL	dx	ex
SIGNAL	fx	gx
SIGNAL	gx	hx
GROUND	cx, ex, (ALL COMMONED)	ax, dx, fx, ix

NOTE: "x" DESIGNATES THE COLUMN NUMBER



PC BOARD LAYOUT (CONNECTOR SIDE) SCALE 5:1

THIS DRAWING IS A CONTROLLED DOCUMENT.

DIMENSIONS:	TOLERANCES UNLESS OTHERWISE SPECIFIED:	DATE: 29FEB2012	NAME: SHARPE ALEXANDER
mm	9 PLC ±0.5 3 PLC ±0.13 5 PLC ±0.13 4 PLC ±0.13 ANGLES ±1°	CHK: KRAN HOLL	PRODUCT SPEC: 108-2072
MATERIAL:	FINISH:	APPD: SHARPE ALEXANDER	APPLICATION SPEC: 114-13056
CUSTOMER DRAWING		SIZE: A100779	DRAWING NO: C=1410367

SCALE 6:1 SHEET 1 OF 1 REV A