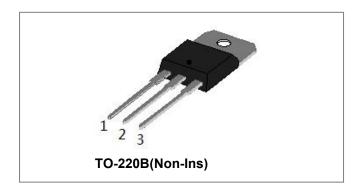


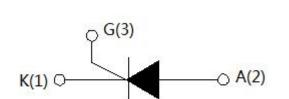




#### SCT640/840 Series 40A SCRs

**Circuit Diagram** 





#### **Description**

With high ability to withstand the shock loading of large current, SCT640/840 provide high dv/dt rate with strong resistance to electromagnetic interference. They are especially recommended for use on solid state relay, motorcycle, power charger, T-tools etc.

#### **Maximum Ratings:**

Characteristics	Symbol	Condition	Max.	Units
Storage junction temperature range	T <sub>stg</sub>	-	-40-150	$^{\circ}$ C
Operating junction temperature range	Tj	-	-40-125	$^{\circ}\!\mathbb{C}$
Repetitive peak off-state voltage(T <sub>j</sub> =25℃)	$V_{DRM}$	-	600/800	V
Repetitive peak reverse voltage(T <sub>j</sub> =25°C)	$V_{RRM}$	-	600/800	V
Non repetitive surge peak off-state voltage	V <sub>DSM</sub>	-	V <sub>DRM</sub> +100	V
Non repetitive peak reverse voltage	$V_{RSM}$	-	V <sub>RRM</sub> +100	V
RMS on-state current	I <sub>(TRMS)</sub>	TO-220B(Non-Ins)(T <sub>C</sub> =85°C)	40	А
Non repetitive surge peak on-state current (tp=10ms)	I <sub>TSM</sub>	-	460	Α
I <sup>2</sup> t value for fusing (tp=10ms)	l²t	-	1060	A <sup>2</sup> s
Critical rate of rise of on-state current $(I_G=2\times I_{GT})$	dl/dt	-	50	A/µs
Peak gate current	I <sub>GM</sub>	-	4	Α
Average gate power dissipation	P <sub>G(AV)</sub>	-	1	W
Peak gate power	P <sub>GM</sub>	-	5	W

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# **Electrical Characteristics**(Tj=25℃ unless otherwise specified)

Symbol	Test Condition		Unit		
	rest Condition	MIN.	TYP.	MAX.	Offic
I <sub>GT</sub>	· V <sub>D</sub> =12V R <sub>L</sub> =33Ω	-	15	35	mA
$V_{GT}$	VD-12V NL-3322	-	1	1.5	V
$V_{GD}$	$V_D=V_{DRM}T_j=125^{\circ}C$ R <sub>L</sub> =3.3K $\Omega$	0.2	-	-	V
lμ	I <sub>G</sub> =1.2I <sub>GT</sub>	-	-	90	mA
I <sub>H</sub>	I <sub>T</sub> =500mA	-	-	75	mA
dV/dt	V <sub>D</sub> =2/3V <sub>DRM</sub> Gate Open T <sub>j</sub> =125°C	200	-	-	V/µs

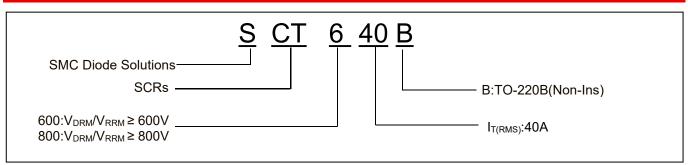
# **Static Characteristics**

Symbol	Condition	Max.	Units
$V_{TM}$	I <sub>T</sub> =80A tp=380μs,Tj=25℃	1.55	V
I <sub>DRM</sub>	$V_D = V_{DRM} V_R = V_{RRM}$ , Tj=25°C	10	μA
I <sub>RRM</sub>	V <sub>D</sub> =V <sub>DRM</sub> V <sub>R</sub> =V <sub>RRM</sub> , Tj=125°C	4	mA

#### **Thermal Resistances**

Symbol	Cond	dition	Value	Units
Rth(j-c)	Junction to case(AC)	TO-220B(Non-Ins)	0.78	°C/W

### **Ordering Information**



Device	Package	Shipping	
SCTX40B	TO-220B(Non-Ins)	50pcs/ Tube	

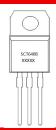
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### **Marking Diagram**



Where XXXXX is YYWWL

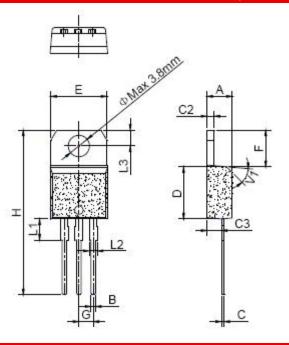
 SCT640B
 = Part name

 YY
 = Year

 WW
 = Week

 L
 = Lot Number

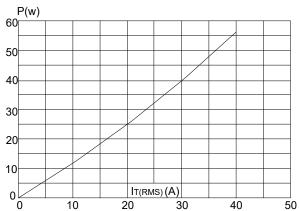
### **Mechanical Dimensions TO-220B(Non-Ins)**



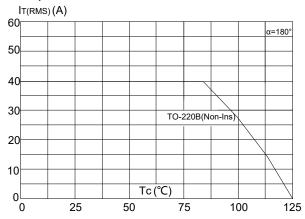
SYMBOL	Millimeters			Inches		
	Min.	Тур.	Max.	Min.	Тур.	Max.
А	4.40		4.60	0.173		0.181
В	0.61		0.88	0.024		0.035
С	0.46		0.70	0.018		0.028
C2	1.21		1.32	0.048		0.052
C3	2.40		2.72	0.094		0.107
D	8.60		9.70	0.339		0.382
E	9.60		10.4	0.378		0.409
F	6.20		6.60	0.244		0.260
G		2.54			0.1	
Н	28.0		29.8	1.102		1.173
L1		3.75			0.148	
L2	1.14		1.70	0.045		0.067
L3	2.65		2.95	0.104		0.116
V1		45°			45°	

#### **Ratings and Characteristics Curves**

**FIG.1:** Maximum power dissipation versus RMS on-state current



**FIG.2:** RMS on-state current versus case temperature



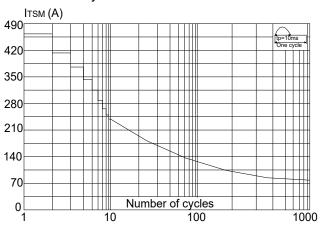
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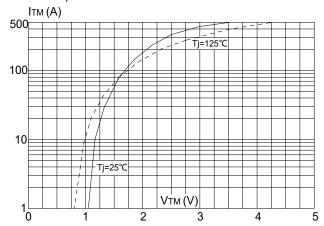




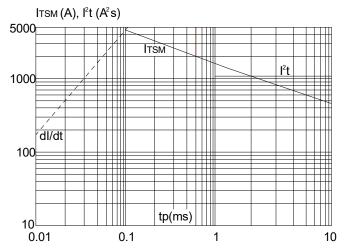
**FIG.3:** Surge peak on-state current versus number of cycles



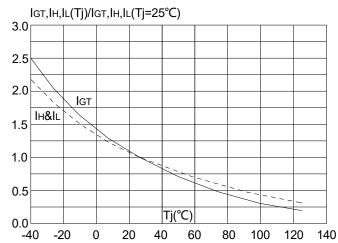
**FIG.4:** On-state characteristics (maximum values)



**FIG.5:** Non-repetitive surge peak on-state current for a sinusoidal pulse with width tp<10ms, and corresponging value of I<sup>2</sup>t (dI/dt < 50A/µs)



**FIG.6:** Relative variations of gate trigger current, holding current and latching current versus junction temperature









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