

1.0 to 7.125 GHz Broad Band SPDT Switch

■ FEATURES

Frequency range
Operation voltage range
1.0 to 7.125 GHz
1.6 to 5.0 V (1.8V typ.)

• Low control voltage 1.8 V typ.

Low insertion loss

0.50 dB typ. @ f = 2.4 to 2.5 GHz, V_{DD} = 1.8 V 0.50 dB typ. @ f = 4.9 to 5.9 GHz, V_{DD} = 1.8 V 0.55 dB typ. @ f = 5.9 to 7.125 GHz, V_{DD} = 1.8 V

High isolation

25dB typ. @ f = 2.4 to 2.5 GHz, V_{DD} = 1.8 V 25dB typ. @ f = 4.9 to 5.9 GHz, V_{DD} = 1.8 V 25dB typ. @ f = 5.9 to 7.125 GHz, V_{DD} = 1.8 V

High linearity

 P_{-1dB} = +31 dBm typ. @ f = 7.125GHz, V_{DD} = 1.8 V

Ultra small & ultra-thin Package

DFN6-75 (1.0 mm x 1.0 mm x 0.375 mm typ.)

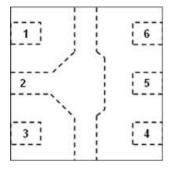
• RoHS compliant and Halogen Free, MSL1

■ APPLICATION

- 802.11a/b/g/n/ac/ax networks
- Wi-Fi Module, Access points, Smartphone and others mobile devices
- Transmit/receive switching, antenna switching and others switching applications

■ BLOCK DIAGRAM (DFN6-75)

(TOP VIEW)



■ GENERAL DESCRIPTION

The NJG1818K75 is 1.8V low operating and control voltage SPDT switch intended for WLAN systems.

The NJG1818K75 features low insertion loss and high isolation for high frequency up to 7.125GHz extended by Wi-Fi 6E. Furthermore, this switch is realized high handling power performance with 1.8 V low operation voltage. Integrated ESD protection devices on each port achieve excellent ESD robustness.

Integrated DC blocking capacitors at all RF ports and the ultra-small package of DFN6-75 offer very small mounting area.

■ TRUTH TABLE

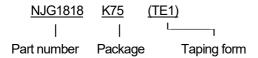
"H" = V_{CTL} (H), "L" = V_{CTL} (L)

ON PATH	VCTL
PC-P1	Н
PC-P2	L

■PIN CONFIGURATION

PIN NO.	SYMBOL	DESCRIPTION
1	P1	RF terminal
2	GND	Ground terminal
3	P2	RF terminal
4	VCTL	Control signal input terminal.
5	PC	Common RF terminal
6 VDD		Voltage supply terminal

■ PRODUCT NAME INFORMATION



■ ORDERING INFORMATION

PART NUMBER	PACKAGE OUTLINE	RoHS	HALOGEN- FREE	TERMINAL FINISH	MARKING	WEIGHT (mg)	MOQ (pcs.)
NJG1818K75	DFN6 -75	Yes	Yes	Ni/Pd/Au	6	1.2	5,000

■ ABSOLUTE MAXIMUM RATINGS

(General conditions: $T_a = +25^{\circ}C$, $Z_s = Z_l = 50 \Omega$)

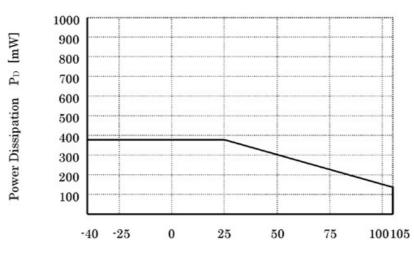
		-	
PARAMETER	SYMBOL	RATINGS	UNIT
RF input power	P _{IN}	+31 ⁽¹⁾	dBm
Supply voltage	V_{DD}	6.0	V
Control voltage	V _{CTL}	6.0	V
Power dissipation ⁽²⁾	P _D	380	mW
Operating temperature	Topr	-40 to +105	°C
Storage temperature	T _{stg}	-55 to +150	°C

^{(1):} $V_{DD} = 1.8 \text{ V}$, ON port

■ POWER DISSIPATION VS.AMBIENT TEMPERATURE

Please, refer to the following Power Dissipation and Ambient Temperature. (Please note the surface mount package has a small maximum rating of Power Dissipation $[P_D]$, a special attention should be paid in designing of thermal radiation.)

Power Dissipation – Ambient Temperature Characteristic Mounted on board



Ambient Temperature Ta [℃]

Nisshinbo Micro Devices Inc.

^{(2):} Four-layer FR4 PCB (76.2 x 114.3 mm, with through-hole), T_i = 150°C

■ ELECTRICAL CHARACTERISTICS 1 (DC CHARACTERISTICS)

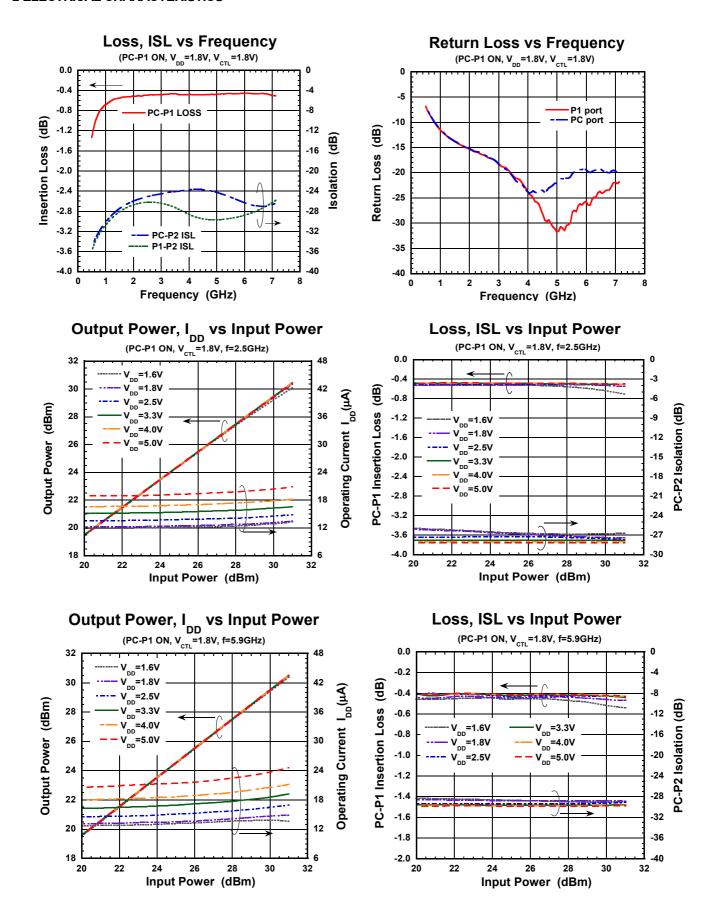
(General conditions: T_a = +25°C, with application circuit)

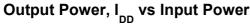
PARAMETER	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNIT
Supply voltage	V _{DD}		1.6	1.8	5.0	V
Operating current	I _{DD}	No RF input, V _{DD} = 1.8 V	-	15	30	μΑ
Control voltage (LOW)	Vctl (L)		0	ı	0.45	V
Control voltage (HIGH)	V _{CTL} (H)		1.35	1.8	5.0	V
Control current	Iсть	V _{CTL} (H) = 1.8 V	-	4	10	μA

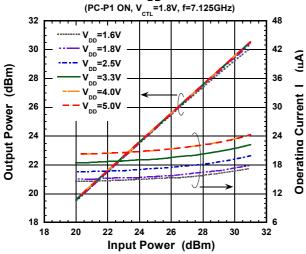
■ ELECTRICAL CHARACTERISTICS 2 (RF CHARACTERISTICS)

(General conditions: $T_a = +25$ °C, $Z_s = Z_I = 50 \Omega$, $V_{DD} = 1.8 \text{ V}$, V_{CTL} (H) = 1.8V, V_{CTL} (L) = 0V, with application circuit)

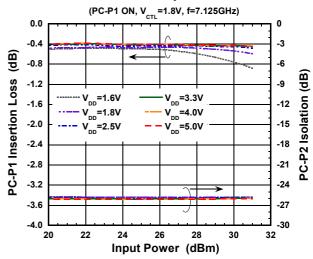
PARAMETER	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNIT	
Insertion loss		f = 2.4 to 2.5 GHz	-	0.50	0.70		
	LOSS	f = 4.9 to 5.9 GHz	-	0.50	0.70	dB	
		f = 5.9 to 7.125 GHz	-	0.55	0.75		
		f = 2.4 to 2.5 GHz	23	25	-		
Isolation	ISL	f = 4.9 to 5.9 GHz	22	25	-	dB	
		f = 5.9 to 7.125 GHz	22	25	-		
		f = 2.4 to 2.5 GHz	13	16	-		
Return loss	RL	f = 4.9 to 5.9 GHz	14	19	-	dB	
		f = 5.9 to 7.125 GHz	14	19	-		
Input power at 1dB compression point	P _{-1dB}	f = 2.4 to 7.125 GHz	+28	+31	-	dBm	
Switching time	Tsw	50% V _{CTL} to 10/90% RF	-	200	400	ns	



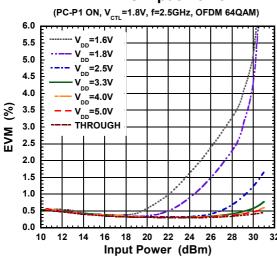




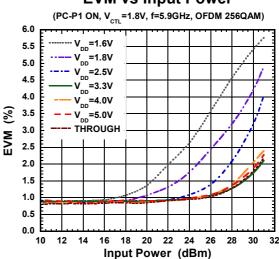
Loss, ISL vs Input Power



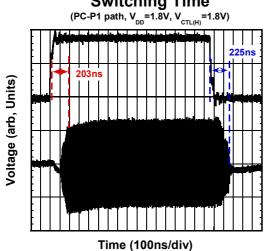
EVM vs Input Power

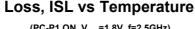


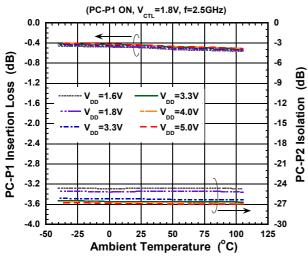
EVM vs Input Power



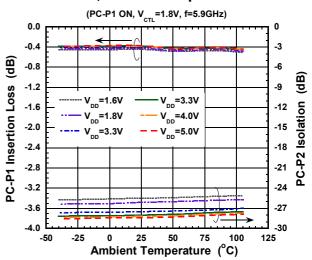
Switching Time



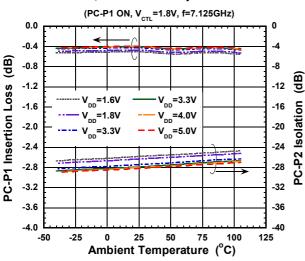




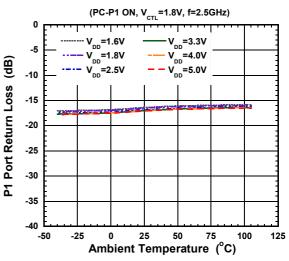
Loss, ISL vs Temperature



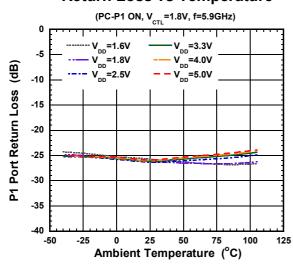
Loss, ISL vs Temperature



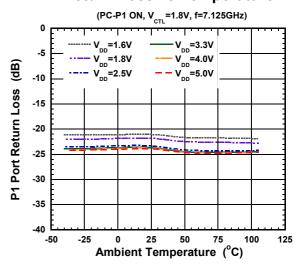
Return Loss vs Temperature

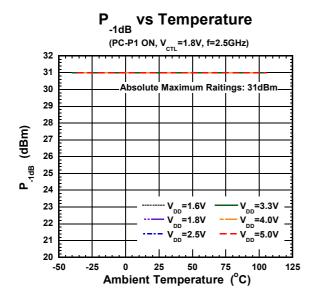


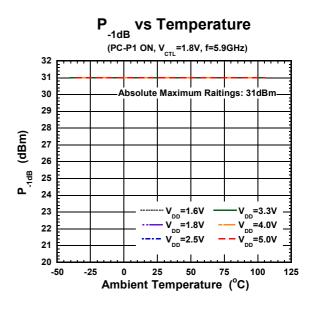
Return Loss vs Temperature

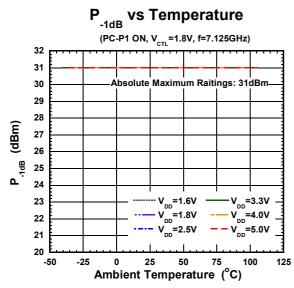


Return Loss vs Temperature

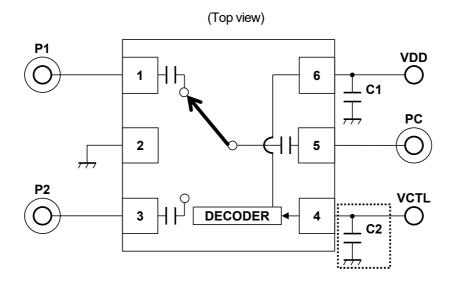








■ APPLICATION CIRCUIT



NOTE:

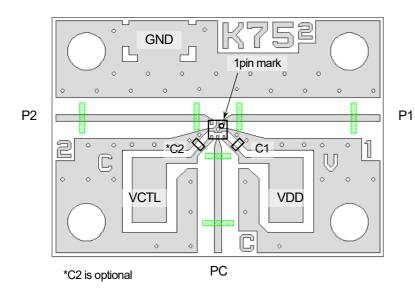
The bypass capacitor C2 is optional, and is recommended only when the control line is affected under noisy environment.

■ PARTS LIST

Part ID	Value	Notes
C1	1000 pF	MURATA MFG
C2	10 pF	(GRM03 Series)

■ EVALUATION BOARD

(TOP VIEW)



PCB: FR-4, t = 0.2 mm

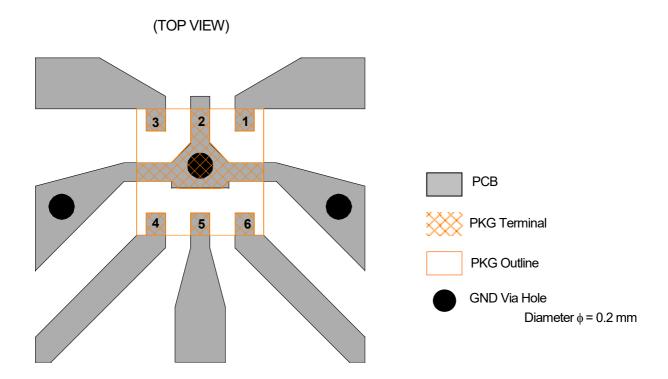
Capacitor size: 0603 (0.6 x 0.3 mm)

Strip line width: 0.4 mm PCB size: 19.4 x 14.0 mm Through hole diameter: 0.2 mm

■ Loss of PCB and connectors

Frequency (GHz)	Loss (dB)
2.4	0.38
2.5	0.39
4.9	0.61
5.9	0.77
7.125	0.85

■ PCB LAYOUT GUIDELINE



PRECAUTIONS

For good RF performance, exposed pad should be connected to PCB ground plane as close as possible.

■ RECOMMENDED FOOTPRINT PATTERN (DFN6-75) <Reference>

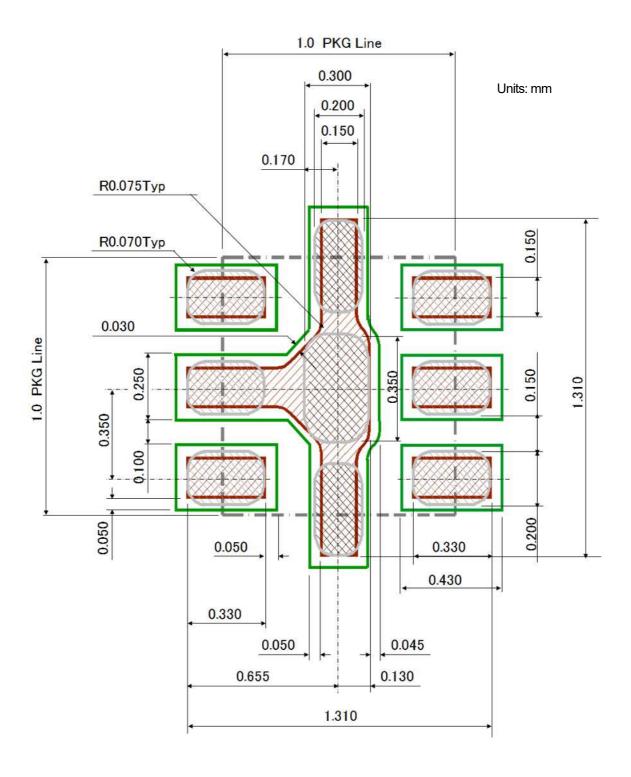
Package: 1.0 mm x 1.0 mm

: Land

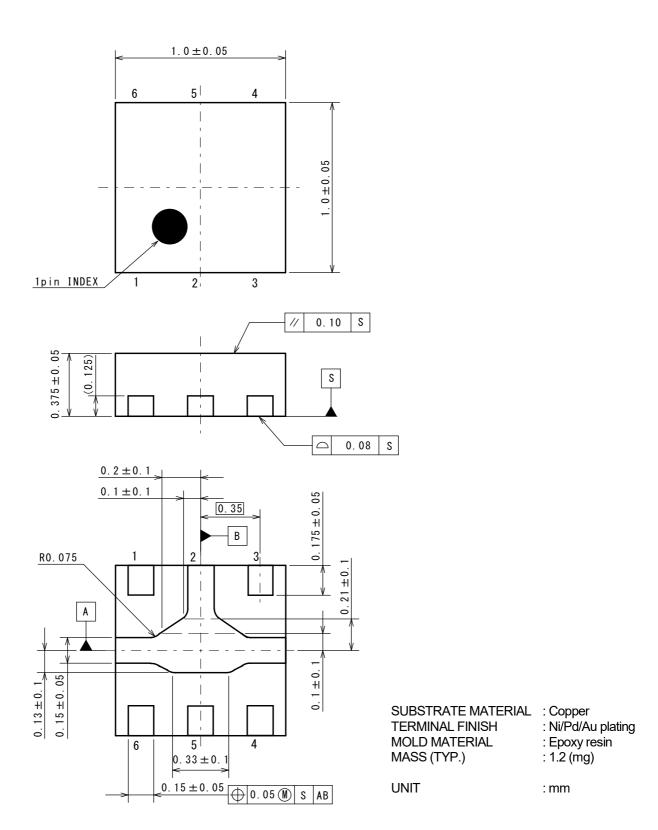
Pin pitch: 0.35 mm

: Mask (Open area) *Metal mask thickness: 100 μm

: Resist (Open area)

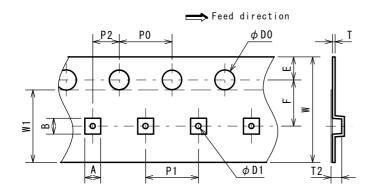


■PACKAGE OUTLINE (DFN6-75)



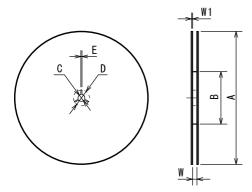
■ PACKING SPECIFICATION (DFN6-75)

TAPING DIMENSIONS



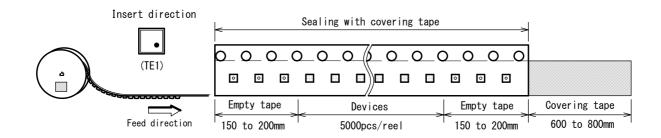
SYMBOL	DIMENSION	REMARKS
A	1. 19 ^{+0. 04} -0. 01	BOTTOM DIMENSION
В	1. 19 +0. 04	BOTTOM DIMENSION
D0	1.5 +0.1	
D1	0.5±0.05	
E	1.75±0.1	
F	3.5±0.05	
P0	4.0±0.1	
P1	4.0±0.1	
P2	2.0±0.05	
T	0.18±0.05	
T2	0.69 ± 0.1	
W	8.0±0.1	
W1	5.5±0.1	THICKNESS 60 μ max

REEL DIMENSIONS

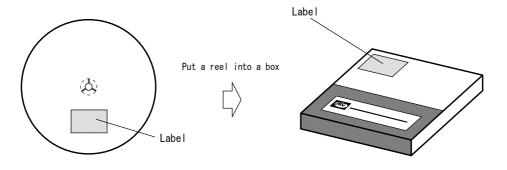


SYMBOL	DIMENSION	
A	ϕ 180 $_{-3}^{0}$	
В	ϕ 60 $^{+1}_{0}$	
С	φ 13±0.2	
D	ϕ 21±0.8	
Е	2±0.5	
W	9±0.3	
W1	1. 2	

TAPING STATE



PACKING STATE



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