Old Company Name in Catalogs and Other Documents

On April 1st, 2010, NEC Electronics Corporation merged with Renesas Technology Corporation, and Renesas Electronics Corporation took over all the business of both companies. Therefore, although the old company name remains in this document, it is a valid Renesas Electronics document. We appreciate your understanding.

Renesas Electronics website: http://www.renesas.com

April 1st, 2010 Renesas Electronics Corporation

Issued by: Renesas Electronics Corporation (http://www.renesas.com)

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RENESAS

JUNCTION FIELD EFFECT TRANSISTOR 2SK4028

N-CHANNEL SILICON JUNCTION FIELD EFFECT TRANSISTOR FOR IMPEDANCE CONVERTER OF ECM

DESCRIPTION

The 2SK4028 is suitable for converter of ECM.

FEATURES

High gain

-1.0 dB (V_{DD} = 2.0 V, C = 5 pF, R_L = 2.2 k Ω)

- Low noise
 - -115 dB (V_{DD} = 2.0 V, C = 5 pF, R_L = 2.2 k Ω)
- Ultra thin thickness package
- t = 0.3 mm TYP.

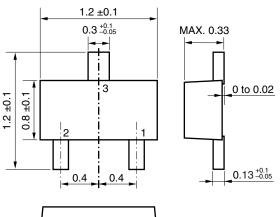
ORDERING INFORMATION

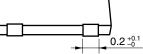
PART NUMBER	PACKAGE
2SK4028	3pXSOF03 (0812)

ABSOLUTE MAXIMUM RATINGS ($T_A = 25^{\circ}C$)

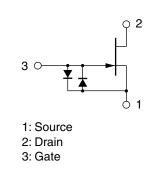
Drain to Source Voltage (V _{GS} = -1.0 V)	VDSX	20	V
Gate to Drain Voltage	Vgdo	-20	V
Drain Current	lо	10	mA
Gate Current	lg	10	mA
Total Power Dissipation	Р⊤	100	mW
Junction Temperature	Tj	125	°C
Storage Temperature	Tstg	–55 to +125	°C

PACKAGE DRAWING (Unit: mm)





EQUIVALENT CIRCUIT



Caution Please take care of ESD (Electro Static Discharge) when you handle the device in this document.

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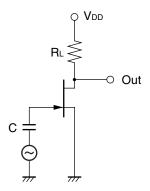
ELECTRICAL CHARACTERISTICS (TA = 25°C)

CHARACTERISTICS	SYMBOL	TEST CONDITIONS MIN.		TYP.	MAX.	UNIT
Zero Gate Voltage Drain Cut-off Current	IDSS	V _{DS} = 2.0 V, V _{GS} = 0 V	90	250	430	μA
Gate Cut-off Voltage	V _{GS(off)}	V _{DS} = 2.0 V, I _D = 1.0 μA		-0.37	-1.0	V
Forward Transfer Admittance	y fs1	V _{DS} = 2.0 V, I _D = 30 <i>µ</i> A, f = 1.0 kHz	320	470		μS
	y fs2	V _{DS} = 2.0 V, V _{GS} = 0 V, f = 1.0 kHz	800	1600		μS
Input Capacitance	Ciss	V _{DS} = 2.0 V, V _{GS} = 0 V, f = 1.0 MHz		4.0		pF
Voltage Gain	Gv	V_{DD} = 2.0 V, C = 5 pF, RL = 2.2 k Ω ,		-1.0		dB
		V⊪ = 10 mV, f = 1 kHz				
Noise Voltage	NV	V_{DD} = 2.0 V, C = 5 pF, RL = 2.2 k Ω ,		-115		dB
		A-curve				

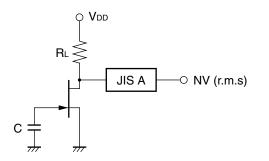
IDSS CLASSIFICATION

MARKING	DE	DF	DH	DJ
loss (µA)	90 to 180	150 to 240	210 to 350	320 to 430

GAIN TEST CIRCUIT



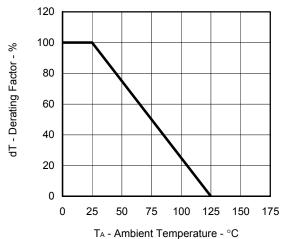
NOISE VOLTAGE TEST CIRCUIT



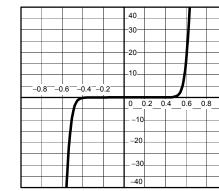
les - Gate to Source Current - µA

TYPICAL CHARACTERISTICS (TA = 25^{\circ}C)

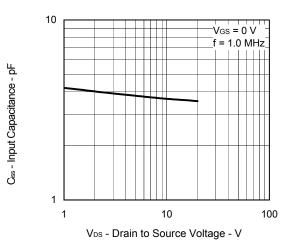
DERATING FACTOR OF POWER DISSIPATION



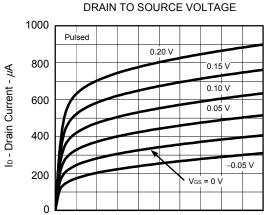
GATE TO SOURCE CURRENT vs. GATE TO SOURCE VOLTAGE



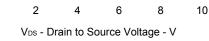
VGS - Gate to Source Voltage - V



INPUT CAPACITANCE vs. DRAIN TO SOURCE VOLTAGE

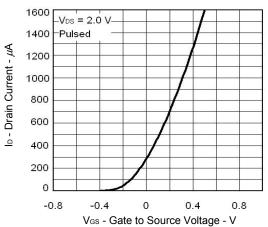


DRAIN CURRENT vs.

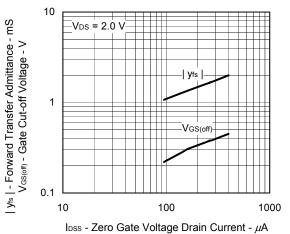


0

DRAIN CURRENT vs. GATE TO SOURCE VOLTAGE

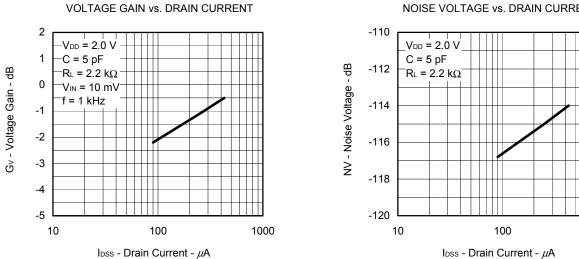


FORWARD TRANSFER ADMITTANCE AND GATE CUT-OFF VOLTAGE vs. ZERO GATE VOLTAGE DRAIN CURRENT



1000





NOISE VOLTAGE vs. DRAIN CURRENT

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