

To our customers,

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## Old Company Name in Catalogs and Other Documents

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Renesas Electronics website: <http://www.renesas.com>

April 1<sup>st</sup>, 2010  
Renesas Electronics Corporation

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## 2SK2059(L), 2SK2059(S)

Silicon N Channel MOS FET

REJ03G0993-0200  
(Previous: ADE-208-1341)

Rev.2.00

Sep 07, 2005

### Application

High speed power switching

### Features

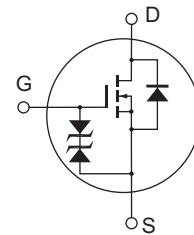
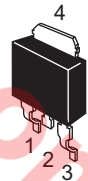
- Low on-resistance
- High speed switching
- No secondary breakdown
- Suitable for switching regulator, DC - DC converter

### Outline

RENESAS Package code: PRSS0004ZD-A  
(Package name: DPAK(L)-(1))



RENESAS Package code: PRSS0004ZD-C  
(Package name: DPAK(S))



1. Gate
2. Drain
3. Source
4. Drain

## Absolute Maximum Ratings

(Ta = 25°C)

Item	Symbol	Ratings	Unit
Drain to source voltage	$V_{DS}$	600	V
Gate to source voltage	$V_{GS}$	$\pm 30$	V
Drain current	$I_D$	3	A
Drain peak current	$I_{D(pulse)}^{*1}$	6	A
Body to drain diode reverse drain current	$I_{DR}$	3	A
Channel dissipation	$P_{ch}^{*2}$	20	W
Channel temperature	$T_{ch}$	150	°C
Storage temperature	$T_{stg}$	-55 to +150	°C

Notes: 1.  $PW \leq 10 \mu s$ , duty cycle  $\leq 1 \%$   
 2. Value at  $T_c = 25^\circ C$

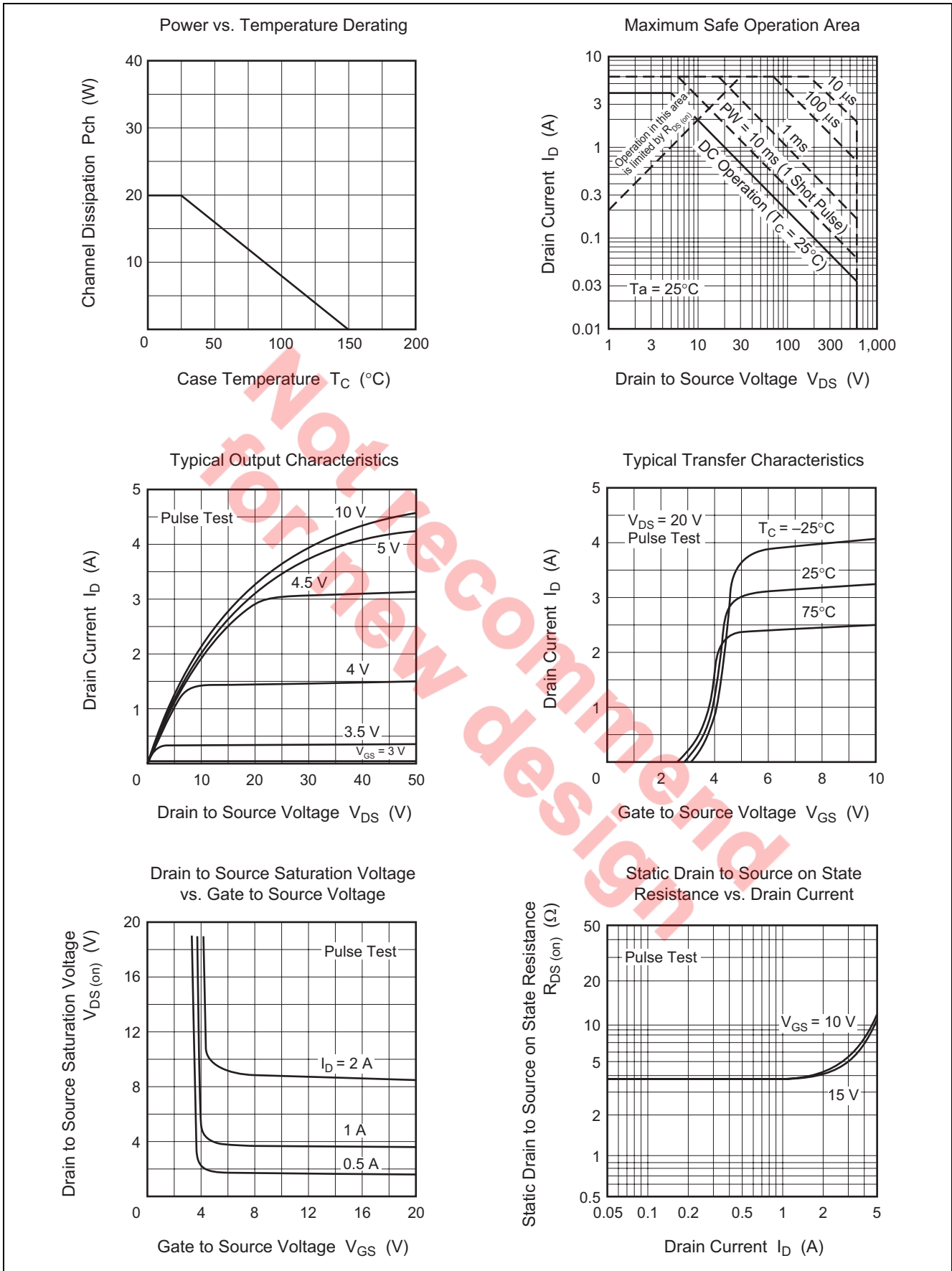
## Electrical Characteristics

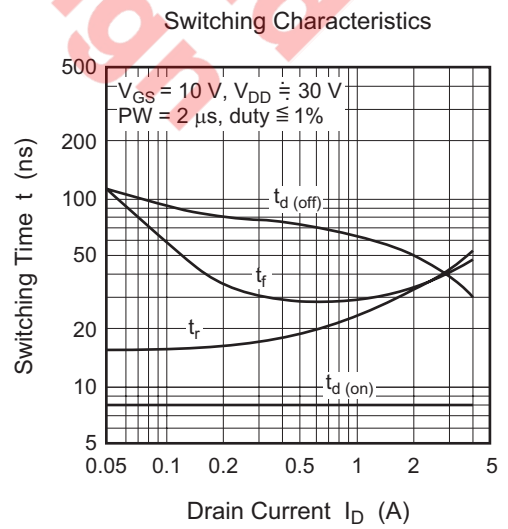
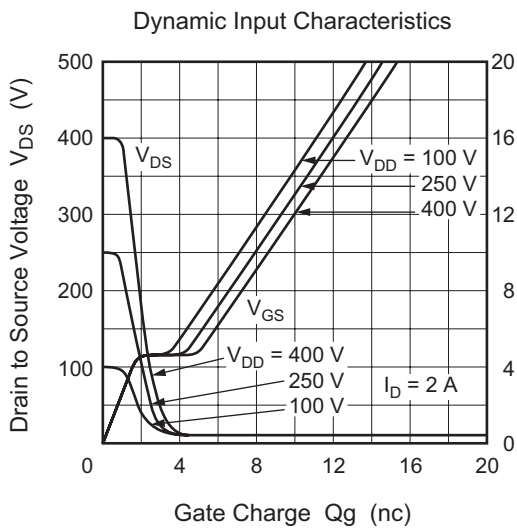
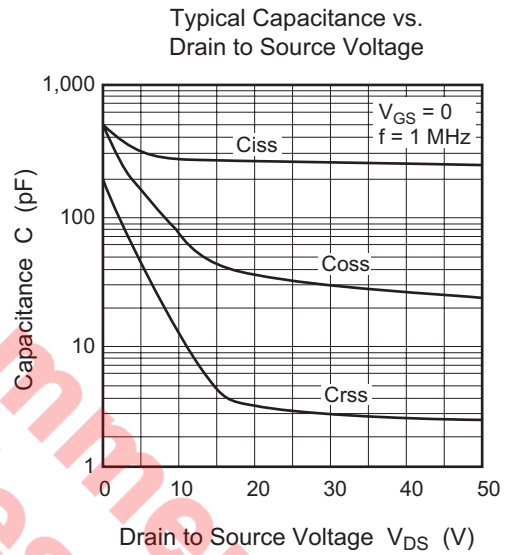
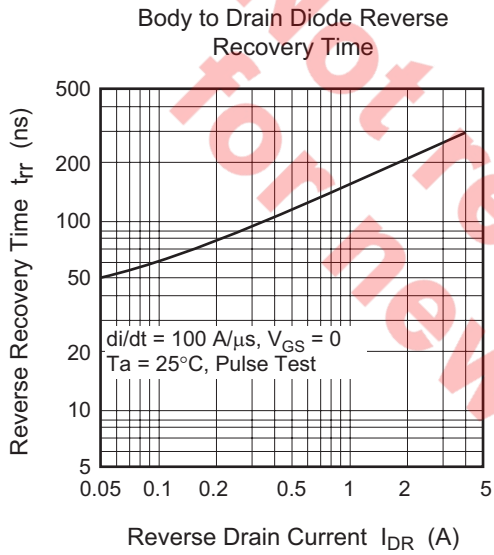
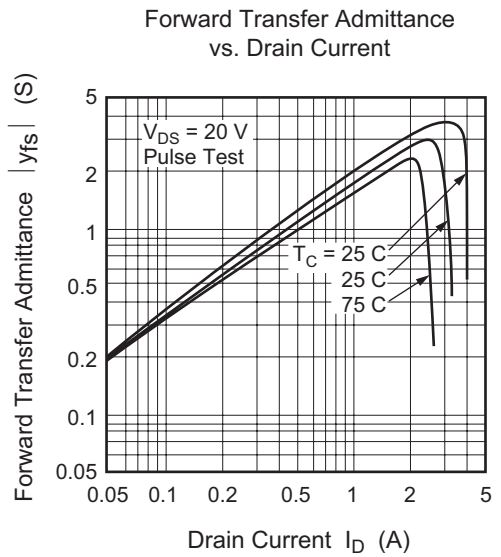
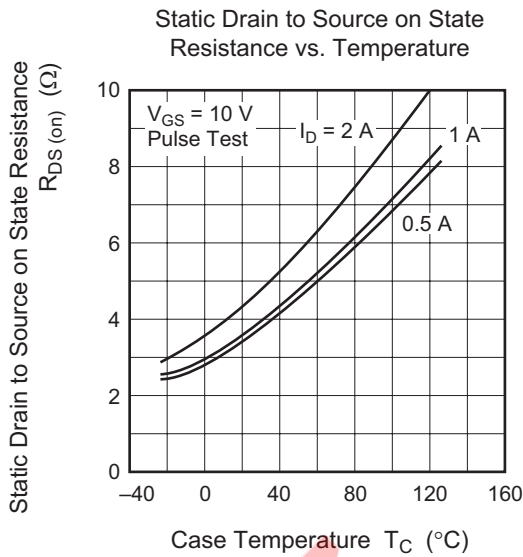
(Ta = 25°C)

Item	Symbol	Min	Typ	Max	Unit	Test conditions
Drain to source breakdown voltage	$V_{(BR)DSS}$	600	—	—	V	$I_D = 10 \text{ mA}$ , $V_{GS} = 0$
Gate to source breakdown voltage	$V_{(BR)GSS}$	$\pm 30$	—	—	V	$I_G = \pm 100 \mu A$ , $V_{DS} = 0$
Gate to source leak current	$I_{GSS}$	—	—	$\pm 10$	$\mu A$	$V_{GS} = \pm 25 \text{ V}$ , $V_{DS} = 0$
Zero gate voltage drain current	$I_{DSS}$	—	—	100	$\mu A$	$V_{DS} = 500 \text{ V}$ , $V_{GS} = 0$
Gate to source cutoff voltage	$V_{GS(off)}$	2.0	—	3.0	V	$I_D = 1 \text{ mA}$ , $V_{DS} = 10 \text{ V}$
Static drain to source on state resistance	$R_{DS(on)}$	—	3.8	5.0	$\Omega$	$I_D = 1 \text{ A}$ , $V_{GS} = 10 \text{ V}^{*3}$
Forward transfer admittance	$ y_{fs} $	1.2	2.0	—	S	$I_D = 1 \text{ A}$ , $V_{DS} = 10 \text{ V}^{*3}$
Input capacitance	$C_{iss}$	—	295	—	pF	$V_{DS} = 10 \text{ V}$ , $V_{GS} = 0$ , $f = 1 \text{ MHz}$
Output capacitance	$C_{oss}$	—	70	—	pF	
Reverse transfer capacitance	$C_{rss}$	—	12	—	pF	
Turn-on delay time	$t_{d(on)}$	—	8	—	ns	$I_D = 1 \text{ A}$ , $V_{GS} = 10 \text{ V}$ , $R_L = 30 \Omega$
Rise time	$t_r$	—	25	—	ns	
Turn-off delay time	$t_{d(off)}$	—	65	—	ns	
Fall time	$t_f$	—	30	—	ns	
Body to drain diode forward voltage	$V_{DF}$	—	0.9	—	V	$I_F = 3 \text{ A}$ , $V_{GS} = 0$
Body to drain diode reverse recovery time	$t_{rr}$	—	220	—	ns	$I_F = 3 \text{ A}$ , $V_{GS} = 0$ , $di_F / dt = 100 \text{ A} / \mu s$

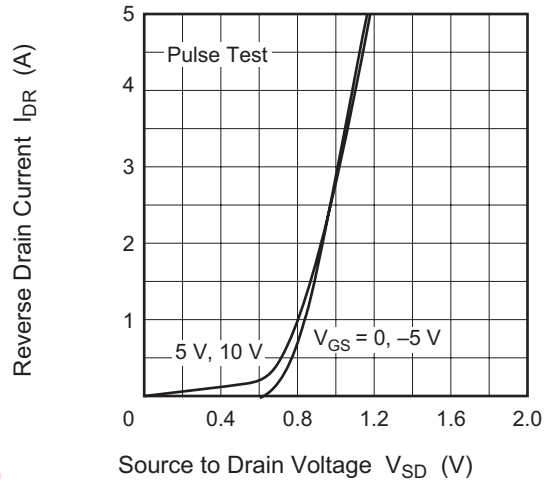
Note: 3. Pulse Test

### Main Characteristics



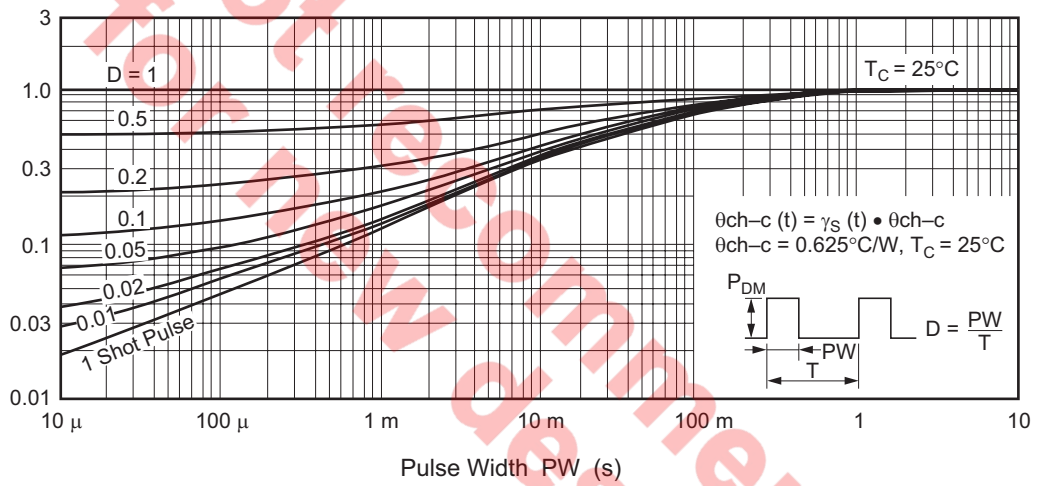


Reverse Drain Current vs. Source to Drain Voltage

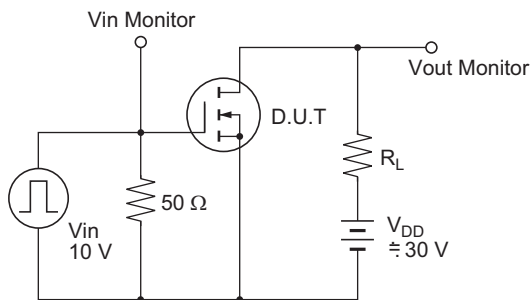


Normalized Transient Thermal Impedance  $\gamma_S(t)$

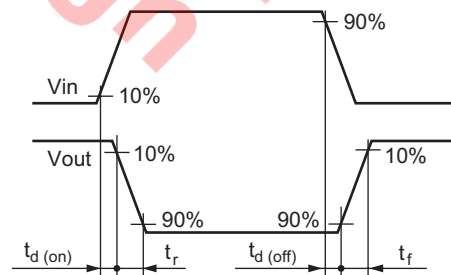
Normalized Transient Thermal Impedance vs. Pulse Width



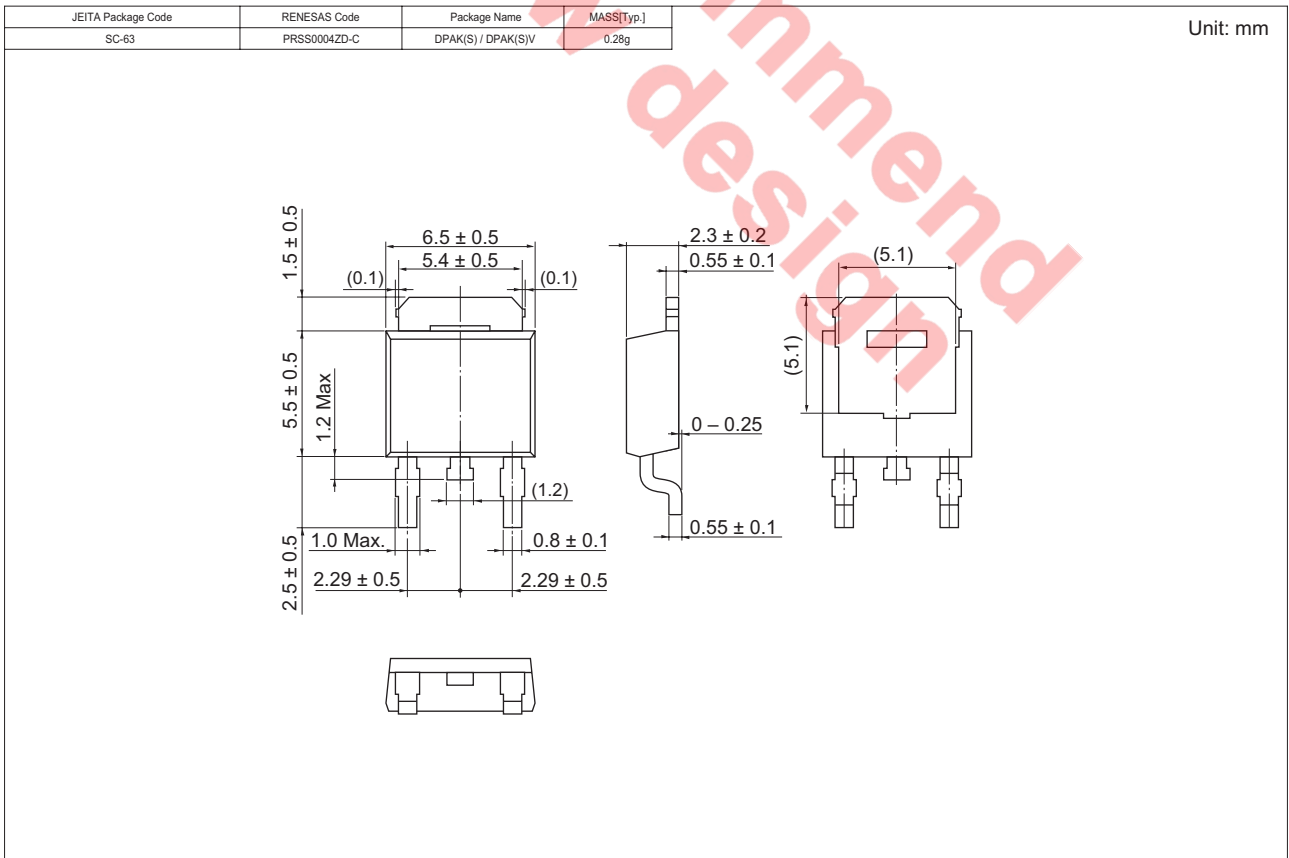
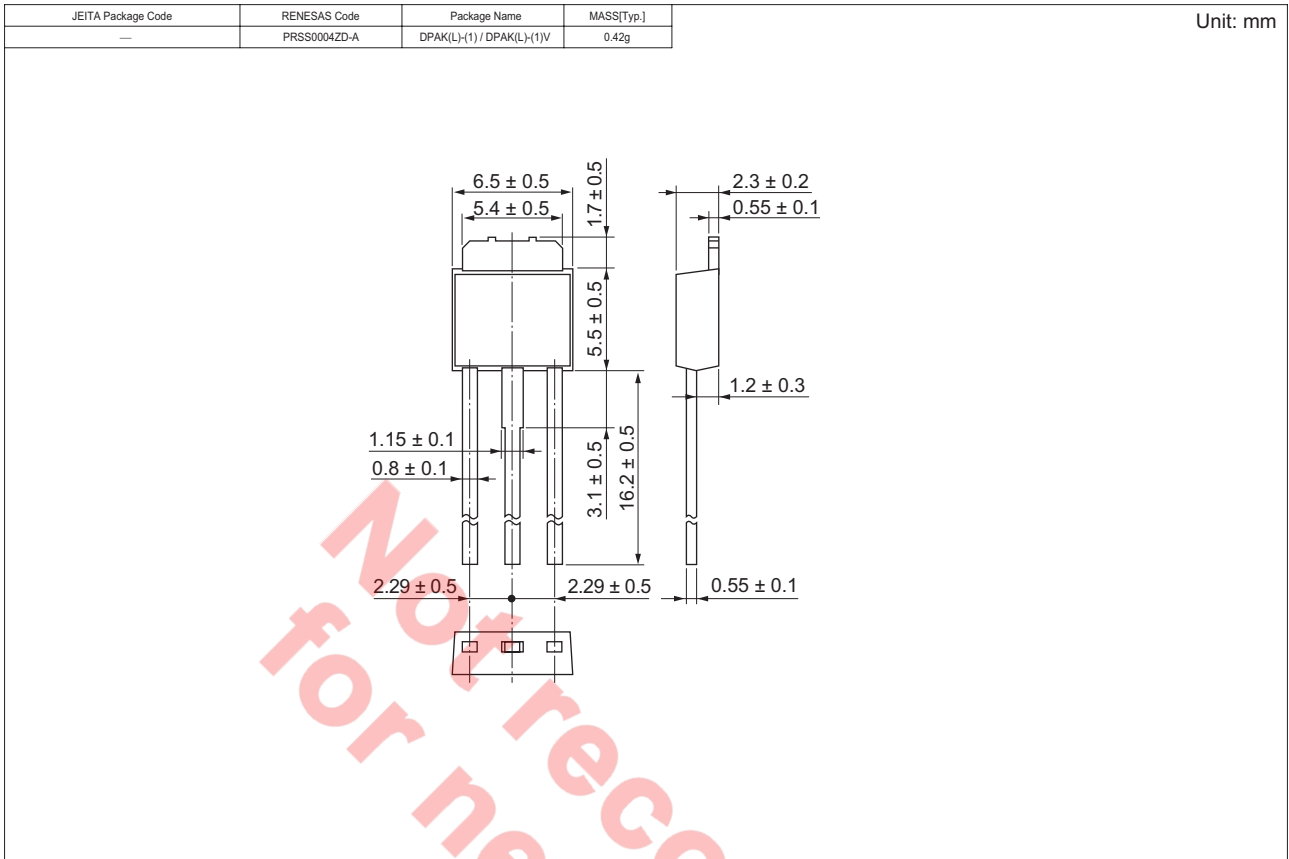
Switching Time Test Circuit



Waveforms



Package Dimensions





### Ordering Information

Part Name	Quantity	Shipping Container
2SK2059L-E	3200 pcs	Box (Sack)
2SK2059STL-E	3000 pcs	Taping

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