To our customers,

Old Company Name in Catalogs and Other Documents

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Renesas Electronics website: http://www.renesas.com

April 1st, 2010
Renesas Electronics Corporation

Issued by: Renesas Electronics Corporation (http://www.renesas.com)

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HAT2054M

Silicon N Channel Power MOS FET
Power Switching

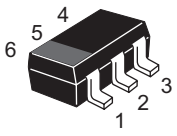
REJ03G1173-0400
(Previous: ADE-208-756B)
Rev.4.00
Sep 07, 2005

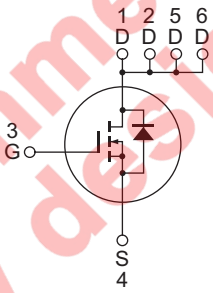
Features

- Low on-resistance
- Low drive current
- High density mounting
- 4.5 V gate drive device can be driven from 5 V source

Outline

RENESAS Package code: PTSP0006FA-A
(Package name: TSOP-6)





4	Source
3	Gate
1, 2, 5, 6	Drain

Absolute Maximum Ratings

(Ta = 25°C)

Item	Symbol	Value	Unit
Drain to source voltage	V_{DSS}	30	V
Gate to source voltage	V_{GSS}	±20	V
Drain current	I_D Note 2	6.3	A
Drain peak current	$I_{D\ (pulse)}$ Note 1	25.2	A
Body to drain diode reverse drain current	I_{DR} Note 2	6.3	A
Channel dissipation	Pch $_{(pulse)}$ Note 2	2.0	W
	Pch $_{(continuous)}$ Note 3	1.05	W
Channel temperature	Tch	150	°C
Storage temperature	Tstg	–55 to +150	°C

Notes: 1. PW ≤ 10 µs, duty cycle ≤ 1%
2. When using the alumina ceramic board (50 × 50 × 0.7 mm), PW ≤ 5 s, Ta = 25°C
3. When using the alumina ceramic board (50 × 50 × 0.7 mm), Ta = 25°C

Electrical Characteristics

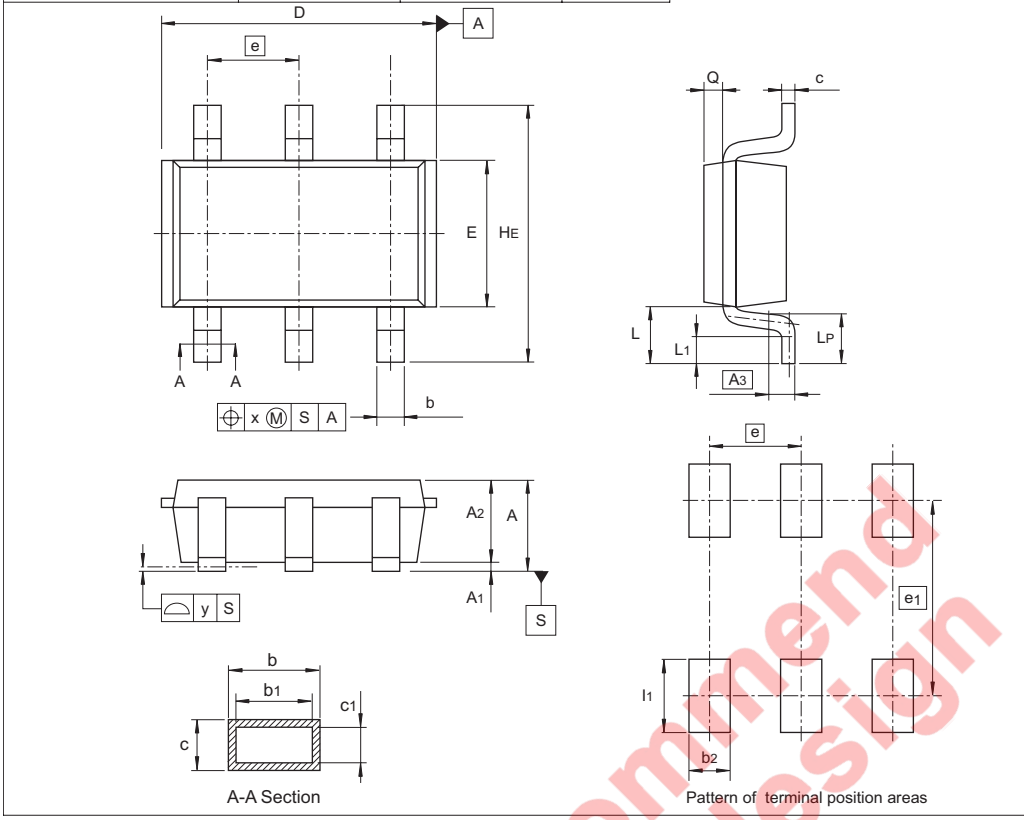
(Ta = 25°C)

Item	Symbol	Min	Typ	Max	Unit	Test Conditions
Drain to source breakdown voltage	$V_{(BR)\ DSS}$	30	—	—	V	I_D = 10 mA, V_{GS} = 0
Gate to source leak current	I_{GSS}	—	—	±0.1	µA	V_{GS} = ±20 V, V_{DS} = 0
Zero gate voltage drain current	I_{DSS}	—	—	1	µA	V_{DS} = 30 V, V_{GS} = 0
Gate to source cutoff voltage	$V_{GS\ (off)}$	1.0	—	2.5	V	V_{DS} = 10 V, I_D = 1 mA
Static drain to source on state resistance	$R_{DS\ (on)}$	—	26	31	mΩ	I_D = 3 A, V_{GS} = 10 V Note 4
	$R_{DS\ (on)}$	—	40	52	mΩ	I_D = 3 A, V_{GS} = 4.5 V Note 4
Forward transfer admittance	$\|y_{fs}\|$	4	7	—	S	I_D = 3 A, V_{DS} = 10 V Note 4
Input capacitance	Ciss	—	620	—	pF	V_{DS} = 10 V
Output capacitance	Coss	—	170	—	pF	V_{GS} = 0
Reverse transfer capacitance	Crss	—	110	—	pF	f = 1 MHz
Turn-on delay time	$t_{d\ (on)}$	—	13	—	ns	V_{GS} = 10 V, I_D = 3 A,
Rise time	t_r	—	90	—	ns	R_L = 3.3 Ω
Turn-off delay time	$t_{d\ (off)}$	—	50	—	ns	
Fall time	t_f	—	40	—	ns	
Body to drain diode forward voltage	V_{DF}	—	0.95	—	V	I_F = 6.3 A, V_{GS} = 0 Note 4
Body to drain diode reverse recovery time	t_{rr}	—	(50)	—	ns	I_F = 6.3 A, V_{GS} = 0 di_F/dt = 20 A/µs

Note: 4. Pulse test

Package Dimensions

JEITA Package Code	RENESAS Code	Package Name	MASS[Typ.]
SC-95 Modified	PTSP0006FA-A	TSOP-6 / TSOP-6V	0.012g



Reference Symbol	Dimension in Millimeters		
	Min	Nom	Max
A	0.8	—	1.1
A_1	0	—	0.1
A_2	0.8	0.9	1.0
A_3	—	0.25	—
b	0.25	0.32	0.4
b_1	—	0.3	—
c	0.1	0.13	0.15
c_1	—	0.11	—
D	2.8	2.95	3.1
E	1.45	1.6	1.75
e	—	1.0	—
H_E	2.6	2.8	3.0
L	0.3	—	0.7
L_1	0.1	—	0.5
L_P	0.2	—	0.6
x	—	—	0.05
y	—	—	0.05
b_2	—	—	0.45
e_1	—	2.2	—
I_1	—	—	0.8
Q	—	0.2	—

Ordering Information

Part Name	Quantity	Shipping Container
HAT2054M-EL-E	3000 pcs	Taping

Note: For some grades, production may be terminated. Please contact the Renesas sales office to check the state of production before ordering the product.

Renesas Technology Corp. Sales Strategic Planning Div. Nippon Bldg., 2-6-2, Ohte-machi, Chiyoda-ku, Tokyo 100-0004, Japan

RENESAS SALES OFFICES

http://www.renesas.com

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Renesas Technology America, Inc.
450 Holger Way, San Jose, CA 95134-1368, U.S.A
Tel: <1> (408) 382-7500, Fax: <1> (408) 382-7501

Renesas Technology Europe Limited
Dukes Meadow, Millboard Road, Bourne End, Buckinghamshire, SL8 5FH, U.K.
Tel: <44> (1628) 585-100, Fax: <44> (1628) 585-900

Renesas Technology Hong Kong Ltd.
7th Floor, North Tower, World Finance Centre, Harbour City, 1 Canton Road, Tsimshatsui, Kowloon, Hong Kong
Tel: <852> 2265-6688, Fax: <852> 2730-6071

Renesas Technology Taiwan Co., Ltd.
10th Floor, No.99, Fushing North Road, Taipei, Taiwan
Tel: <886> (2) 2715-2888, Fax: <886> (2) 2713-2999

Renesas Technology (Shanghai) Co., Ltd.
Unit2607 Ruijing Building, No.205 Maoming Road (S), Shanghai 200020, China
Tel: <86> (21) 6472-1001, Fax: <86> (21) 6415-2952

Renesas Technology Singapore Pte. Ltd.
1 Harbour Front Avenue, #06-10, Keppel Bay Tower, Singapore 098632
Tel: <65> 6213-0200, Fax: <65> 6278-8001

Renesas Technology Korea Co., Ltd.
Kukje Center Bldg. 18th Fl., 191, 2-ka, Hangang-ro, Yongsan-ku, Seoul 140-702, Korea
Tel: <82> 2-796-3115, Fax: <82> 2-796-2145

Renesas Technology Malaysia Sdn. Bhd.
Unit 906, Block B, Menara Amcorp, Amcorp Trade Centre, No.18, Jalan Persiaran Barat, 46050 Petaling Jaya, Selangor Darul Ehsan, Malaysia
Tel: <603> 7955-9390, Fax: <603> 7955-9510

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