Old Company Name in Catalogs and Other Documents

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Renesas Electronics website: http://www.renesas.com

April 1st, 2010 Renesas Electronics Corporation

Issued by: Renesas Electronics Corporation (http://www.renesas.com)

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2SK2729

Silicon N Channel MOS FET High Speed Power Switching

REJ03G1027-0300

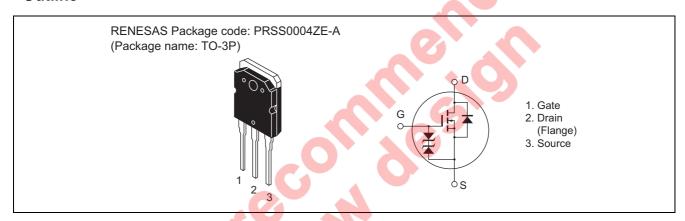
(Previous: ADE-208-455A)

Rev.3.00 Sep 07, 2005

Features

- Low on-resistance
- High speed switching
- Low drive current
- Avalanche ratings

Outline



Absolute Maximum Ratings

 $(Ta = 25^{\circ}C)$

Item	Symbol	Ratings	Unit	
Drain to source voltage	V _{DSS}	500	V	
Gate to source voltage	V _{GSS}	±30	V	
Drain current	I _D	20	А	
Drain peak current	I _{D(pulse)} *1	80	Α	
Body to drain diode reverse drain current	I _{DR}	20	Α	
Avalanche current	I _{AP} *3	20	Α	
Avalanche energy	E _{AR} *3	22	mJ	
Channel dissipation	Pch* ²	150	W	
Channel temperature	Tch	150	°C	
Storage temperature	Tstg	-55 to +150	°C	

Notes: 1. PW \leq 10 μ s, duty cycle \leq 1 %

2. Value at Tc = 25°C

3. Value at Tch = 25°C, Rg \geq 50 Ω

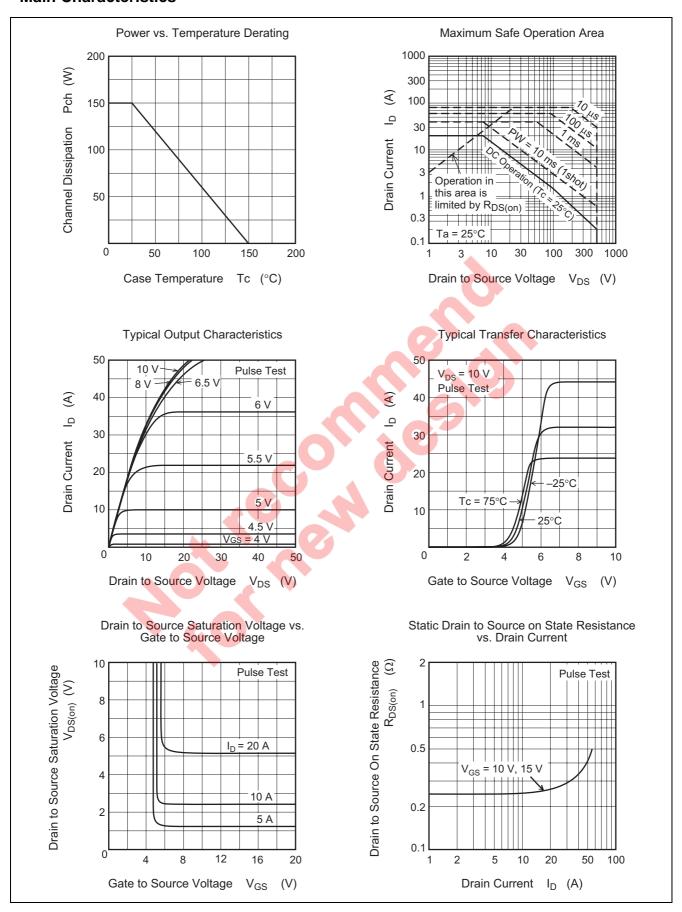
Electrical Characteristics

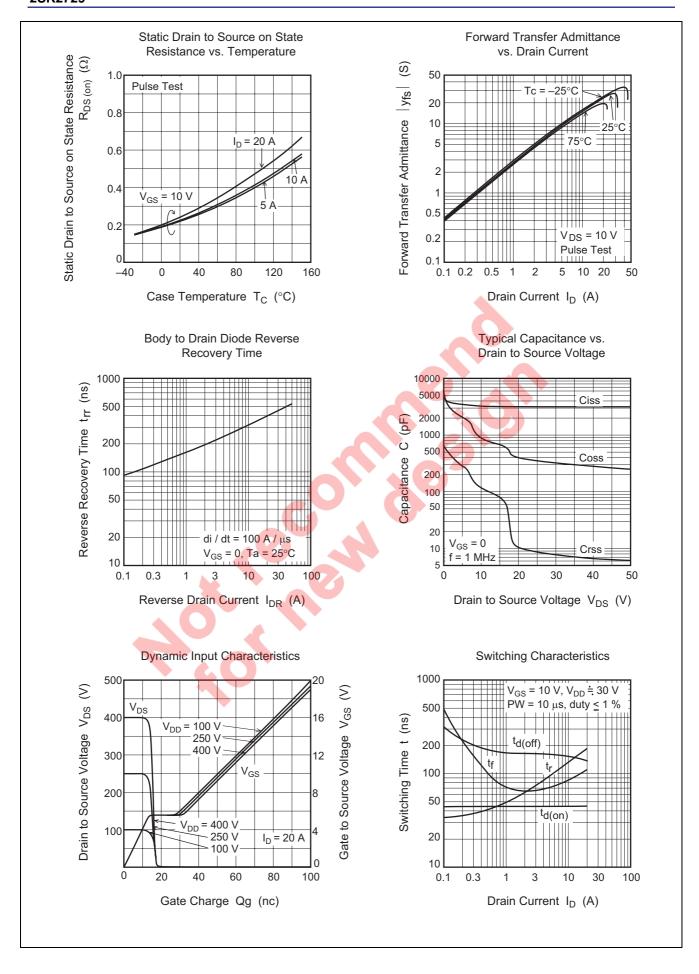
 $(Ta = 25^{\circ}C)$

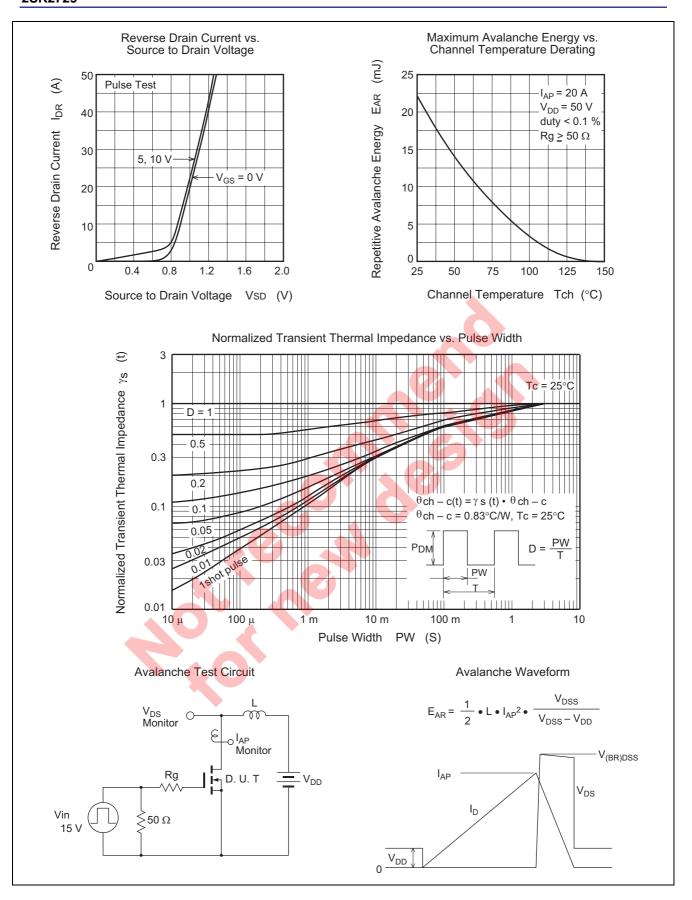
Item	Symbol	Min	Тур	Max	Unit	Test Conditions	
Drain to source breakdown voltage	V _{(BR)DSS}	500	-4		V	$I_D = 10 \text{ mA}, V_{GS} = 0$	
Gate to source breakdown voltage	$V_{(BR)GSS}$	±30			V	$I_G = \pm 100 \mu A, V_{DS} = 0$	
Gate to source leak current	I_{GSS}	_		±10	μΑ	$V_{GS} = \pm 25 \text{ V}, V_{DS} = 0$	
Zero gate voltage drain current	I _{DSS}	_		10	μΑ	$V_{DS} = 500 \text{ V}, V_{GS} = 0$	
Gate to source cutoff voltage	$V_{GS(off)}$	2.5	Y -	3.5	V	$I_D = 1 \text{ mA}, V_{DS} = 10 \text{ V}^{*4}$	
Static drain to source on state	R _{DS(on)}		0.24	0.29	Ω	$I_D = 10 \text{ A}, V_{GS} = 10 \text{ V}^{*4}$	
resistance			-		-	10.4.2.4.4.4.4.4.4.4.4.4.4.4.4.4.4.4.4.4.	
Forward transfer admittance	y _{fs}	9	15		S	$I_D = 10 \text{ A}, V_{DS} = 10 \text{ V}^{*4}$	
Input capacitance	Ciss		3300		рF	$V_{DS} = 10 \text{ V}, V_{GS} = 0,$	
Output capacitance	Coss	+	900		рF	f = 1 MHz	
Reverse transfer capacitance	Crss		120		рF		
Total gate charge	Qg	-	55	_	nc	$V_{DD} = 400 \text{ V}, V_{GS} = 10 \text{ V},$	
Gate to source charge	Qgs	_	14	_	nc	$I_D = 20 \text{ A}$	
Gate to drain charge	Qgd	_	17	_	nc		
Turn-on delay time	t _{d(on)}	_	45	_	ns	$V_{GS} = 10 \text{ V}, I_D = 10 \text{ A},$	
Rise time	t _r	_	140	_	ns	$R_L = 3 \Omega$	
Turn-off delay time	t _{d(off)}	_	150	_	ns		
Fall time	t _f	_	85	_	ns		
Body to drain diode forward voltage	V_{DF}	_	1.0	_	V	$I_D = 20 \text{ A}, V_{GS} = 0$	
Body to drain diode reverse recovery time	t _{rr}	_	400	_	ns	$I_F = 20 \text{ A}, V_{GS} = 0$ $di_F/dt = 100 \text{ A/}\mu\text{s}$	

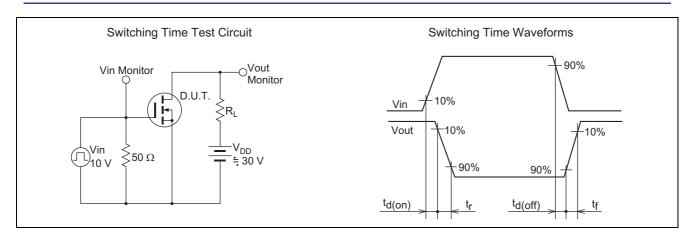
Note: 4. Pulse test

Main Characteristics



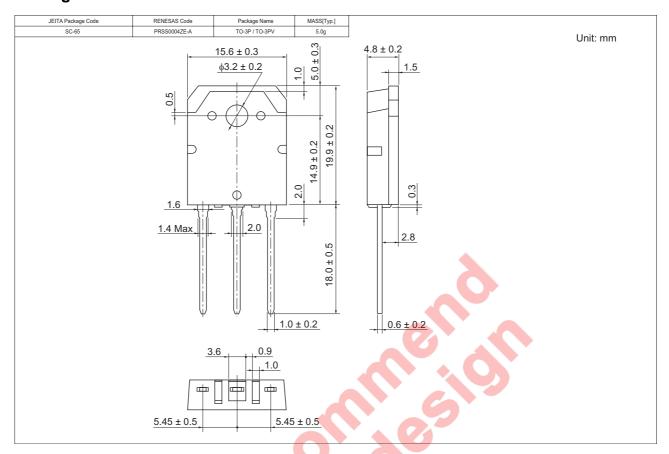








Package Dimensions



Ordering Information

Part Name	Quantity	Shipping Container
2SK2729-E	360 pcs	Box (Tube)

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