

# RJK1028DSP

100V, 3A, 165mΩ max.  
Silicon N Channel Power MOS FET  
Power Switching

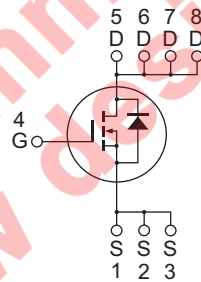
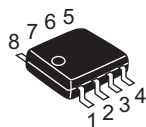
R07DS0197EJ0300  
Rev.3.00  
Apr 11, 2013

## Features

- High speed switching
- Capable of 4.5 V gate drive
- Low drive current
- High density mounting
- Low on-resistance  
 $R_{DS(on)} = 125 \text{ m}\Omega$  typ. (at  $V_{GS} = 10 \text{ V}$ )
- Pb-free
- Halogen-free

## Outline

RENESAS Package code: PRSP0008DD-D  
(Package name: SOP-8<FP-8DAV>)



1, 2, 3 Source  
4 Gate  
5, 6, 7, 8 Drain

## Absolute Maximum Ratings

( $T_a = 25^\circ\text{C}$ )

Item	Symbol	Ratings	Unit
Drain to source voltage	$V_{DSS}$	100	V
Gate to source voltage	$V_{GSS}$	+12, -5	V
Drain current	$I_D$	3	A
Drain peak current	$I_{D(pulse)}$ <sup>Note1</sup>	12	A
Body-drain diode reverse drain current	$I_{DR}$	3	A
Avalanche current	$I_{AP}$ <sup>Note 2</sup>	2	A
Avalanche energy	$E_{AS}$ <sup>Note 2</sup>	0.4	mJ
Channel dissipation	$P_{ch}$ <sup>Note3</sup>	1.8	W
Channel to ambient thermal impedance	$\theta_{ch-a}$ <sup>Note3</sup>	70	$^\circ\text{C}/\text{W}$
Channel temperature	$T_{ch}$	150	$^\circ\text{C}$
Storage temperature	$T_{stg}$	-55 to +150	$^\circ\text{C}$

Notes: 1.  $PW \leq 10 \mu\text{s}$ , duty cycle  $\leq 1\%$

2. Value at  $T_{ch} = 25^\circ\text{C}$ ,  $R_g \geq 50 \Omega$

3. When using the glass epoxy board (FR4 40 x 40 x 1.6 mm),  $PW \leq 10\text{s}$

## Electrical Characteristics

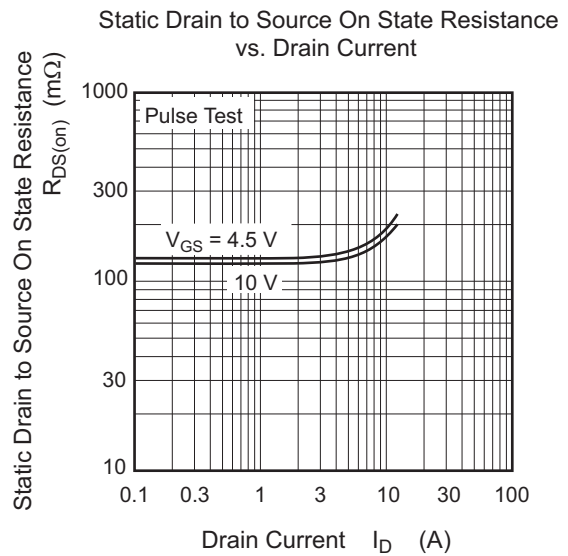
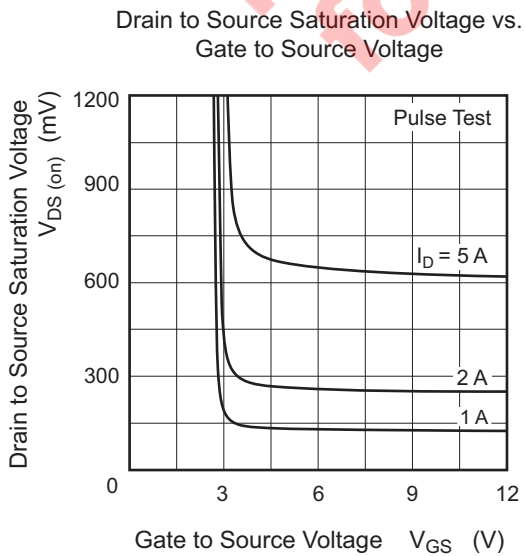
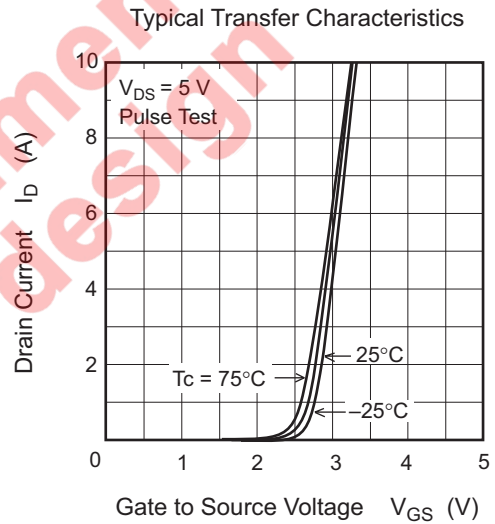
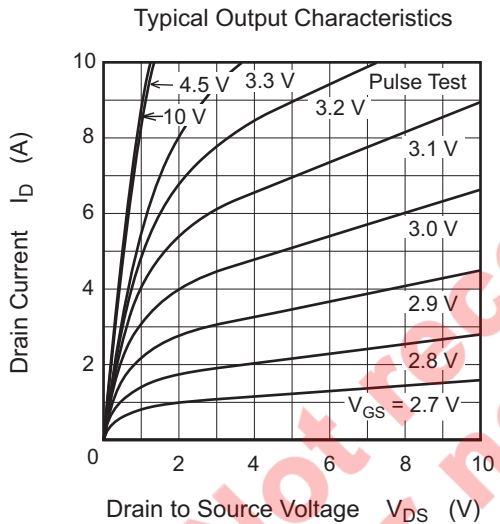
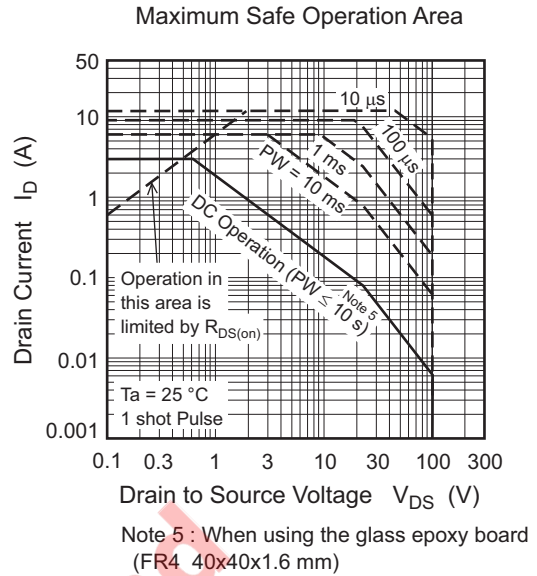
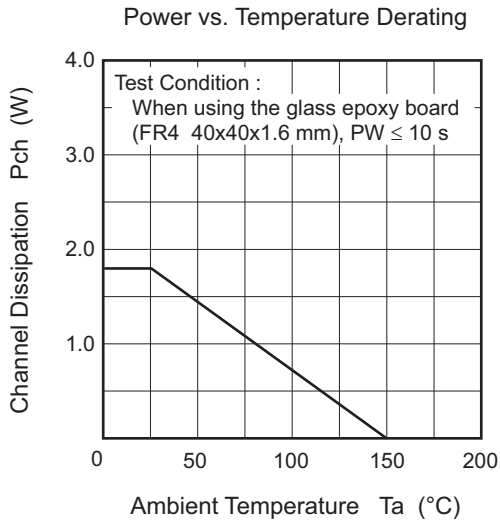
(Ta = 25°C)

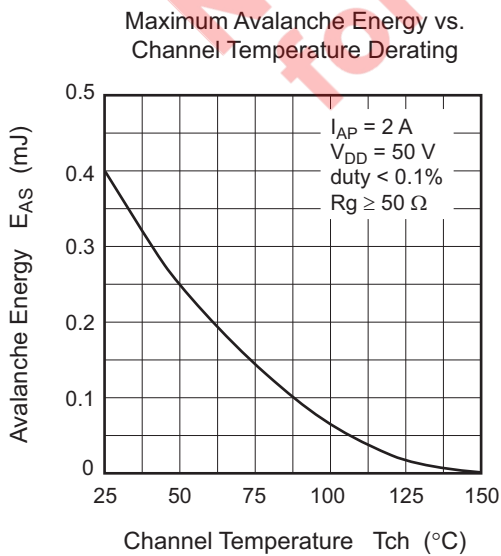
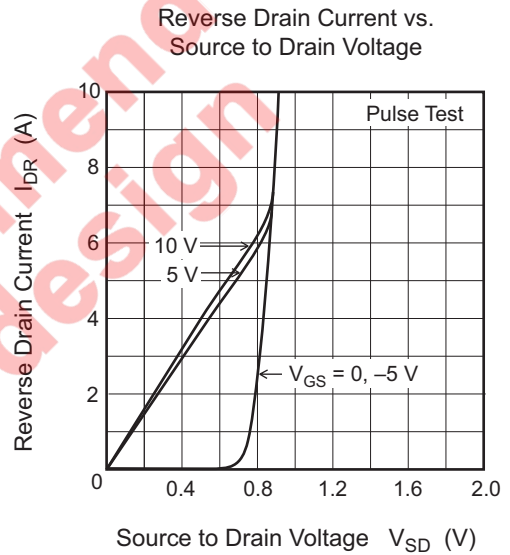
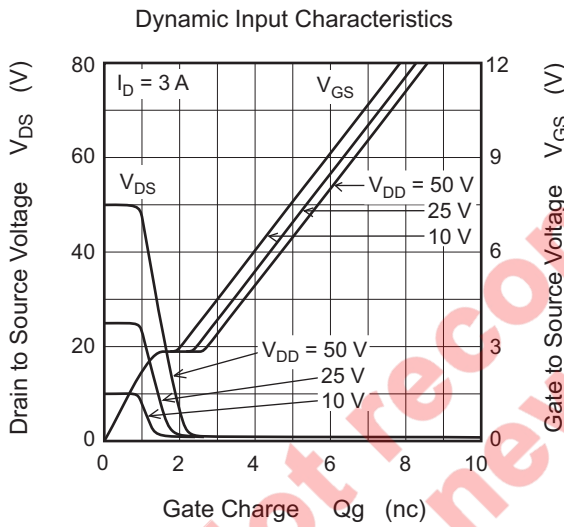
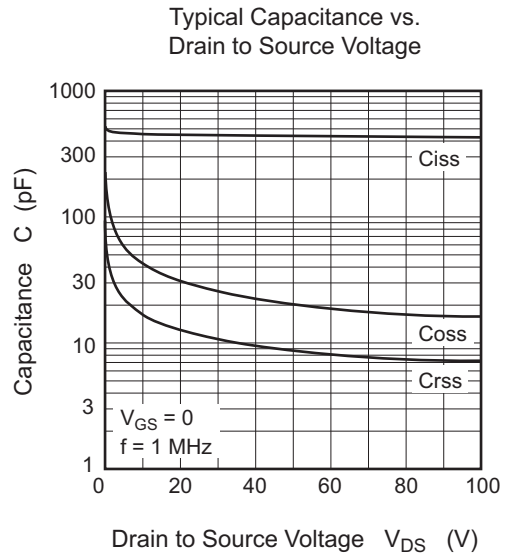
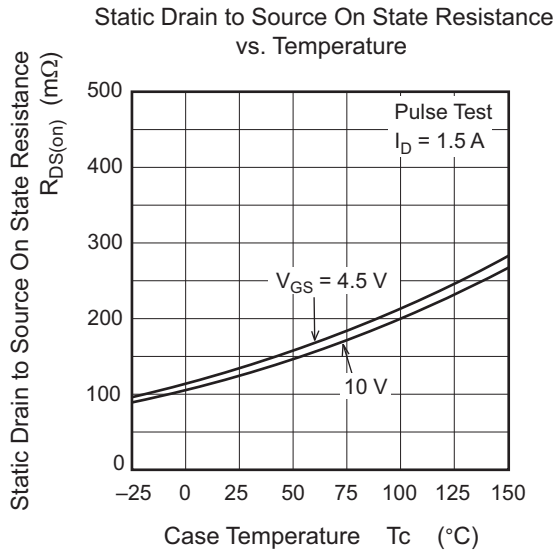
Item	Symbol	Min	Typ	Max	Unit	Test Conditions
Drain to source breakdown voltage	$V_{(BR)DSS}$	100	—	—	V	$I_D = 10 \text{ mA}$ , $V_{GS} = 0$
Gate to source leak current	$I_{GSS}$	—	—	$\pm 0.1$	$\mu\text{A}$	$V_{GS} = +12, -5 \text{ V}$ , $V_{DS} = 0$
Zero gate voltage drain current	$I_{DSS}$	—	—	10	$\mu\text{A}$	$V_{DS} = 100 \text{ V}$ , $V_{GS} = 0$
Gate to source cutoff voltage	$V_{GS(off)}$	1.2	—	2.5	V	$V_{DS} = 10 \text{ V}$ , $I_D = 1 \text{ mA}$
Static drain to source on state resistance	$R_{DS(on)}$	—	125	165	$\text{m}\Omega$	$I_D = 1.5 \text{ A}$ , $V_{GS} = 10 \text{ V}$ <sup>Note4</sup>
	$R_{DS(on)}$	—	135	180	$\text{m}\Omega$	$I_D = 1.5 \text{ A}$ , $V_{GS} = 4.5 \text{ V}$ <sup>Note4</sup>
Forward transfer admittance	$ y_{fs} $	—	6.5	—	S	$I_D = 1.5 \text{ A}$ , $V_{DS} = 5 \text{ V}$ <sup>Note4</sup>
Input capacitance	$C_{iss}$	—	450	—	pF	$V_{DS} = 10 \text{ V}$
Output capacitance	$C_{oss}$	—	42	—	pF	$V_{GS} = 0$
Reverse transfer capacitance	$C_{rss}$	—	17	—	pF	$f = 1 \text{ MHz}$
Gate Resistance	$R_g$	—	2.7	—	$\Omega$	
Total gate charge	$Q_g$	—	3.7	—	nC	$V_{DD} = 50 \text{ V}$
Gate to source charge	$Q_{gs}$	—	1.5	—	nC	$V_{GS} = 4.5 \text{ V}$
Gate to drain charge	$Q_{gd}$	—	1.5	—	nC	$I_D = 3 \text{ A}$
Turn-on delay time	$t_{d(on)}$	—	8.3	—	ns	$V_{GS} = 10 \text{ V}$ , $I_D = 1.5 \text{ A}$
Rise time	$t_r$	—	4.5	—	ns	$V_{DD} \cong 30 \text{ V}$
Turn-off delay time	$t_{d(off)}$	—	37	—	ns	$R_L = 20 \Omega$
Fall time	$t_f$	—	5.2	—	ns	$R_g = 4.7 \Omega$
Body-drain diode forward voltage	$V_{DF}$	—	0.82	1.07	V	$I_F = 3 \text{ A}$ , $V_{GS} = 0$ <sup>Note4</sup>
Body-drain diode reverse recovery time	$t_{rr}$	—	24	—	ns	$I_F = 3 \text{ A}$ , $V_{GS} = 0$ $di_F/dt = 100 \text{ A}/\mu\text{s}$

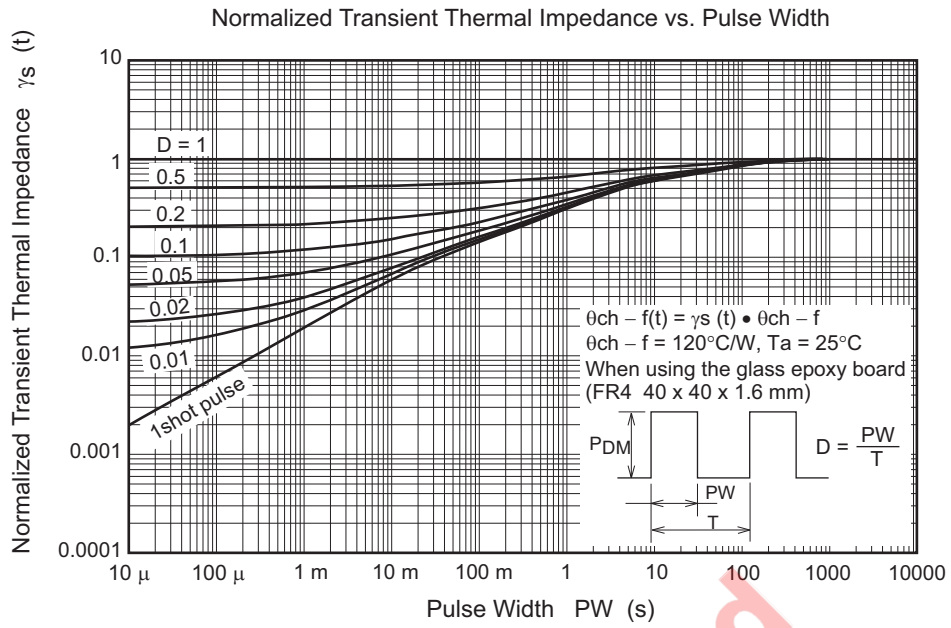
Notes: 4. Pulse test

Not recommended  
for new designs

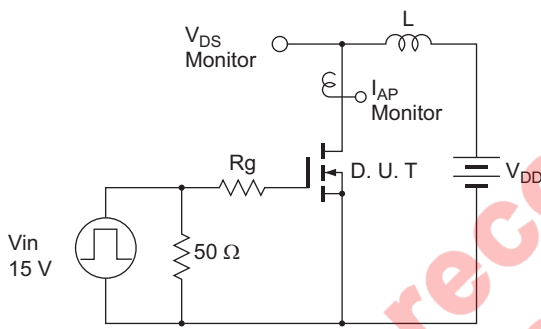
Main Characteristics



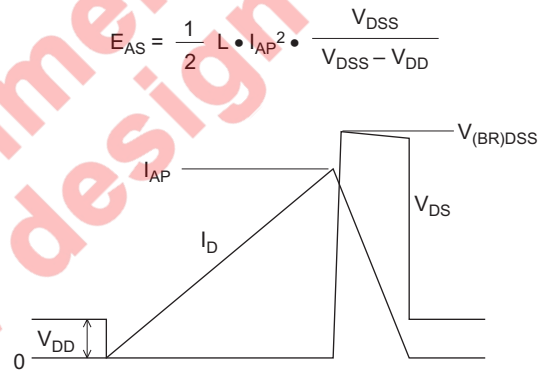




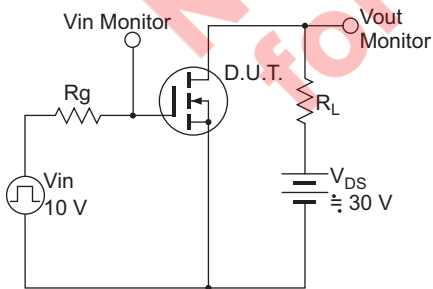
Avalanche Test Circuit



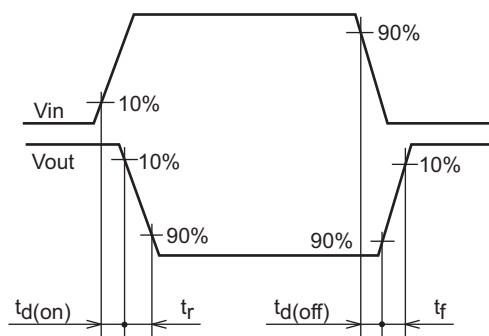
Avalanche Waveform



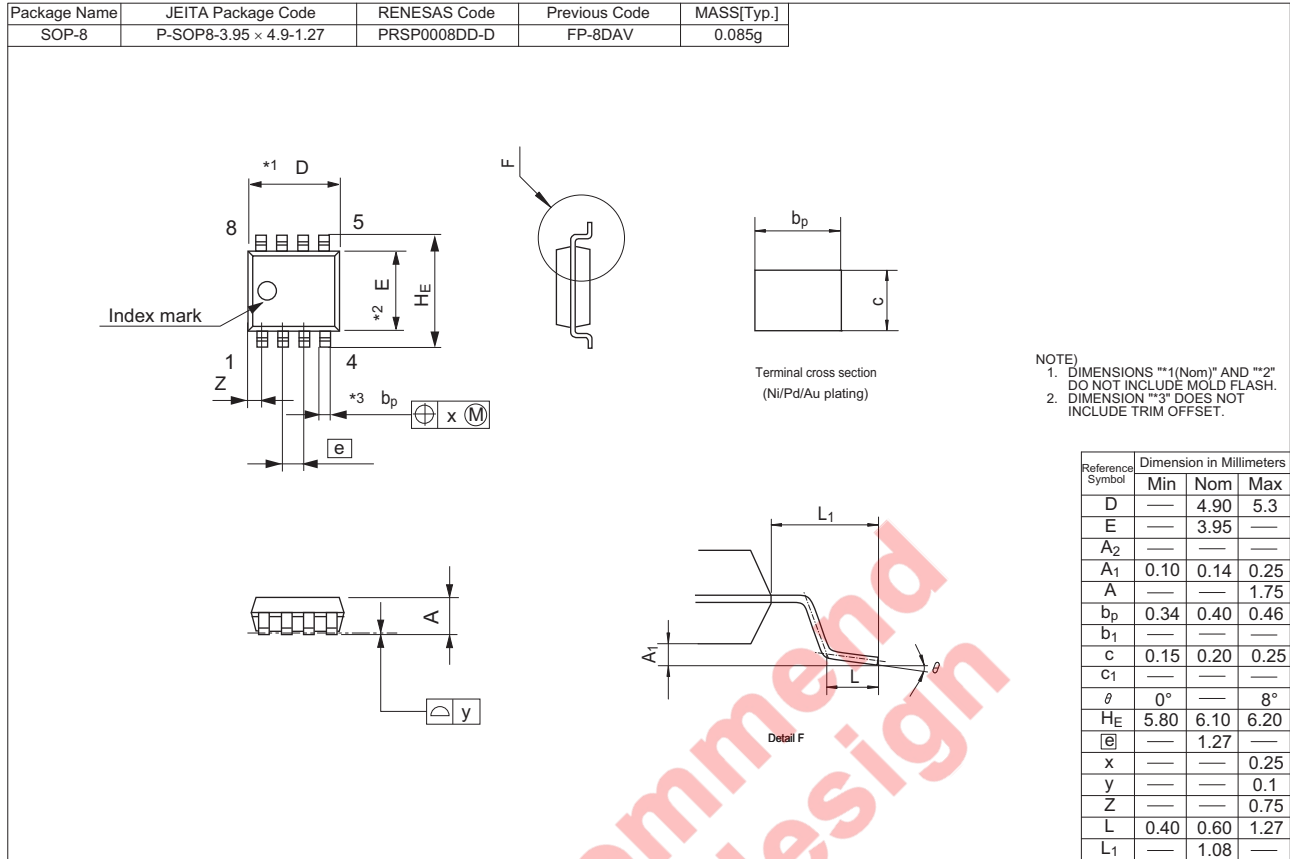
Switching Time Test Circuit



Switching Time Waveform



### Package Dimensions



### Ordering Information

Orderable Part Number	Quantity	Shipping Container
RJK1028DSP-00-J5	2500 pcs	Taping

Not recommended for new design

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