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April 1st, 2010 Renesas Electronics Corporation

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Renesas

MOS FIELD EFFECT TRANSISTOR 2SK2110

N-CHANNEL MOSFET FOR HIGH-SPEED SWITCHING

DESCRIPTION

The 2SK2110 is a N-channel MOSFET of a vertical type and is a switching element that can be directly driven by the output of an IC operating at 5 V.

This product has a low on-state resistance and superb switching characteristics and is ideal for driving the actuators, such as motors and DC/DC converters.

FEATURES

- Low on-state resistance
- $R_{DS(on)} = 1.5 \Omega MAX. (V_{GS} = 4.0 V, I_D = 0.3 A)$
- · High switching speed
- $t_{on} + t_{off} < 100 \text{ ns}$
- · Low parasitic capacitance

<R> **ORDERING INFORMATION**

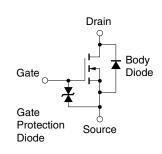
PART NUMBER	
2SK2110	SC-62 (Power Mini Mold)

Marking: NT

ABSOLUTE MAXIMUM RATINGS (TA = 25°C)

Drain to Source Voltage (V _{GS} = 0 V)	VDSS	100	V
Gate to Source Voltage (Vps = 0 V)	Vgss	±20	V
Drain Current (DC)	D(DC)	±0.5	А
Drain Current (pulse)	D(pulse)	±1.0	А
Total Power Dissipation Note2	Pτ	2.0	W
Channel Temperature	Tch	150	°C
Storage Temperature	Tstg	–55 to +150	°C

EQUIVALENT CIRCUIT



Notes 1. PW \leq 10 ms, Duty Cycle \leq 50%

- **2.** Mounted on ceramic substrate of $16 \text{ cm}^2 \times 0.7 \text{ mm}$
- Remark The diode connected between the gate and source of the transistor serves as a protector against ESD. <R> When this device actually used, an additional protection circuit is externally required if a voltage exceeding the rated voltage may be applied to this device.

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The mark <R> shows major revised points.

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The revised points can be easily searched by copying an "<R>" in the PDF file and specifying it in the "Find what:" field.

PACKAGE DRAWING (Unit: mm)

0.42

±0.06

ç. 25

0.8 MIN.

å 2.5 40 1.5 ±0.1

 $0.41^{+0.03}_{-0.05}$

1. Source

2. Drain

3. Gate

4.5 ±0.1

1.6 ±0.2

0.47

1.5 TYP

3.0 TYP

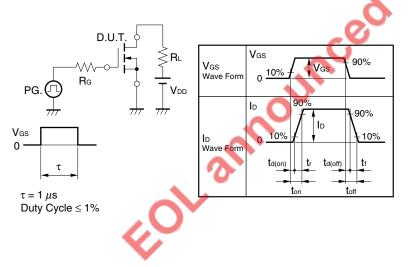
±0.06

0.42

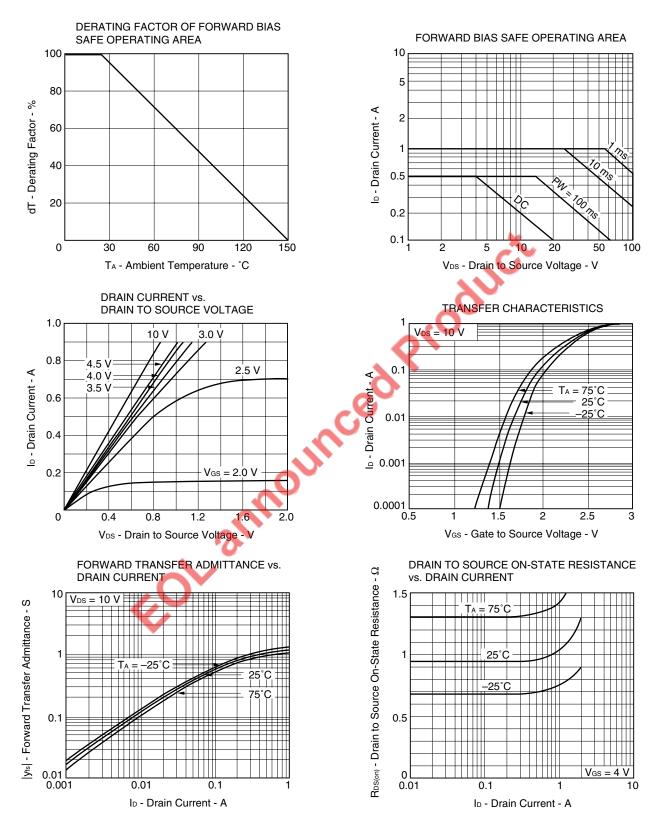
<R> ELECTRICAL CHARACTERISTICS (TA = 25°C)

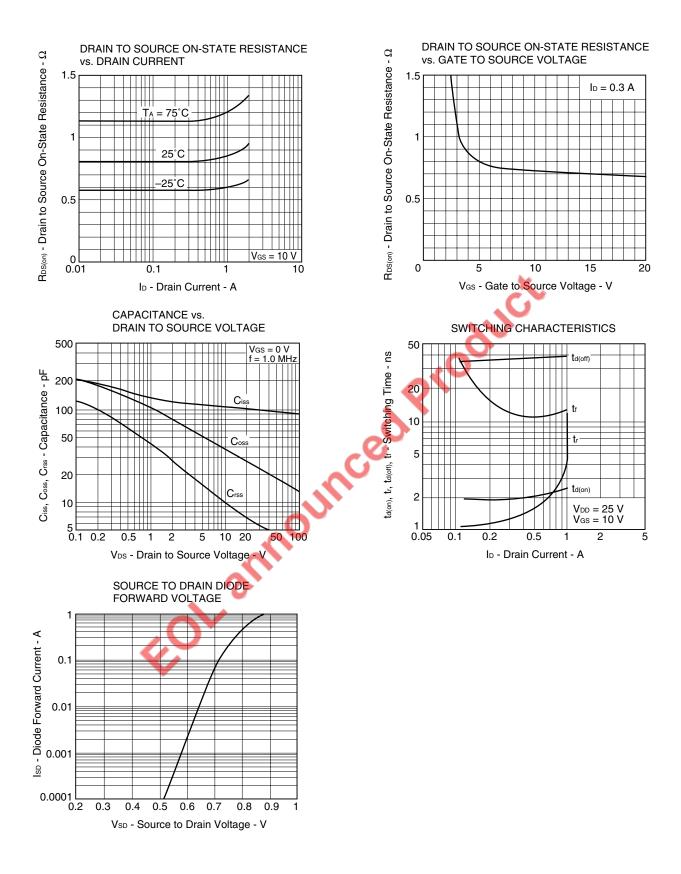
CHARACTERISTICS	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNIT
CHARACTERISTICS	STIVIDUL		IVIIIN.		IVIAA.	UNIT
Zero Gate Voltage Drain Current	IDSS	V _{DS} = 100 V, V _{GS} = 0 V			10	μA
Gate Leakage Current	lgss	V _{GS} = ±20 V, V _{DS} = 0 V			±10	μA
Gate Cut-off Voltage	V _{GS(off)}	V _{DS} = 10 V, I _D = 1 mA	0.8	1.5	2.0	V
Forward Transfer Admittance Note	y fs	V _{DS} = 10 V, I _D = 0.3 A	0.4			S
Drain to Source On-state Resistance Note	RDS(on)1	V _{GS} = 4.0 V, I _D = 0.3 A		0.95	1.5	Ω
	RDS(on)2	V _{GS} = 10 V, I _D = 0.3 A		0.82	1.2	Ω
Input Capacitance	Ciss	V _{DS} = 10 V		100		pF
Output Capacitance	Coss	V _{GS} = 0 V		38		pF
Reverse Transfer Capacitance	Crss	f = 1.0 MHz		10		pF
Turn-on Delay Time	td(on)	V _{DD} = 25 V, I _D = 0.3 A		2.0		ns
Rise Time	tr	V _{GS} = 10 V		1.3		ns
Turn-off Delay Time	$t_{d(off)}$	R _G = 10 Ω		38		ns
Fall Time	tr			13		ns
Note Pulsed		Pro				
		6				

<R> **TEST CIRCUIT SWITCHING TIME**









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