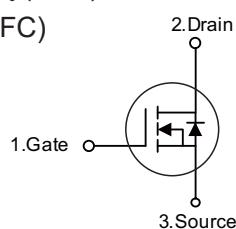


### Features

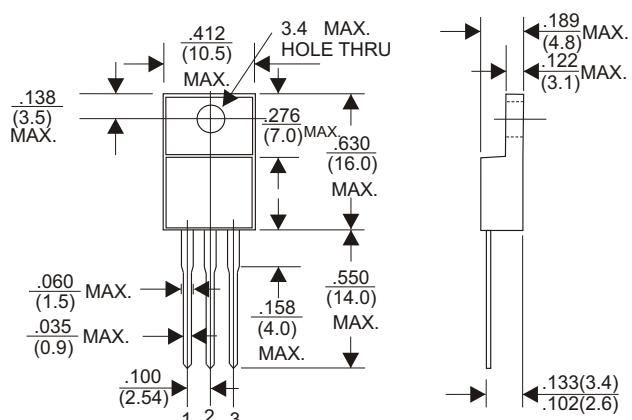
- 650V,20A
- $R_{DS(ON)} = 0.35\Omega$  (Typ.) @  $V_{GS} = 10V$ ,  $I_D = 10A$
- Fast Switching
- Improved dv/dt Capability
- 100% Avalanche Tested

### Application

- Switch Mode Power Supply(SMPS)
- Uninterruptible Power Supply(UPS)
- Power Factor Correction (PFC)



**ITO-220F (FULLY INSULATED)**



Dimensions in inches and (millimeters)

### ABSOLUTE MAXIMUM RATINGS ( $T_c = 25^\circ C$ unless otherwise specified)

Parameter	Symbol	20N65F		Units
Drain-Source Voltage	$V_{DSS}$	650		V
Gate-Source Voltage	$V_{GSS}$	$\pm 30$		V
Continuous Drain Current	$T_c = 25^\circ C$	20		A
	$T_c = 100^\circ C$	13		A
Pulsed Drain Current <sup>note1</sup>	$I_{DM}$	80		A
Single Pulsed Avalanche Energy <sup>note2</sup>	$E_{AS}$	1350		mJ
Power Dissipation	$P_D$	167	416	W
Thermal Resistance, Junction to Case	$R_{\theta JC}$	0.75	0.3	$^\circ C/W$
Thermal Resistance, Junction to Ambient	$R_{\theta JA}$	60	60	$^\circ C/W$
Operating and Storage Temperature Range	$T_J, T_{STG}$	-55 to +150		$^\circ C$

# 20N65F

## Electrical Characteristics ( $T_c=25^\circ\text{C}$ unless otherwise specified)

Symbol	Parameter	Test Condition	Min.	Typ.	Max.	Units
<b>Off Characteristics</b>						
$V_{(\text{BR})\text{DSS}}$	Drain-Source Breakdown Voltage	$V_{GS}=0\text{V}, I_D=250\mu\text{A}$	650	-	-	V
$I_{\text{DSS}}$	Zero Gate Voltage Drain Current	$V_{DS} = 650\text{V}, V_{GS} = 0\text{V}, T_J = 25^\circ\text{C}$	-	-	1	$\mu\text{A}$
$I_{GSS}$	Gate to Body Leakage Current	$V_{DS} = 0\text{V}, V_{GS} = \pm 30\text{V}$	-	-	$\pm 100$	nA
<b>On Characteristics</b>						
$V_{GS(\text{th})}$	Gate Threshold Voltage	$V_{DS} = V_{GS}, I_D=250\mu\text{A}$	2	3	4	V
$R_{DS(\text{on})}$	Static Drain-Source on-Resistance note3	$V_{GS} = 10\text{V}, I_D = 10\text{A}$	-	0.35	0.45	$\Omega$
<b>Dynamic Characteristics</b>						
$C_{iss}$	Input Capacitance	$V_{DS} = 25\text{V}, V_{GS} = 0\text{V}, f = 1.0\text{MHz}$	-	2978	-	pF
$C_{oss}$	Output Capacitance		-	291	-	pF
$C_{rss}$	Reverse Transfer Capacitance		-	40	-	pF
$Q_g$	Total Gate Charge	$V_{DD} = 520\text{V}, I_D = 20\text{A}, V_{GS} = 10\text{V}$	-	80	-	nC
$Q_{gs}$	Gate-Source Charge		-	12	-	nC
$Q_{gd}$	Gate-Drain("Miller") Charge		-	34	-	nC
<b>Switching Characteristics</b>						
$t_{d(on)}$	Turn-on Delay Time	$V_{DD} = 325\text{V}, I_D = 20\text{A}, R_G = 25\Omega$	-	37	-	ns
$t_r$	Turn-on Rise Time		-	66	-	ns
$t_{d(off)}$	Turn-off Delay Time		-	175	-	ns
$t_f$	Turn-off Fall Time		-	84	-	ns
<b>Drain-Source Diode Characteristics and Maximum Ratings</b>						
$I_S$	Maximum Continuous Drain to Source Diode Forward Current	-	-	20	-	A
$I_{SM}$	Maximum Pulsed Drain to Source Diode Forward Current	-	-	80	-	A
$V_{SD}$	Drain to Source Diode Forward Voltage	$V_{GS} = 0\text{V}, I_{SD} = 20\text{A}$	-	-	1.4	V
$t_{rr}$	Reverse Recovery Time	$V_{GS} = 0\text{V}, I_S = 20\text{A}, di/dt = 100\text{A}/\mu\text{s}$	-	450	-	ns
$Q_{rr}$	Reverse Recovery Charge		-	7.1	-	$\mu\text{C}$

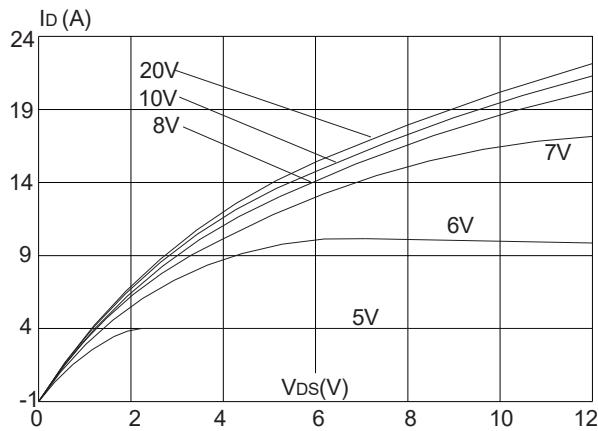
Notes: 1. Repetitive Rating: Pulse Width Limited by Maximum Junction Temperature

2.  $I_{AS} = 16\text{A}, V_{DD} = 50\text{V}, R_G = 25 \Omega$ , Starting  $T_J = 25^\circ\text{C}$

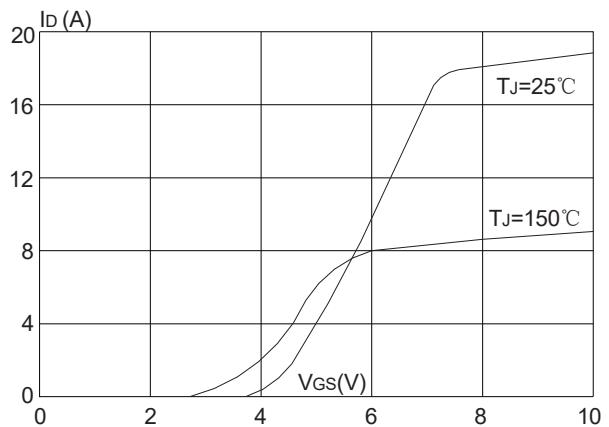
3. Pulse Test: Pulse Width  $\leq 350\mu\text{s}$ , Duty Cycle  $\leq 1\%$

## RATING AND CHARACTERISTIC CURVES (20N65F)

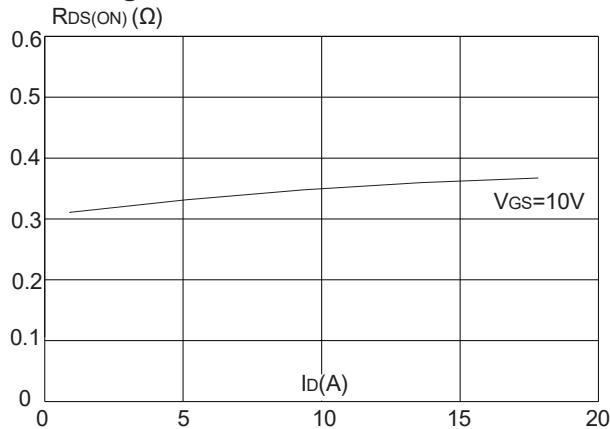
**Figure 1:** Output Characteristics



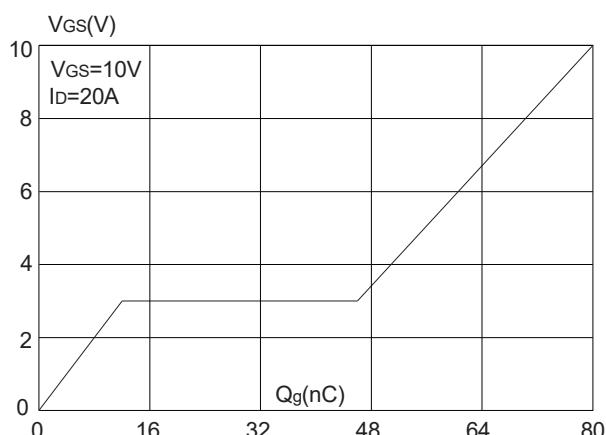
**Figure 2:** Typical Transfer Characteristics



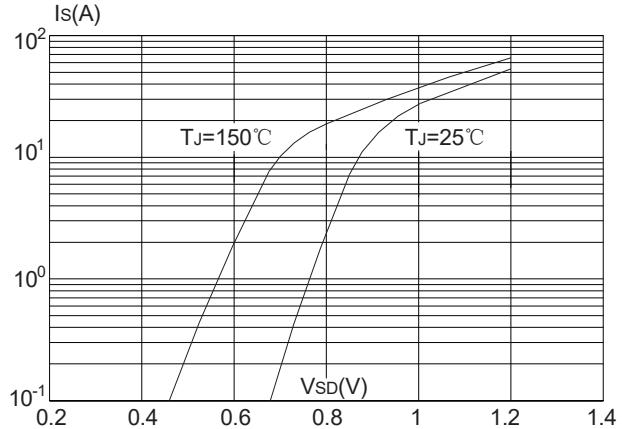
**Figure 3:** On-resistance vs. Drain Current



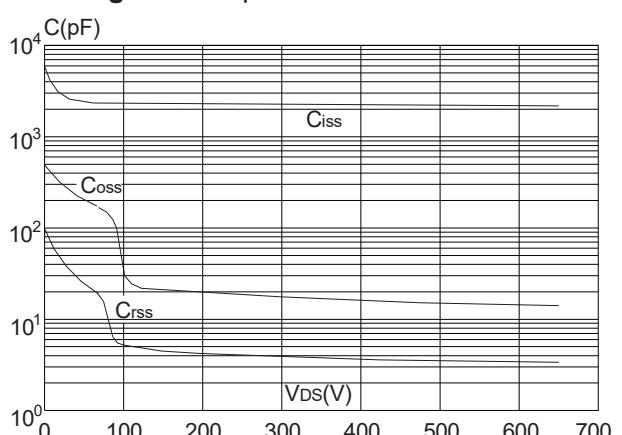
**Figure 5:** Gate Charge Characteristics



**Figure 4:** Body Diode Characteristics

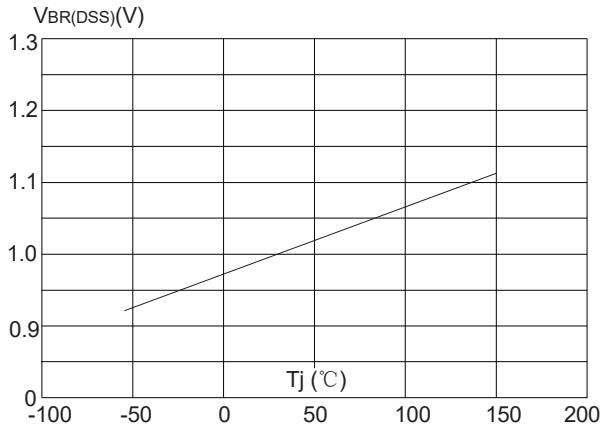


**Figure 6:** Capacitance Characteristics

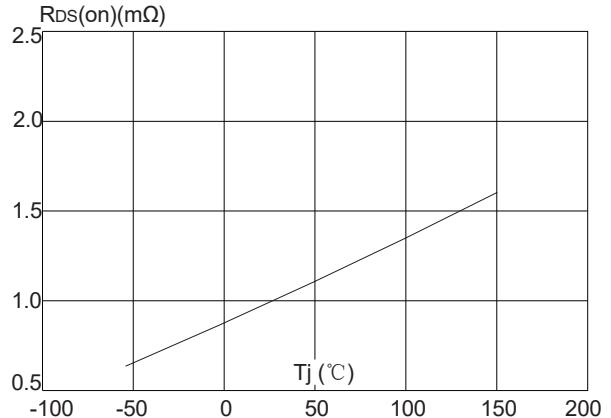


## RATING AND CHARACTERISTIC CURVES (20N65F)

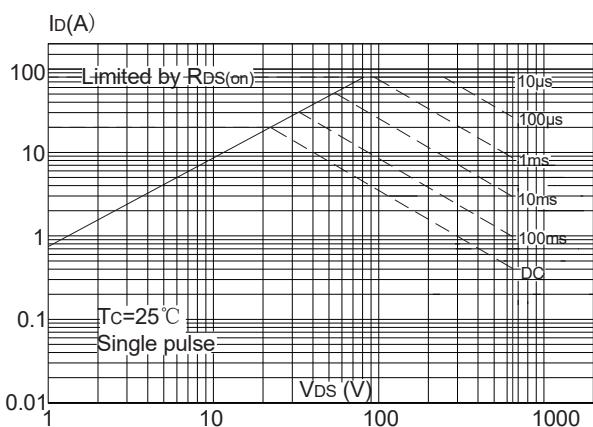
**Figure 7:** Normalized Breakdown Voltage vs. Junction Temperature



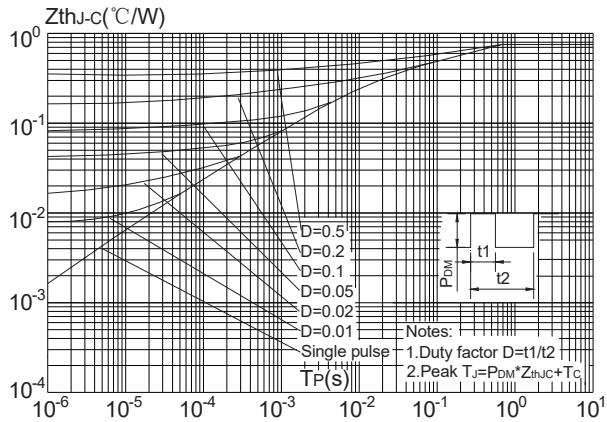
**Figure 8:** Normalized on Resistance vs. Junction Temperature



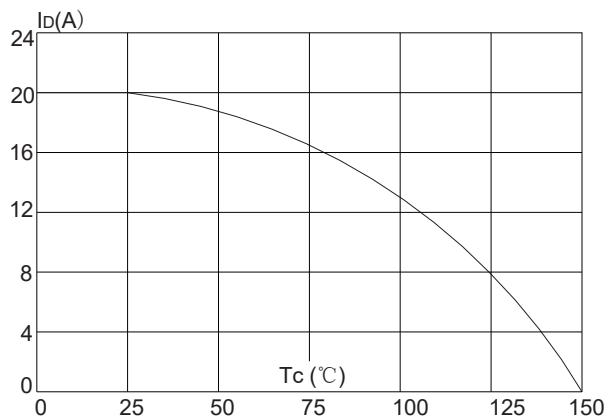
**Figure 9:** Maximum Safe Operating Area



**Figure 11:** Maximum Effective Transient Thermal Impedance, Junction-to-Case (TO-220F)



**Figure 10:** Maximum Continuous Drain Current vs. Case Temperature



**Figure 12:** Maximum Effective Transient Thermal Impedance, Junction-to-Case (TO-247, TO-3P)

