



## Intel® Quark™ Microcontroller D1000

Specifications

Product Images

Ordering and Compliance

### Ordering and spec information

#### Intel® Quark™ Microcontroller D1000, T&R

Spec Code	SLKMJ
Ordering Code	DMNIAD01SLVBT
Step	B1
RCP	\$2.54

### Trade compliance information

ECCN	3A991.A.2
CCATS	NA
US HTS	8542310001

### PCN/MDDS Information

#### SLKMJ

938719	<a href="#">PCN   MDDS</a>
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Downloads and Software

All information provided is subject to change at any time, without notice. Intel may make changes to manufacturing life cycle, specifications, and product descriptions at any time, without notice. The information herein is provided "as-is" and Intel does not make any representations or warranties whatsoever regarding accuracy of the information, nor on the product features, availability, functionality, or compatibility of the products listed. Please contact system vendor for more information on specific products or systems.

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Refer to Datasheet for formal definitions of product properties and features.

"Announced" SKUs are not yet available. Please refer to the Launch Date for market availability.

Some products can support AES New Instructions with a Processor Configuration update, in particular, i7-2630QM/i7-2635QM, i7-2670QM/i7-2675QM, i5-2430M/i5-2435M, i5-2410M/i5-2415M. Please contact OEM for the BIOS that includes the latest Processor configuration update.

‡ This feature may not be available on all computing systems. Please check with the system vendor to determine if your system delivers this feature, or reference the system specifications (motherboard, processor, chipset, power supply, HDD, graphics controller, memory, BIOS, drivers, virtual machine monitor-VMM, platform software, and/or operating system) for feature compatibility. Functionality, performance, and other benefits of this feature may vary depending on system configuration.

See <http://www.intel.com/content/www/us/en/architecture-and-technology/hyper-threading/hyper-threading-technology.html?wapkw=hyper+threading> for more information including details on which processors support Intel® HT Technology.

Max Turbo Frequency refers to the maximum single-core processor frequency that can be achieved with Intel® Turbo Boost Technology. See [www.intel.com/technology/turboboost/](http://www.intel.com/technology/turboboost/) for more information.

The Recommended Customer Price ("RCP") is pricing guidance for Intel products. Prices are for direct Intel customers, typically represent 1,000-unit purchase quantities, and are subject to change without notice. Taxes and shipping, etc. not included. Prices may vary for other package types and shipment quantities, and special promotional arrangements may apply. If sold in bulk, price represents individual unit. Listing of these RCP does not constitute a formal pricing offer from Intel. Please work with your appropriate Intel representative to obtain a formal price quotation.

System and Maximum TDP is based on worst case scenarios. Actual TDP may be lower if not all I/Os for chipsets are used.

Low Halogen: Applies only to brominated and chlorinated flame retardants (BFRs/CFRs) and PVC in the final product. Intel components as well as purchased components on the finished assembly meet JS-709 requirements, and the PCB / substrate meet IEC 61249-2-21 requirements. The replacement of halogenated flame retardants and/or PVC may not be better for the environment.

For benchmarking data see <http://www.intel.com/performance>.

Intel processor numbers are not a measure of performance. Processor numbers differentiate features within each processor family, not across different processor families. See <http://www.intel.com/content/www/us/en/processors/processor-numbers.html> for details.

Processors that support 64-bit computing on Intel® architecture require an Intel 64 architecture-enabled BIOS.



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## Specifications

## Essentials

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## Essentials

Export spreadsheet

Product Collection	Intel® Quark™ Microcontroller D1000 Series
Code Name	Products formerly Silver Butte
Vertical Segment	Embedded
Processor Number	D1000
Status	Launched
Launch Date <a href="#">?</a>	Q3'15
Recommended Customer Price <a href="#">?</a>	\$2.54
Use Conditions <a href="#">?</a>	Embedded Broad Market Commercial Temp, Embedded Broad Market Extended Temp

## Performance

# of Cores <a href="#">?</a>	1
# of Threads <a href="#">?</a>	1
Processor Base Frequency <a href="#">?</a>	33 MHz
TDP <a href="#">?</a>	0.025 W
Configurable TDP-up Frequency <a href="#">?</a>	32 MHz
Configurable TDP-up <a href="#">?</a>	0.025 W
Configurable TDP-down Frequency <a href="#">?</a>	1 MHz
Configurable TDP-down <a href="#">?</a>	0.0016 W
VID Voltage Range <a href="#">?</a>	1.62v - 3.63v

## Supplemental Information

Embedded Options Available <a href="#">?</a>	Yes
Datasheet	<a href="#">View now</a>
Description	Datasheet describes all publicly disclosed specifications including electrical characteristics, mechanical, and component functionality, a list of major features, a functional description, and an architectural overview.
Additional Information URL	<a href="#">View now</a>

## I/O Specifications

General Purpose IO	SPI, I2C, 24GPIO
UART	2

## Package Specifications

Sockets Supported <a href="#">?</a>	QFN40
Operating Temperature Range	-40°C to 85°C
Package Size	6mm x 6mm
Low Halogen Options Available	See MDDS

## Advanced Technologies

Instruction Set <a href="#">?</a>	32-bit
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## More support options for Intel® Quark™ Microcontroller D1000



Product Support



Downloads and Software



Support Community



Warranty and Repair



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