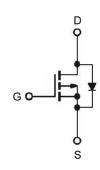


P-Channel 60-V (D-S) MOSFET

Description

The device is using trench DMOS technology. This advanced technology has been especially tailored to minimize R_{DS(ON)}, provide superior switching performance, and withstand high energy pulse in the avalanche and commutation mode. These devices are well suited for high efficiency fast switching applications.

Graphic Symbol



Features

- $R_{DS(ON)} = 70 \text{m}\Omega$ @ $V_{GS} = -10V$
- Fast switching
- 100% EAS Guaranteed
- Green Device Available

Typical Applications

- Notebook
- Load Switch
- Networking
- LED Lighting

Package type: PDFN 3.3X3.3

AEC-Q101 qualification available

Packing & Order Information

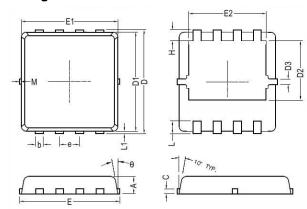
3,000/Reel





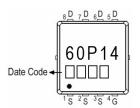
RoHS Compliant

Package Dimension



REF.	Millimeter		REF.	Millimeter				
	Min.	Nom.	Max.	KEF.	Min.	Nom.	Max.	
Α	0.70	0.75	0.80	E1	3.00	3.15	3.20	
b	0.25	0.30	0.35	E2	2.39	2.59		
С	0.10	0.15	0.25	е	0.65 BSC			
D	3.25	3.35	3.45	Н	0.30	0.39	0.50	
D1	3.00	3.10	3.20	L	0.30 0.40		0.50	
D2	1.78	1.88	1.98	L1	- 0.13		0.20	
D3	-	0.13	-	θ	-	10°	12°	
E	3.20	3.30	3.40	М	-	-	0.15	

Marking





P-Channel 60-V (D-S) MOSFET

MAXIMUM RATINGS AND ELECTRICAL CHARACTERISTICS

Absolute Maximum Ratings					
Symbol	Parameter	Value	Units		
V _{DS}	Drain-Source Voltage	-60	V		
V _G s	Gate-Source Voltage	±20	V		
I-	Continuous Drain Current ¹ (T _C =25°C)	-14	А		
lD	Continuous Drain Current ¹ (T _C =100°C)	-9	А		
I _{DM}	Pulsed Drain Current ^{1,2}	-56	А		
las	Single Pulse Avalanche Current, L =0.1mH ³	-25	А		
Eas	Single Pulse Avalanche Energy, L =0.1mH ³	31	mJ		
D	Power Dissipation ⁴ (T _C =25°C)	34.7	W		
P_D	Power Dissipation ⁴ (T _A =25°C)	2	W		
TJ/TsTG	Operating Junction and Storage Temperature	-55 to +150	°C		

Thermal Resistance Ratings						
Symbol	Parameter	Maximum	Units			
$R_{\theta JA}$	Maximum Junction-to-Ambient ¹	62	°C/W			
Rejc	Maximum Junction-to-Case ¹	3.6	°C/W			

Electrical Characteristics (T _J =25°C unless otherwise specified)						
Symbol	Parameter	Test Conditions	Min.	Тур.	Max.	Units
$V_{GS(th)}$	Gate Threshold Voltage	V _{DS} =V _{GS} , I _D =-250μA	-1.2	-	-2.5	V
BV _{DSS}	Drain-Source Breakdown Voltage	V _{GS} =0V, I _D =-250µA	-60	-	-	V
g fs	Forward Transconductance	V _{DS} =-10V, I _D =-3A	-	7	-	S
I _{GSS}	Gate-Source Leakage Current	V _{DS} =0V, V _{GS} =±20V	-	-	±100	nA
I _{DSS}	Drain-Source Leakage Current	V _{DS} =-48V, V _{GS} =0V, T _J =25°C	-	-	-1	μА
		V _{DS} =-48V, V _{GS} =0V, T _J =125°C			-10	
Ppc (***)	Static Drain-Source On-Resistance ²	V _{GS} =-10V, I _D =-12A	-	- 70	70	mΩ
R _{DS} (on)		V _{GS} =-4.5V, I _D =-8A	-	-	105	
EAS	Single Pulse Avalanche Energy ⁵	V _{DD} =25V, L =0.1mH, I _{AS} =12A	7.2	-	-	mJ
V _{SD}	Diode Forward Voltage ²	Is =-1A, VGS =0V, TJ =25°C	-	-	-1.2	V
Is	Continuous Source Current ^{1,6}	V V 0V 5 0	-	-	-14	
Ism	Pulsed Source Current ^{2,6}	V _G =V _D =0V, Force Current	-	-	-28	Α



P-Channel 60-V (D-S) MOSFET

Dynamic and Switching Characteristics						
Symbol	Parameter	Test Conditions	Min.	Тур.	Max.	Units
Qg	Total Gate Charge ²	V _{DS} =-30V		16.4		
Qgs	Gate-Source Charge	I _D =-3A		3		nC
Qgd	Gate-Drain Charge	V _{GS} =-4.5V		3.6		
t _{d(on)}	Turn-On Delay Time ²	V _{DS} =-15V		28		
tr	Rise Time	I _D =-1A		19		
td(off)	Turn-Off Delay Time	V _{GS} =-10V		60		ns
tf	Fall Time	$R_G = 3.3\Omega$		8		
C _{ISS}	Input Capacitance	V _{DS} =-15V		1447		
Coss	Output Capacitance	V _{GS} =0V		97.3		pF
Crss	Reverse Transfer Capacitance	f =1.0MHz		70		1

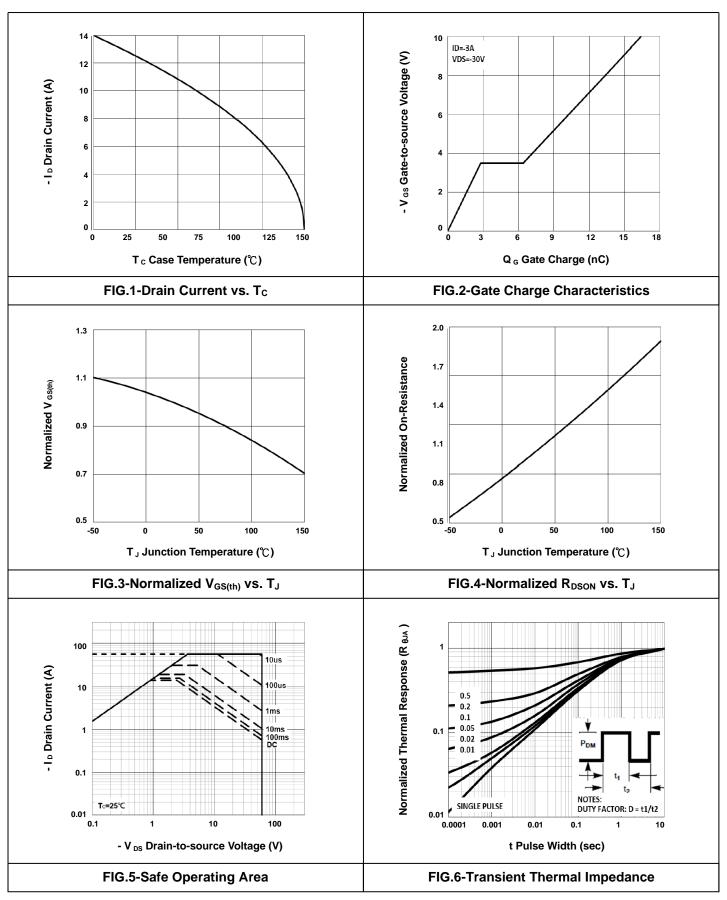
Notes

- 1. The data tested by surface mounted on a 1 inch 2 FR-4 board with 2OZ copper.
- The data tested by pulsed, pulse width ≤ 300us, duty cycle ≤ 2%.
- 3. The EAS data shows maximum rating. The test condition is V_{DD}=-25V, V_{GS}=-10V, L=0.1mH, I_{AS}=-25A.
- 4. The power dissipation is limited by 150°C junction temperature.
- 5. The Min. value is 100% EAS test guaranteed.
- 6. The data is theoretically the same as I_D and I_{DM}, in real applications, should be limited by total power dissipation.



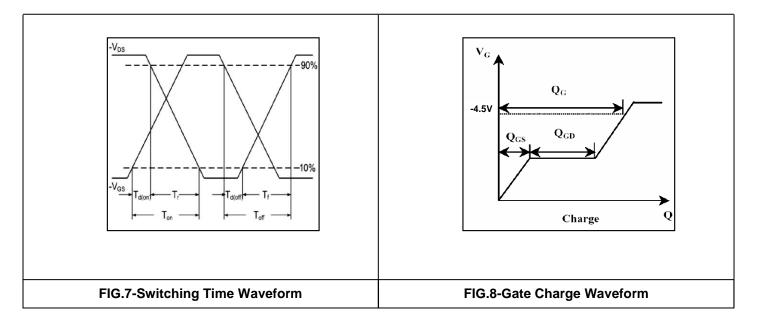
P-Channel 60-V (D-S) MOSFET

Typical Electrical Characteristics





P-Channel 60-V (D-S) MOSFET





P-Channel 60-V (D-S) MOSFET

Disclaimer

ALL PRODUCT, PRODUCT SPECIFICATIONS AND DATA ARE SUBJECT TO CHANGE WITHOUT NOTICE TO IMPROVE RELIABILITY, FUNCTION OR DESIGN OR OTHERWISE. Bruckewell Technology Inc., its affiliates, agents, and employees, and all persons acting on its or their behalf (collectively, "Bruckewell"), disclaim any and all liability for any errors, inaccuracies or incompleteness contained in any datasheet or in any other disclosure relating to any product. Bruckewell makes no warranty, representation or guarantee regarding the suitability of the products for any particular purpose or the continuing production of any product. To the maximum extent permitted by applicable law, Bruckewell disclaims

- (i) Any and all liability arising out of the application or use of any product.
- (ii) Any and all liability, including without limitation special, consequential or incidental damages.
- (iii) Any and all implied warranties, including warranties of fitness for particular purpose, non-infringement and merchantability.

Statements regarding the suitability of products for certain types of applications are based on Bruckewell's knowledge of typical requirements that are often placed on Bruckewell products in generic applications.

Such statements are not binding statements about the suitability of products for a particular application. It is the customer's responsibility to validate that a particular product with the properties described in the product specification is suitable for use in a particular application. Parameters provided in datasheets and/or specifications may vary in different applications and performance may vary over time.

Product specifications do not expand or otherwise modify Bruckewell's terms and conditions of purchase, including but not limited to the warranty expressed therein.