

MSH40N032

N-Channel 40-V (D-S) MOSFET

Description

The device is using trench DMOS technology. This advanced technology has been especially tailored to minimize $R_{DS(ON)}$, provide superior switching performance, and withstand high energy pulse in the avalanche and commutation mode. These devices are well suited for high efficiency fast switching applications.

The device meets the RoHS and Green Product requirement, 100% EAS guaranteed with full function reliability approved.

Features

- $R_{DS(ON)} = 3.2m\Omega @ V_{GS} = 10V$
- Low Gate Charge
- Excellent dv/dt Capability
- 100% EAS Guaranteed
- Green Device Available

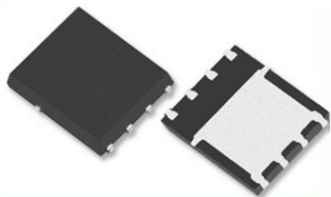
Typical Applications

- Power Management in Desktop Computer
- DC/DC converters
- Synchronous rectifier applications

Package type : PDFN 5X6

Packing & Order Information

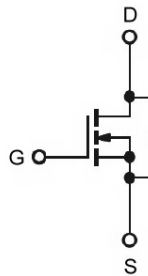
3,000/Reel



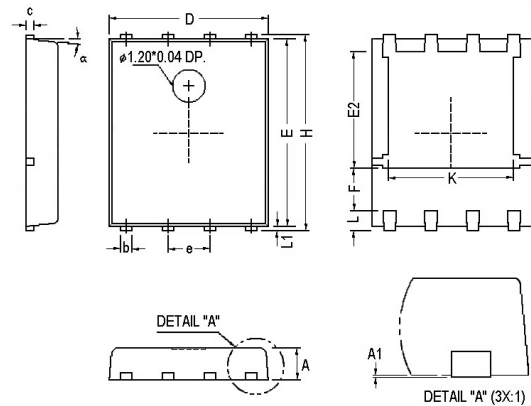
AEC-Q101 Qualified
Available

RoHS Compliant

Graphic Symbol

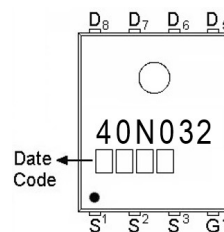


Package Dimension



| REF. | Millimeter | | | REF. | Millimeter | | |
|------|------------|------|------|----------|------------|------|------|
| | Min. | Nom. | Max. | | Min. | Nom. | Max. |
| A | 0.85 | 1.00 | 1.15 | E | 5.70 | - | 5.90 |
| A1 | 0.00 | - | 0.10 | e | - | 1.27 | - |
| b | 0.30 | - | 0.51 | H | 5.90 | - | 6.20 |
| c | 0.20 | - | 0.30 | L | - | 0.60 | - |
| D | 4.80 | - | 5.00 | L1 | 0.06 | - | 0.20 |
| F | 1.10 Ref. | | | α | 0° | - | 12° |
| E2 | 3.50 Ref. | | | K | 3.70 | 3.90 | 4.10 |

Marking



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MAXIMUM RATINGS AND ELECTRICAL CHARACTERISTICS

| Absolute Maximum Ratings | | | |
|----------------------------------|---|------------|-------|
| Symbol | Parameter | Value | Units |
| V _{DS} | Drain-Source Voltage | 40 | V |
| V _{GS} | Gate-Source Voltage | ±20 | V |
| I _D | Continuous Drain Current ¹ (T _C =25°C) | 90 | A |
| | Continuous Drain Current ¹ (T _C =100°C) | 72 | A |
| I _{DM} | Pulsed Drain Current ^{1,2} | 240 | A |
| I _{AS} | Single Pulse Avalanche Current, L =0.1mH ³ | 54 | A |
| E _{AS} | Single Pulse Avalanche Energy, L =0.1mH ³ | 145 | mJ |
| P _D | Power Dissipation ⁴ (T _C =25°C) | 50 | W |
| T _J /T _{STG} | Operating Junction and Storage Temperature | -55 to 150 | °C |

| Thermal Resistance Ratings | | | |
|----------------------------|--|---------|-------|
| Symbol | Parameter | Maximum | Units |
| R _{θJA} | Maximum Junction-to-Ambient ¹ | 55 | °C/W |
| R _{θJC} | Maximum Junction-to-Case ¹ | 2.5 | °C/W |

| Electrical Characteristics (T _J =25°C unless otherwise specified) | | | | | | |
|--|--|---|------|------|------|-------|
| Symbol | Parameter | Test Conditions | Min. | Typ. | Max. | Units |
| V _{GS(th)} | Gate Threshold Voltage | V _{DS} =V _{GS} , I _D =250μA | 1.2 | - | 2.2 | V |
| BV _{DSS} | Drain-Source Breakdown Voltage | V _{GS} =0V, I _D =250μA | 40 | - | - | V |
| I _{GSS} | Gate-Source Leakage Current | V _{DS} =0V, V _{GS} =±20V | - | - | ±100 | nA |
| I _{DSS} | Drain-Source Leakage Current | V _{DS} =40V, V _{GS} =0V, T _J =25°C | - | - | 1 | μA |
| | | V _{DS} =40V, V _{GS} =0V, T _J =55°C | - | - | 5 | |
| R _{DS(on)} | Static Drain-Source On-Resistance ² | V _{GS} =10V, I _D =20A | - | 2.5 | 3.2 | mΩ |
| | | V _{GS} =4.5V, I _D =15A | - | 3.8 | 5.3 | |
| E _{AS} | Single Pulse Avalanche Energy ⁵ | V _{DD} =25V, L =0.1mH, I _{AS} =25A | 31 | - | - | mJ |
| V _{SD} | Diode Forward Voltage ² | I _S =1A, V _{GS} =0V, T _J =25°C | - | - | 1.0 | V |
| I _S | Continuous Source Current ^{1,6} | V _G =V _D =0V, Force Current | - | - | 30 | A |
| I _{SM} | Pulsed Source Current ^{2,6} | | - | - | 60 | |

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| Dynamic | | | | | | |
|---------------------|---------------------------------|---|------|------|------|-------|
| Symbol | Parameter | Test Conditions | Min. | Typ. | Max. | Units |
| Q _g | Total Gate Charge ² | V _{DS} =20V | -- | 22.7 | -- | nC |
| Q _{gs} | Gate-Source Charge | I _D =20A | -- | 7.5 | -- | |
| Q _{gd} | Gate-Drain Charge | V _{GS} =10V | -- | 5.5 | -- | |
| t _{d(on)} | Turn-On Delay Time ² | V _{DS} =20V | -- | 10 | -- | ns |
| t _r | Rise Time | I _D =20A | -- | 5 | -- | |
| t _{d(off)} | Turn-Off Delay Time | V _{GS} =10V | -- | 33 | -- | |
| t _f | Fall Time | R _G =3Ω | -- | 6.5 | -- | |
| C _{ISS} | Input Capacitance | V _{DS} =20V | -- | 2648 | -- | pF |
| C _{OSS} | Output Capacitance | V _{GS} =0V | -- | 899 | -- | |
| C _{RSS} | Reverse Transfer Capacitance | f =1.0MHz | -- | 71 | -- | |
| R _g | Gate Resistance | V _{DS} =0V, V _{GS} =0V, f =1.0MHz | | 1.5 | | Ω |

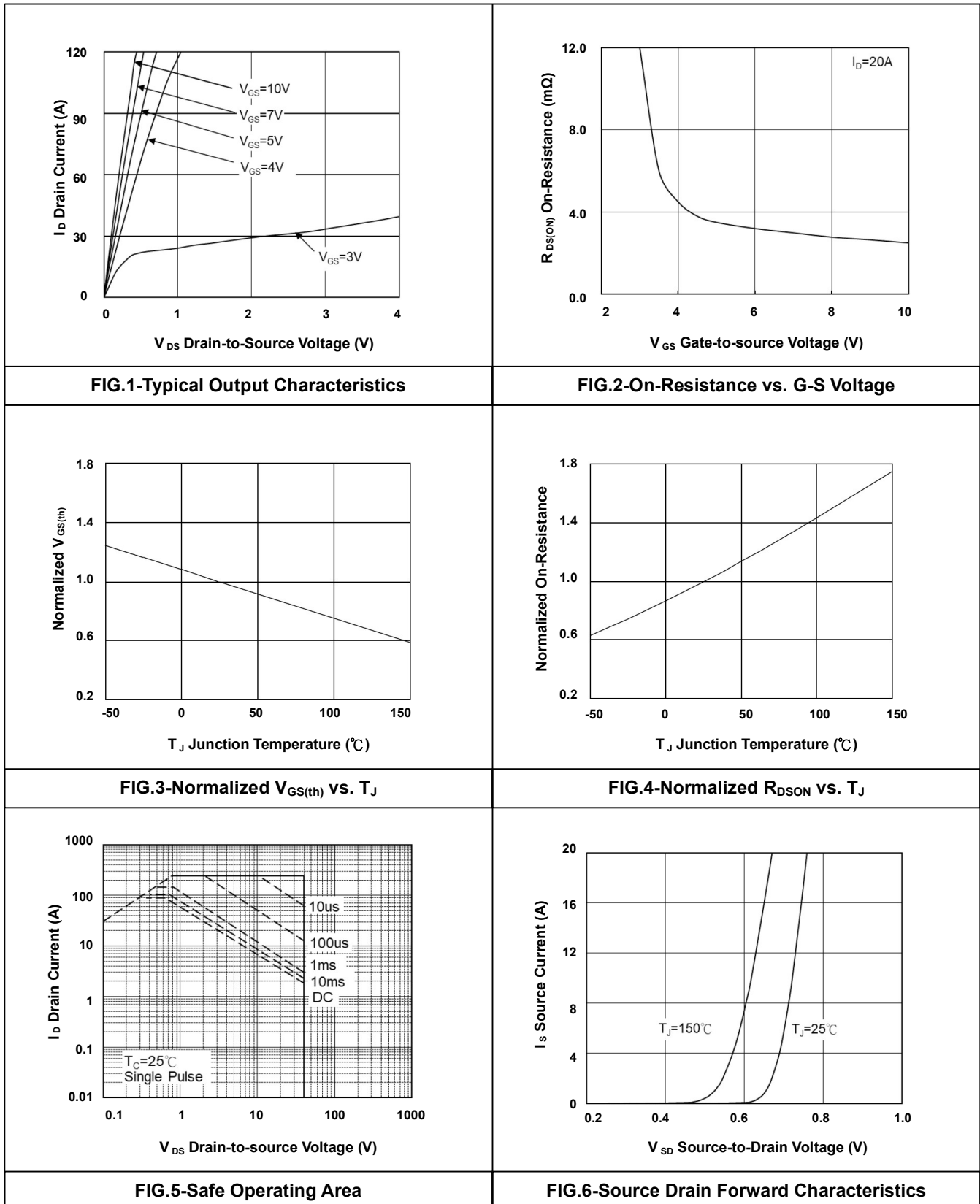
Notes

1. The data tested by surface mounted on a 1 inch² FR-4 board with 2OZ copper.
2. The data tested by pulsed, pulse width ≤ 300us, duty cycle ≤ 2%.
3. The EAS data shows maximum rating. The test condition is V_{DD}=25V, V_{GS}=10V, L=0.1mH, I_{AS}=54A.
4. The power dissipation is limited by 150°C junction temperature.
5. The Min. value is 100% EAS tested guarantee.
6. The data is theoretically the same as I_D and I_{DM}, in real applications, should be limited by total power dissipation.

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- Typical Electrical Characteristics



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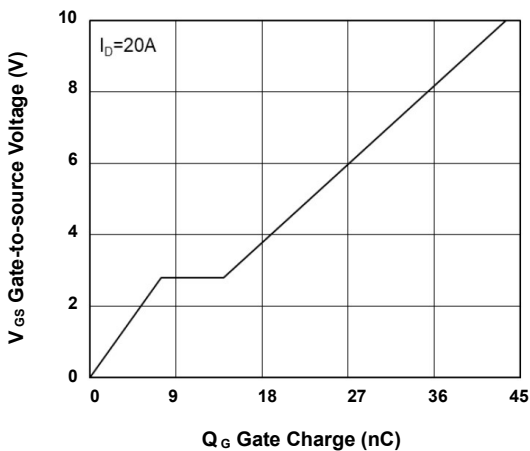


FIG.7-Gate Charge Characteristics

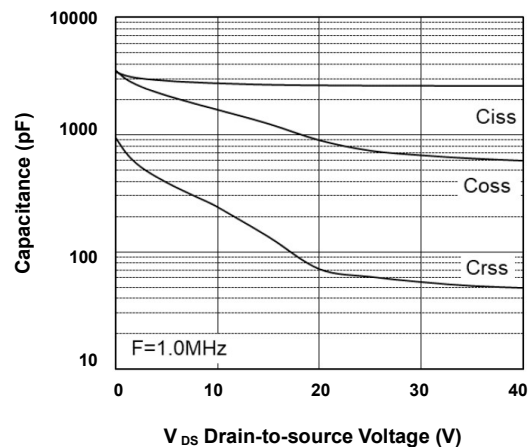


FIG.8-Capacitance Characteristics

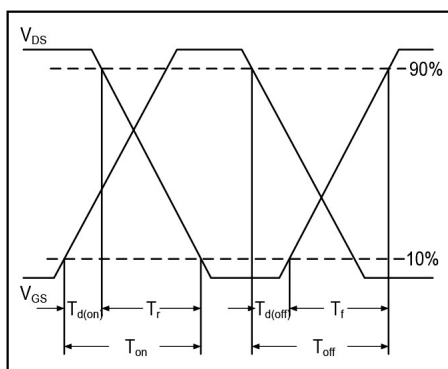


FIG.9-Switching Time Waveform

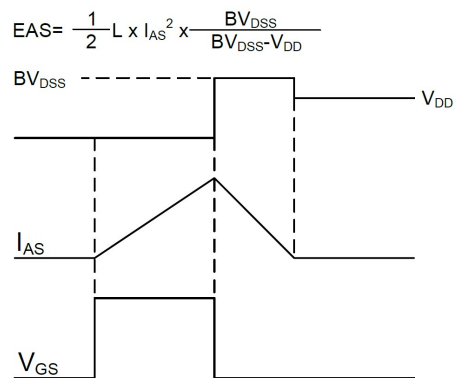


FIG.10-Unclamped Inductive Switching Waveform

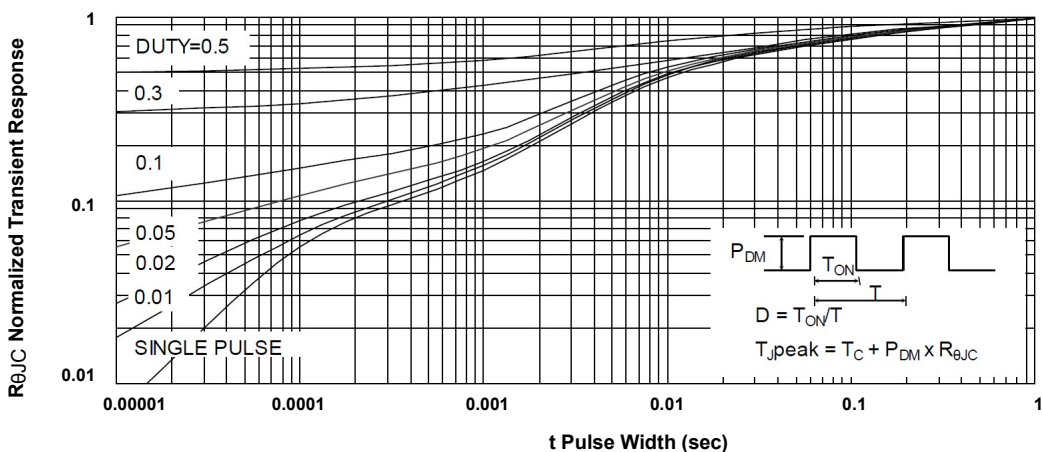


FIG.11-Normalized Maximum Transient Thermal Impedance

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