

AO6400 30V N-Channel MOSFET

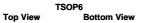
General Description

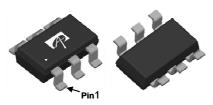
The AO6400 uses advanced trench technology to provide excellent $R_{DS(ON)}$, low gate charge and operation with gate voltages as low as 2.5V. This device is suitable for use as a load switch or in PWM applications.

Product Summary

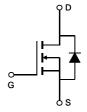
 $\begin{array}{lll} V_{DS} & 30V \\ I_{D} \; (at \, V_{GS} \! = \! 10V) & 6.9A \\ R_{DS(ON)} \; (at \, V_{GS} \! = \! 10V) & < 28m\Omega \\ R_{DS(ON)} \; (at \, V_{GS} \! = \! 4.5V) & < 33m\Omega \\ R_{DS(ON)} \; (at \, V_{GS} \! = \! 2.5V) & < 52m\Omega \end{array}$











Absolute Maximum Ratings T_A=25℃ unless otherwise noted

Parameter		Symbol	Maximum	Units	
Drain-Source Voltage		V _{DS}	30	V	
Gate-Source Voltage		V_{GS}	±12	V	
Continuous Drain	T _A =25℃		6.9		
Current	T _A =70℃	'D	5.8	A	
Pulsed Drain Current ^C		I _{DM}	35]	
Power Dissipation B $T_A=25^{\circ}$		D	2	W	
		P _D	1.3	\ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \	
Junction and Storage	Temperature Range	T _J , T _{STG}	-55 to 150	C	

Thermal Characteristics							
Parameter	Symbol	Тур	Max	Units			
Maximum Junction-to-Ambient A	t ≤ 10s	D	47.5	62.5	℃/W		
Maximum Junction-to-Ambient AD	Steady-State	$R_{\theta JA}$	74	110	℃/W		
Maximum Junction-to-Lead	Steady-State	$R_{\theta JL}$	37	50	℃/W		



Electrical Characteristics (T_J=25℃ unless otherwise noted)

Symbol	Parameter	Conditions	Min	Тур	Max	Units
STATIC I	PARAMETERS					
BV _{DSS}	Drain-Source Breakdown Voltage	$I_D = 250 \mu A, V_{GS} = 0 V$	30			V
I _{DSS}	Zero Gate Voltage Drain Current	V _{DS} =30V, V _{GS} =0V			1	μА
DSS	Zero Gate Voltage Brain Gurrent	T _J =55	C		5	μΑ
I_{GSS}	Gate-Body leakage current	V_{DS} =0V, V_{GS} = ±12V			100	nA
$V_{GS(th)}$	Gate Threshold Voltage	$V_{DS}=V_{GS} I_{D}=250\mu A$	0.65	1.05	1.45	V
$I_{D(ON)}$	On state drain current	V_{GS} =4.5V, V_{DS} =5V	35			Α
		V _{GS} =10V, I _D =6.9A		18	28	mΩ
R	Static Drain-Source On-Resistance	T _J =125	C	28	39	11152
$R_{DS(ON)}$	Static Drain-Source On-Resistance	V_{GS} =4.5V, I_D =6A		19	33	mΩ
		V_{GS} =2.5V, I_D =5A		24	52	mΩ
g _{FS}	Forward Transconductance	$V_{DS} = 5V, I_{D} = 6.9A$		33		S
V_{SD}	Diode Forward Voltage	I _S =1A,V _{GS} =0V		0.7	1	V
I _S	Maximum Body-Diode Continuous Cur			2	Α	
DYNAMI	CPARAMETERS					
C _{iss}	Input Capacitance			630		pF
C _{oss}	Output Capacitance	V_{GS} =0V, V_{DS} =15V, f=1MHz		75		pF
C _{rss}	Reverse Transfer Capacitance	1		50		pF
R_g	Gate resistance	V _{GS} =0V, V _{DS} =0V, f=1MHz	1.5	3	4.5	Ω
SWITCH	NG PARAMETERS					
Q_g	Total Gate Charge			6	7	nC
Q_{gs}	Gate Source Charge	V_{GS} =4.5V, V_{DS} =15V, I_{D} =6.9A		1.3		nC
Q_{gd}	Gate Drain Charge	1		1.8		nC
t _{D(on)}	Turn-On DelayTime			3		ns
t _r	Turn-On Rise Time	V_{GS} =10V, V_{DS} =15V, R_{L} =2.2 Ω ,		2.5		ns
t _{D(off)}	Turn-Off DelayTime	$R_{GEN}=3\Omega$		25		ns
t _f	Turn-Off Fall Time			4		ns
t _{rr}	Body Diode Reverse Recovery Time	I _F =6.9A, dI/dt=100A/μs		8.5		ns
Q _{rr}	Body Diode Reverse Recovery Charge	I _F =6.9A, dI/dt=100A/μs		2.6		nC

A. The value of $R_{\theta JA}$ is measured with the device mounted on 1in² FR-4 board with 2oz. Copper, in a still air environment with T_A =25° C. The value in any given application depends on the user's specific board design. B. The power dissipation P_D is based on $T_{J(MAX)}$ =150° C, using \leqslant 10s junction-to-ambient thermal resistance.

THIS PRODUCT HAS BEEN DESIGNED AND QUALIFIED FOR THE CONSUMER MARKET. APPLICATIONS OR USES AS CRITICAL COMPONENTS IN LIFE SUPPORT DEVICES OR SYSTEMS ARE NOT AUTHORIZED. AOS DOES NOT ASSUME ANY LIABILITY ARISING OUT OF SUCH APPLICATIONS OR USES OF ITS PRODUCTS. AOS RESERVES THE RIGHT TO IMPROVE PRODUCT DESIGN, FUNCTIONS AND RELIABILITY WITHOUT NOTICE.

C. Repetitive rating, pulse width limited by junction temperature T_{J(MAX)}=150° C. Ratings are based on low frequency and duty cycles to keep initialT_{.1}=25° C.

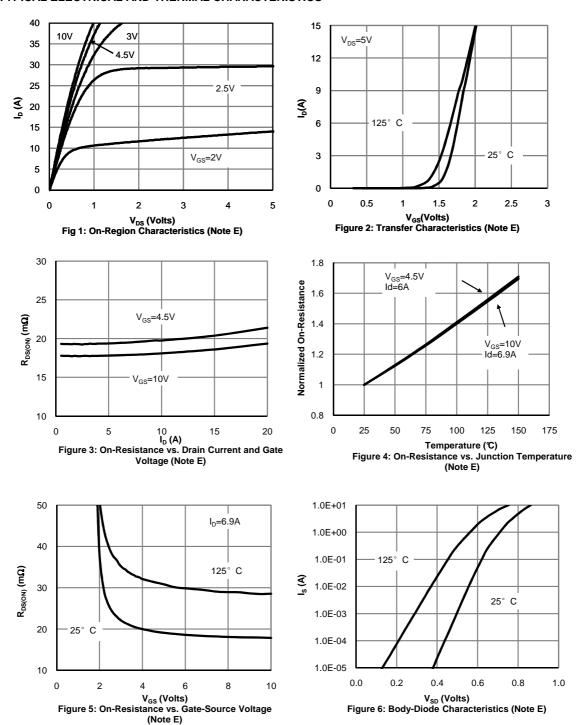
D. The $R_{\theta JA}$ is the sum of the thermal impedence from junction to lead $R_{\theta JL}$ and lead to ambient.

E. The static characteristics in Figures 1 to 6 are obtained using <300μs pulses, duty cycle 0.5% max.

F. These curves are based on the junction-to-ambient thermal impedence which is measured with the device mounted on 1in² FR-4 board with 2oz. Copper, assuming a maximum junction temperature of $T_{J(MAX)}$ =150 $^{\circ}$ C. The SOA curve provides a single pulse rating.



TYPICAL ELECTRICAL AND THERMAL CHARACTERISTICS





TYPICAL ELECTRICAL AND THERMAL CHARACTERISTICS

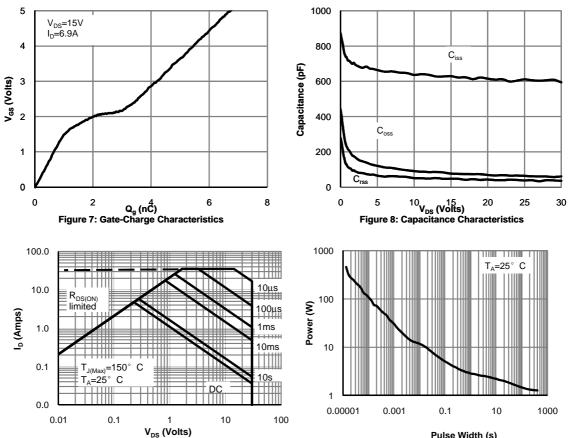


Figure 9: Maximum Forward Biased Safe Operating Area (Note F)

Pulse Width (s)
Figure 10: Single Pulse Power Rating Junction-toAmbient (Note F)

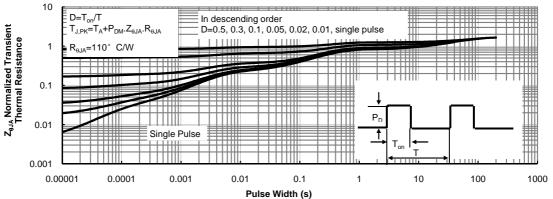
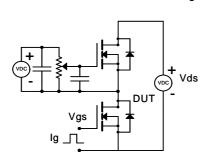
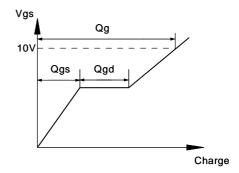


Figure 11: Normalized Maximum Transient Thermal Impedance (Note F)

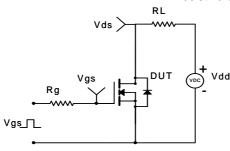


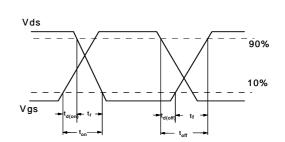
Gate Charge Test Circuit & Waveform



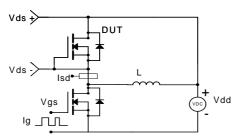


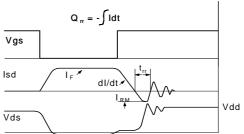
Resistive Switching Test Circuit & Waveforms





Diode Recovery Test Circuit & Waveforms

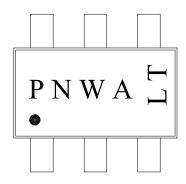






Document No.	PD-00098		
Version	F		
Title	AO6400 Marking Description		

TSOP-6 PACKAGE MARKING DESCRIPTION



Green product

NOTE:

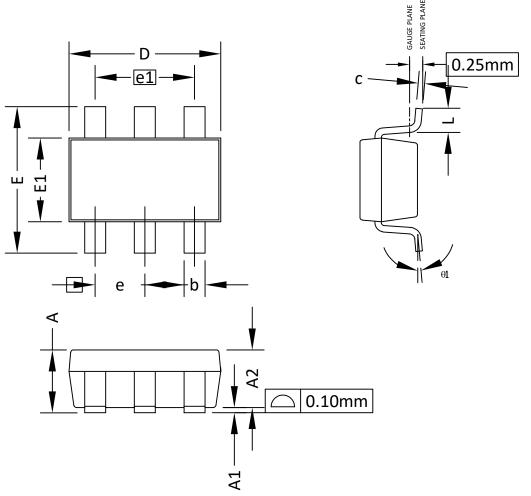
PN - Part number code
W - Year and week code
A - Assembly location code
L&T - Assembly lot code

PART NO.	DESCRIPTION	CODE (PN)
AO6400	Green product	D0
AO6400L	Green product	D0

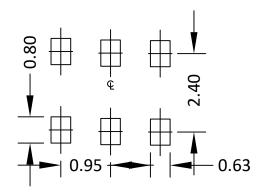


Document No.	PO-00002
Version	1

TSOP6 PACKAGE OUTLINE



RECOMMENDED LAND PATTERN



UNIT: mm

S.4.4.0.1.6		DIM. IN MN	1	DIM. IN INCH		
SYMBOLS	MIN.	NOM.	MAX.	MIN.	NOM.	MAX.
Α	0.800		1.250	0.031		0.049
A1	0.000		0.150	0.000		0.006
A2	0.700	1.100	1.200	0.028	0.043	0.047
b	0.300	0.400	0.500	0.012	0.016	0.020
С	0.080	0.130	0.200	0.003	0.005	0.008
D	2.700	2.900	3.100	0.106	0.114	0.122
E	2.500	2.800	3.100	0.098	0.110	0.122
E1	1.500	1.600	1.700	0.059	0.063	0.067
е		0.95BSC		0.37BSC		
e1	1.9BSC			0.075BSC		
L	0.300		0.600	0.012		0.024
θ1	0°		1.700	0°		0.067

NOT

- 1. PACKAGE BODY SIZES EXCLUDE MOLD FLASH AND GATE BURRS. MOLD FLASH AT THE NON-LEAD SIDES SHOULD BE LESS THAN 5 MILS EACH.
- 2. DIMENSION "L" IS MEASURED IN GAUGE PLANE.
- 3. TOLERANCE ±0.100 mm(4 mil) UNLESS OTHERWISE SPECIFIED.
- 4. FOLLOWED FROM JEDEC MO-178C & MO-193C.
- 5. CONTROLLING DIMENSIONS IS MILLIMETER. CONVERTED INCH DIMENSIONS ARE NOT NECESSARILY EXACT.



AOS Semiconductor Product Reliability Report

AO6400/AO6400L, rev B

Plastic Encapsulated Device

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Jun 27, 2006



This AOS product reliability report summarizes the qualification result for AO6400. Accelerated environmental tests are performed on a specific sample size, and then followed by electrical test at end point. Review of final electrical test result confirms that AO6400 passes AOS quality and reliability requirements. The released product will be categorized by the process family and be monitored on a quarterly basis for continuously improving the product quality.

Table of Contents:

- I. Product Description
- II. Package and Die information
- III. Environmental Stress Test Summary and Result
- IV. Reliability Evaluation
- V. Quality Assurance Information

I. Product Description:

The AO6400 uses advanced trench technology to provide excellent $R_{\rm DS(ON)}$, low gate charge and operation with gate voltages as low as 2.5V. This device is suitable for use as a load switch or in PWM applications. Standard product AO6400 is Pb-free (meets ROHS & Sony 259 specifications). AO6400 is a Green Product ordering option. AO6400 and AO6400L are electrically identical.

Absolute Maximum Ratings T _A =25°C unless otherwise noted						
Parameter		Symbol	Maximum	Units		
Drain-Source Voltage		V _{DS}	30	V		
Gate-Source Voltage		V_{GS}	±12	V		
Continuous Drain	T _A =25°C		6.9			
Current	T _A =70°C	I _D	5.8	Α		
Pulsed Drain Current	Pulsed Drain Current		35			
	T _A =25°C	P _D	2	W		
Power Dissipation	T _A =70°C	I D	1.44	VV		
Junction and Storage Temperature						
Range		T_J,T_STG	-55 to 150	°C		

Thermal Characteristics							
Parameter	Symbol	Тур	Max	Units			
Maximum Junction-to- Ambient	T ≤ 10s	В	47.5	62.5	°C/W		
Maximum Junction-to- Ambient	Steady- State	$R_{ hetaJA}$	74	110	°C/W		
Maximum Junction-to-Lead	Steady- State	$R_{ hetaJL}$	37	50	°C/W		



II. Die / Package Information:

AO6400 AO6400L (Green Compound)

Process Standard sub-micron Standard sub-micron

low voltage N channel process low voltage N channel process

Package Type6 leads TSOP6 leads TSOPLead FrameCopper with Ag spotCopper with Ag spot

Die AttachAg epoxyAg epoxyBond wireAu 2milsAu 2mils

Mold Material Epoxy resin with silica filler Epoxy resin with silica filler

Filler % (Spherical/Flake)90/10100/0Flammability RatingUL-94 V-0UL-94 V-0Backside MetallizationTi / Ni / AgTi / Ni / AgMoisture LevelUp to Level 1 *Up to Level 1*

Note * based on info provided by assembler and mold compound supplier

III. Result of Reliability Stress for AO6400 (Standard) & AO6400L (Green)

Test Item	Test Condition	Time Point	Lot Attribution	Total Sample size	Number of Failures
Solder Reflow Precondition	Standard: 1hr PCT+3 cycle reflow@260°c Green: 168hr 85°c /85%RH +3 cycle reflow@260°c	0hr	Standard: 18 lots Green: 7 lots	3685pcs	0
HTGB	Temp = 150°c, Vgs=100% of Vgsmax	168 / 500 hrs 1000 hrs	9 lots (Note A*)	738pcs 77+5 pcs / lot	0
HTRB	Temp = 150°c , Vds=80% of Vdsmax	168 / 500 hrs 1000 hrs	9 lots (Note A*)	738pcs 77+5 pcs / lot	0
HAST	130 +/- 2°c , 85%RH, 33.3 psi, Vgs = 80% of Vgs max	100 hrs	Standard: 16 lots Green: 6 lots (Note B**)	1210pcs 50+5 pcs / lot	0
Pressure Pot	121°c , 15+/-1 PSIG, RH=100%	96 hrs	Standard: 13 lots Green: 7 lots (Note B**)	1100pcs 50+5 pcs / lot	0
Temperature Cycle	-65°c to 150°c, air to air,	250 / 500 cycles	Standard: 18 lots Green: 7 lots (Note B**)	1375pcs 50+5 pcs / lot	0



III. Result of Reliability Stress for AO6400 (Standard) & AO6400L (Green) Continues

Internal Vision	NA	5	5	0
		5	5	
X-ray		5	5	
	NA	5	5	0
Room Temp	0hr	40	40 wires	0
150°c bake 150°c bake	250hr 500hr	40 40	40 wires 40 wires	
230°c	5 sec	15	15 leads	0
150°c	0hr	10	10	0
	Room Temp 150°c bake 150°c bake 230°c	Cross-section X-ray NA Room Temp 150°c bake 150°c bake 250hr 500hr 230°c 5 sec	Cross-section X-ray 5 NA 5 Room Temp 150°c bake 250hr 40 150°c bake 500hr 40 40 230°c 5 sec 15 5	Cross-section X-ray 5 5 5 NA 5 5 5 Room Temp 150°c bake 150°c bake 150°c bake 150°c bake 250hr 40 40 wires 40 wi

Note A: The HTGB and HTRB reliability data presents total of available AO6400 and AO6400L burn-in data up to the published date.

Note B: The pressure pot, temperature cycle and HAST reliability data for AO6400 and AO6400L comes from the AOS generic package qualification data.

IV. Reliability Evaluation

FIT rate (per billion): 2.4 MTTF = 47564years

In general,500 hrs of HTGB, 150 deg C accelerated stress testing is equivalent to 15 years of lifetime at 55 deg C operating conditions (by applying the Arrhenius equation with an activation energy of 0.7eV and 60% of upper confidence level on the failure rate calculation). AOS reliability group also routinely monitors the product reliability up to 1000 hr at and performs the necessary failure analysis on the units failed for reliability test(s).

The presentation of FIT rate for the individual product reliability is restricted by the actual burn-in sample size of the selected product (AO6400). Failure Rate Determination is based on JEDEC Standard JESD 85. FIT means one failure per billion hours.

Failure Rate = $\text{Chi}^2 \times 10^9 \text{/} [2 \text{ (N) (H) (Af)}] = 1.83 \times 10^9 \text{/} [2 (9 \times 164) (1000) (258)] = 2.4 \text{ MTTF} = <math>10^9 \text{/} \text{FIT} = 4.16 \times 10^8 \text{hrs} = 47564 \text{years}$

 ${f Chi^2}$ = Chi Squared Distribution, determined by the number of failures and confidence interval

N = Total Number of units from HTRB and HTGB tests

H = Duration of HTRB/HTGB testing

Af = Acceleration Factor from Test to Use Conditions (Ea = 0.7eV and Tuse = 55°C)

Acceleration Factor [Af] = Exp [Ea / k (1/Tj u - 1/Tj s)]

Acceleration Factor ratio list:

	55 deg C	70 deg C	85 deg C	100 deg C	115 deg C	130 deg C	150 deg C
Af	258	87	32	13	5.64	2.59	1

Tj s = Stressed junction temperature in degree (Kelvin), K = C+273.16

Tj u = The use junction temperature in degree (Kelvin), K = C+273.16

k = Boltzmann's constant, 8.617164 X 10 ⁻⁵eV / K

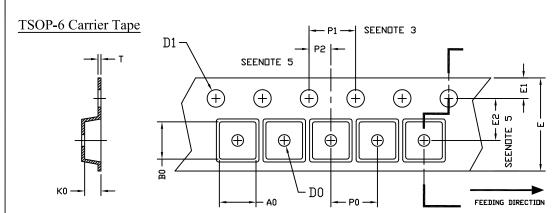


V. Quality Assurance Information

Acceptable Quality Level for outgoing inspection: **0.1%** for electrical and visual. Guaranteed Outgoing Defect Rate: **< 25 ppm** Quality Sample Plan: conform to **Mil-Std-105D**



TSOP-6 Tape and Reel Data



UNIT: MM

PACKA	E A0	В0	К0	D0	D1	E	E1	E2	P0	P1	P2	Т
SDT-2		3.27 ±0.10	1.34 ±0.10	1.10 ±0.01	1.50 +0.10	8.00 ±0.20	1.75 ±0.10	3.50 ±0.05	4.00 ±0.10	4.00 ±0.10	2.00 ±0.10	0.25 ±0.05

