

ATM2302NSA

N-Channel Enhancement Mode Field Effect Transistor

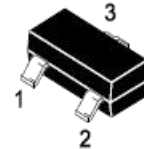
Drain-Source Voltage: 20V

Drain Current: 3.3A

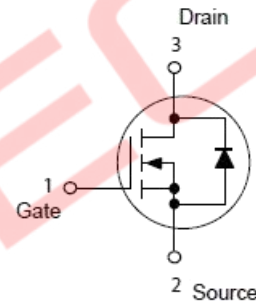
Features

- ◆ Trench FET Power MOSFET
- ◆ Excellent $R_{DS(on)}$ and Low Gate Charge
- ◆ $R_{DS(on)} < 55m\Omega$ ($V_{GS} = 4.5V$)
- ◆ $R_{DS(on)} < 75m\Omega$ ($V_{GS} = 2.5V$)

SOT-23



1 Gate 2 Source 3 Drain



Marking:M22

Application

- ◆ DC/DC Converter
- ◆ Load Switch for Portable Devices
- ◆ Battery Switch

Absolute maximum ratings (Ta=25°C unless otherwise noted)

Parameter	Symbol	Value	Unit
Drain-Source Voltage	V_{DS}	20	V
Gate-Source Voltage	V_{GS}	± 8	V
Continuous Drain Current	I_D	3.3	A
Plused Drain Current	I_{DM}	16	A
Power Dissipation	P_D	0.9	W
Thermal Resistance from Junction to Ambient	$R_{\theta JA}$	139	$^{\circ}C/W$
Junction Temperature	T_J	150	$^{\circ}C$
Storage Temperature	T_{STG}	-55~ +150	$^{\circ}C$

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Electrical characteristics (T_A=25 °C, unless otherwise noted)

Parameter	Symbol	Test Condition	Min.	Typ.	Max.	Unit
Static Characteristics						
Drain-source breakdown voltage	V _{(BR)DSS}	V _{GS} = 0V, I _D =250μA	20			V
Zero gate voltage drain current	I _{DSS}	V _{DS} =20V, V _{GS} = 0V			1	μA
Gate-body leakage current	I _{GSS}	V _{GS} =±8V, V _{DS} = 0V			±0.1	μA
Gate threshold voltage	V _{GS(th)}	V _{DS} =V _{GS} , I _D =250μA	0.5	0.7	1	V
Drain-source on-resistance ¹⁾	R _{DS(on)}	V _{GS} =4.5V, I _D =3A			55	mΩ
		V _{GS} =2.5V, I _D =2A			75	
Forward tranconductance ¹⁾	g _{FS}	V _{DS} =5V, I _D =3A		8		S
Dynamic characteristics						
Input Capacitance ²⁾	C _{iss}	V _{DS} =10V, V _{GS} =0V, f=1MHz		300		pF
Output Capacitance ²⁾	C _{oss}			120		pF
Reverse Transfer Capacitance ²⁾	C _{rss}			80		pF
Total gate charge	Q _g	V _{DS} =10V, V _{GS} =4.5V, I _D =3A		4		nC
Gate-source charge	Q _{gs}			0.7		nC
Gate-drain charge	Q _{gd}			1.5		nC
Switching Characteristics²⁾						
Turn-on delay time	t _{d(on)}	V _{DD} =10V, I _D =3A V _{GEN} =4.5V, R _g =6Ω		10		ns
Turn-on rise time	t _r			50		ns
Turn-off delay time	t _{d(off)}			17		ns
Turn-off fall time	t _f			10		ns
Source-Drain Diode characteristics						
Diode Forward voltage	V _{SD}	V _{GS} =0V, I _S =3.3A		0.75	1.2	V
Diode Forward Current	I _S				3.3	A

Notes:

- 1) Pulse Test: Pulse width ≤300μs, duty cycle ≤2%.
- 2) These parameters have no way to verify.

Typical Characteristics Curves

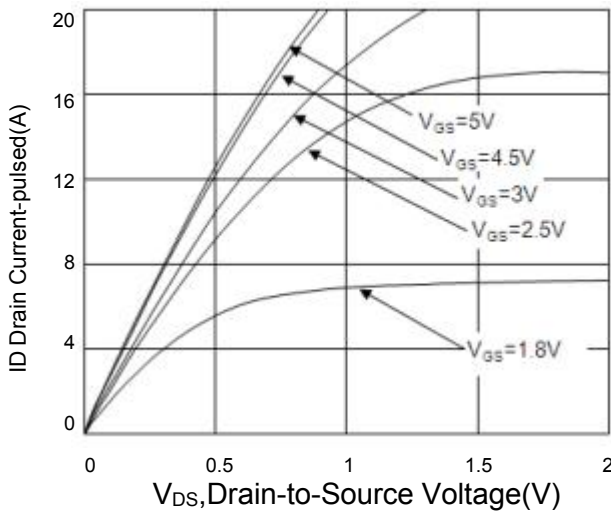


Fig.1 Typical Output Characteristics

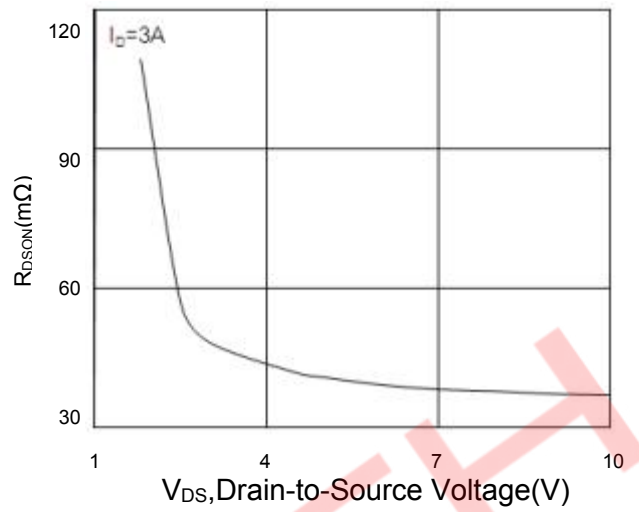


Fig.2 On-Resistance vs. Gate-Source Voltage

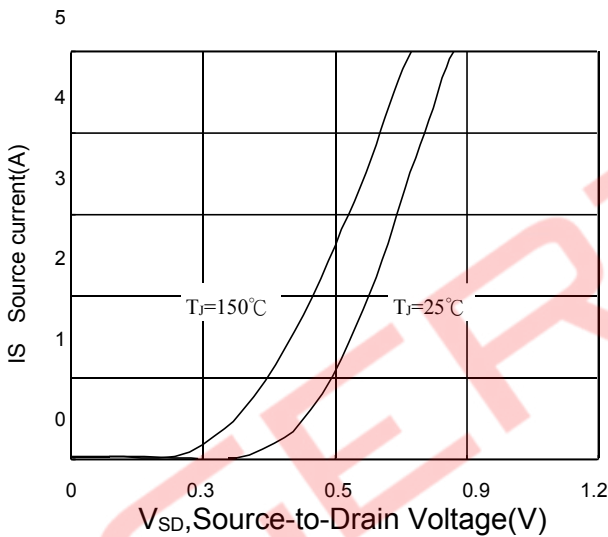


Fig.3 Forward Characteristics of Reverse

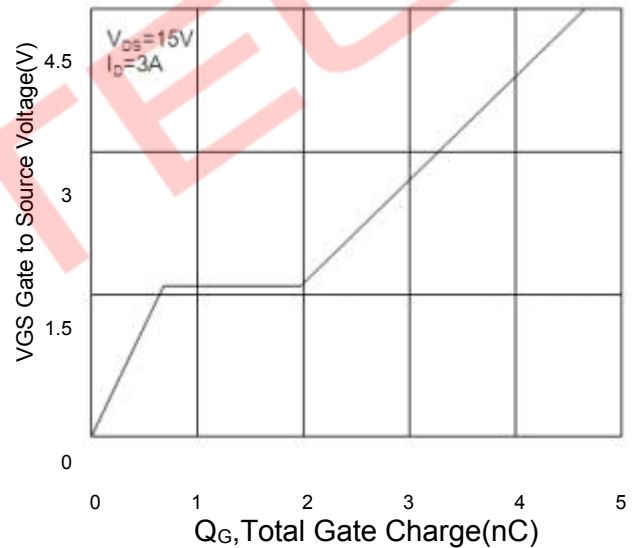


Fig.4 Gate-Charge Characteristics

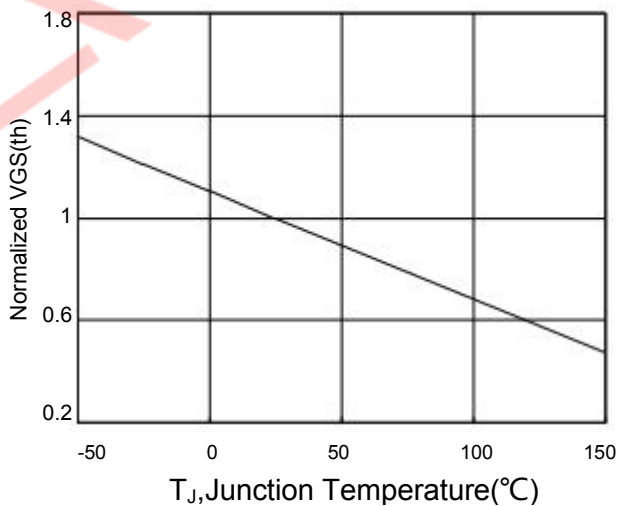


Fig.5 Normalized $V_{GS(th)}$ vs. T_J

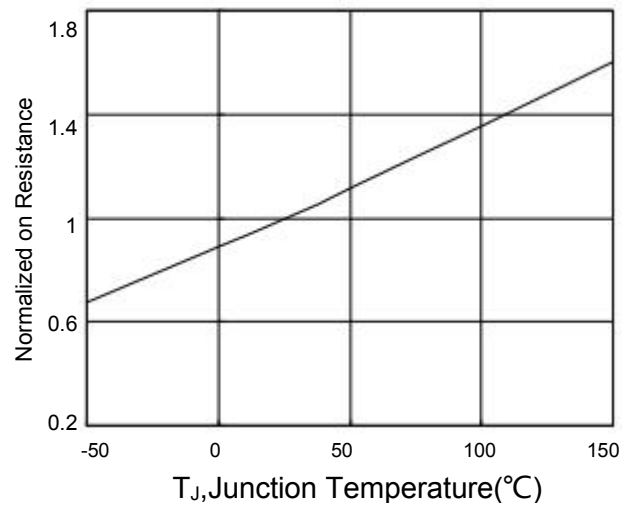


Fig.6 Normalized $R_{DS(on)}$ vs. T_J

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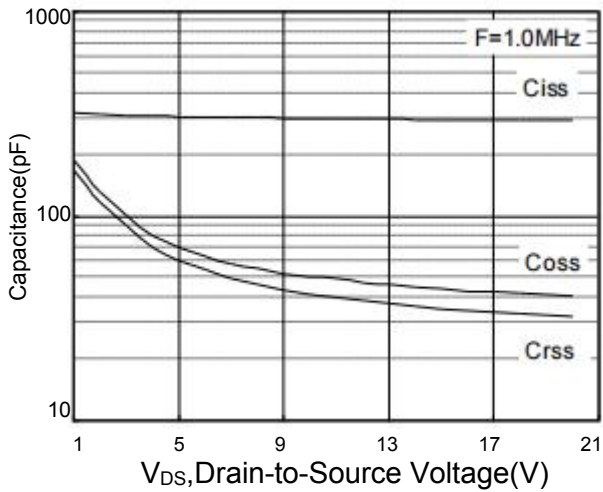


Fig.7 Capacitance

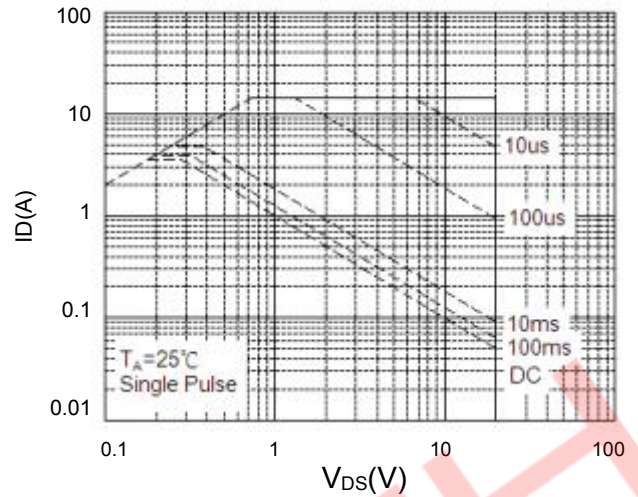


Fig.8 Safe Operating Area

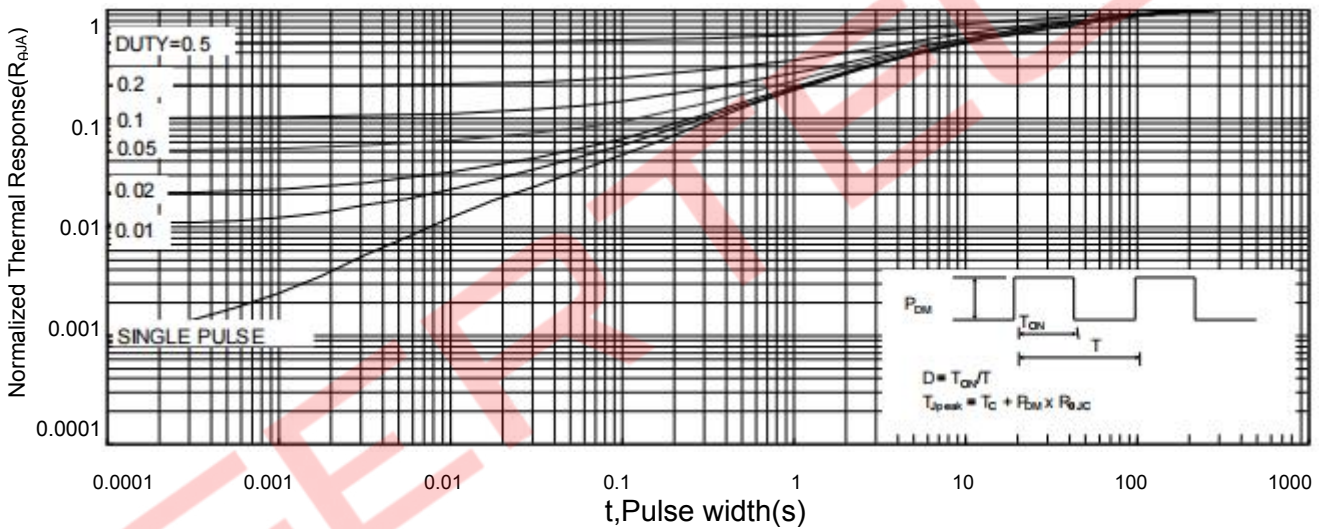


Fig.9 Normalized Maximum Transient Thermal Impedance

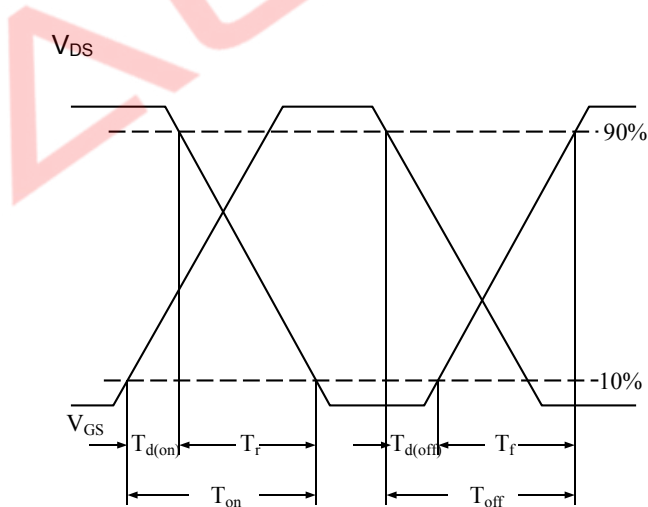


Fig.10 Switching Time Waveform

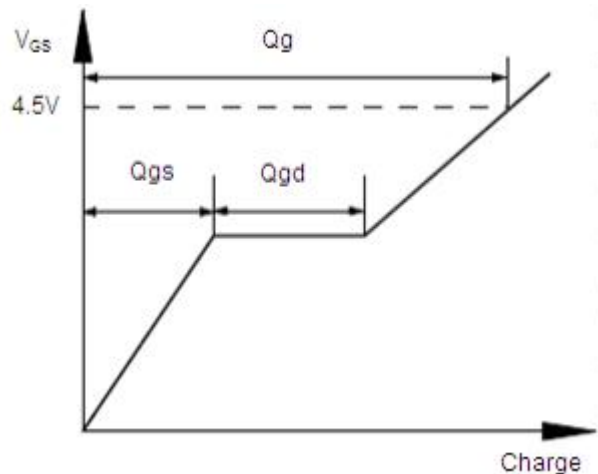
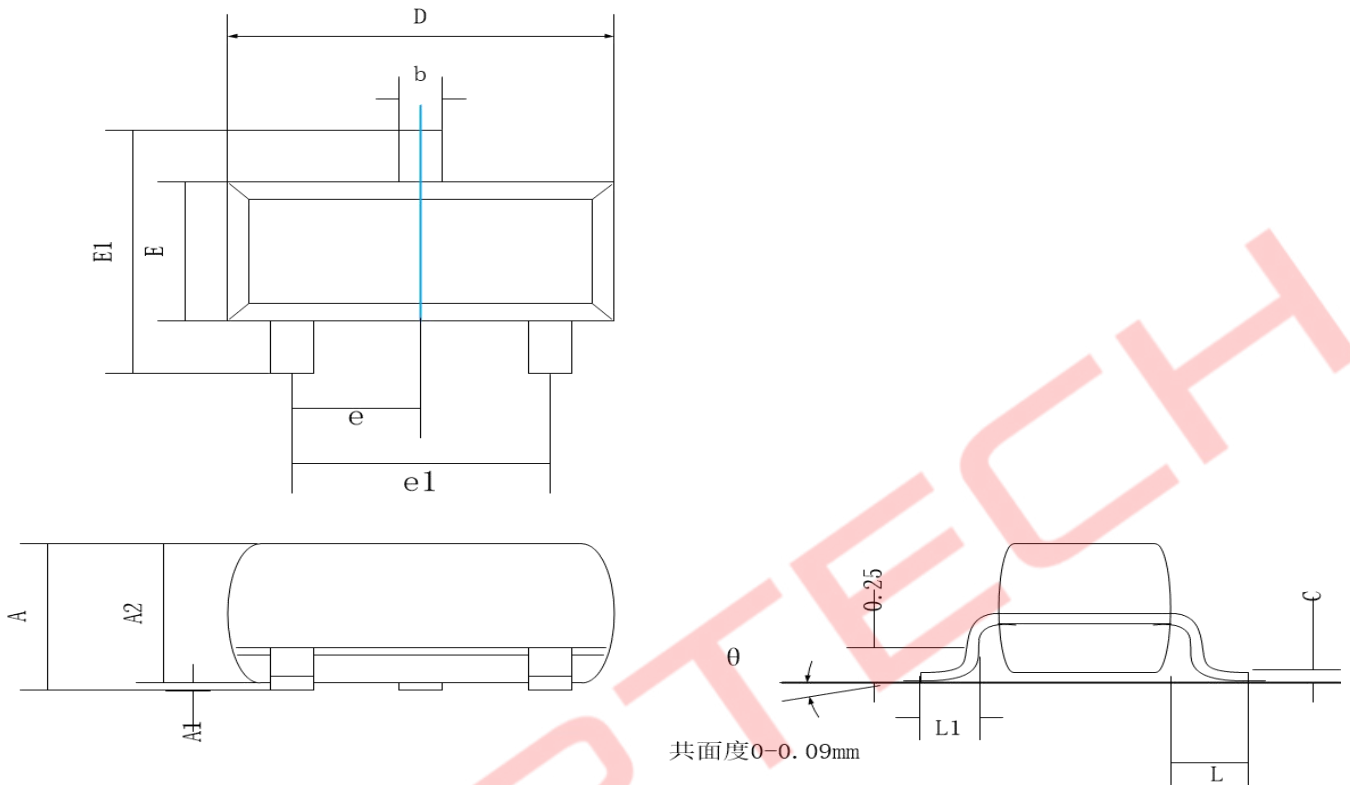


Fig.11 Gate Charge Waveform

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Package Outline

SOT-23

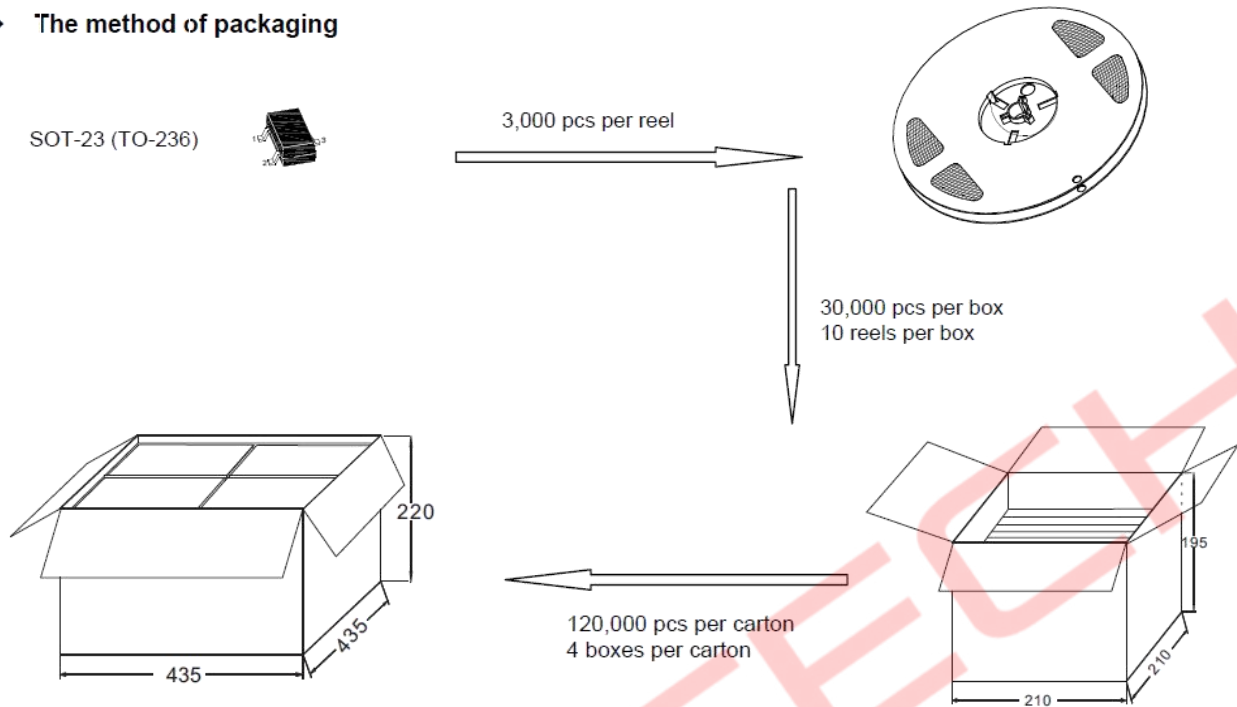


Symbol	Dimensions In Millimeters	
	Min.	Max.
A	0.90	1.15
A1	0.00	0.10
A2	0.90	1.05
b	0.30	0.50
c	0.08	0.15
D	2.80	3.00
E	1.20	1.40
E1	2.25	2.55
e	0.95 REF.	
e1	1.80	2.00
L	0.55 REF.	
L1	0.30	0.50
θ	0°	8°

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Package Specifications

◆ The method of packaging



◆ Embossed tape and reel data

