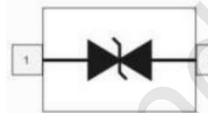




1-Line Bidirectional肖特基二极管保护二极管

SOD923

schematic & pin configuration

simplified outline	Graphic symbol
 Marking: B	

General description

The ESDgD3.3c is designed to protect voltage sensitive components from ESD and transient voltage events. Excellent clamping capability, low leakage, and fast response time, make these parts ideal for ESD protection on designs where board space is at a premium

Features and benefits

- Low capacitance 15 PF(TYP)
- Reverse stand-off voltage: 3.3V Max
- Low leakage current: nA Level
- Low clamping voltage
- Response time is typically < 1 ns
- IEC61000-4-2 Level 4 ESD Protection

Application information

- ce Iphones
- Audio equipment
- portable devices
- Digital cameras
- power supplies

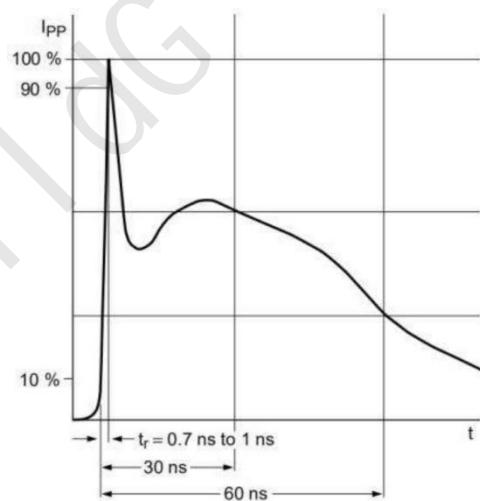
Maximum Ratings (Top = 25 °c, unless otherwise specified)

parameter	symbol	value	unit
peak pulse power (tp = 8/20 µS)	PPPM	110	W
peak pulse current (tp = 8/20 µS)	I _{PPM}	11	A
ESD Voltage IEC 61000-4-2 (air discharge)	V _{ESD}	30	kV
ESD Voltage IEC 61000-4-2 (contact discharge)	V _{ESD}	30	kV
Maximum lead temperature for soldering during 10s	T _L	260	°C
storage Temperature Range	T _{stg}	-55 to +150	°C
operating Temperature Range	Top	-40 to +125	°C
Maximum junction temperature	T _j	150	°C

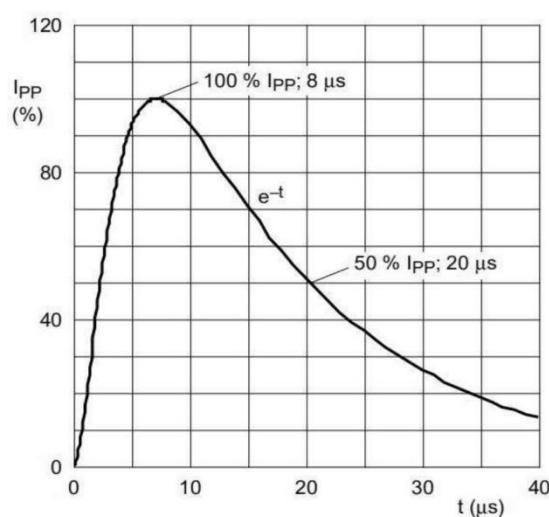
Electrical characteristics (Top = 25 °c, unless otherwise specified)

parameter	symbol	Min	TYP	Max	unit	condition
Reverse working voltage	V _{RWM}	--	--	3.3	V	
Breakdown voltage	V _{BR}	3.5	--	5.0	V	IT= 1mA
Leakage current Leak	I _R	--	--	100	nA	V _{RWM} =3.3V
clamping voltage	V _C	--	--	10.0	V	I _{PP} = 11A, TP=8/20µS
Junction capacitance	C _J	--	15	20	PF	V _R =0V, f= 1MHz

Typical Electrical and Thermal Characteristics (Curves)



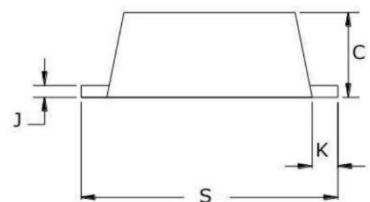
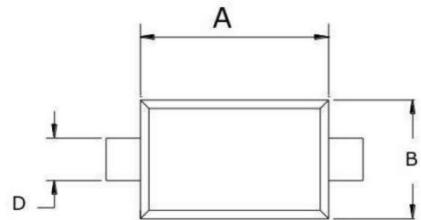
IEC61000-4-2 waveform



IEC 61000-4-5waveform(8/20µS Pulse)

package outline Dimensions

SOD923



SYMBOL	MILLIMETERS	
	MIN	MAX
A	0.74	0.86
B	0.54	0.66
C	0.35	0.45
D	0.14	0.26
K	0.04	0.16
S	0.95	1.10

soldering Footprint (mm)

