

General Description

The AOTE32136C is the highest performance trench

N-ch MOSFETs with extreme high cell density,

which provide excellent RDSON and gate charge

for most of the small power switching and

load switch applications. The meet the RoHS and

Product requirement with full function reliability approved.



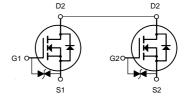
TSSOP-8

General Features

 $V_{DS} = 20V I_{D} = 7A$

 $R_{DS(ON)}$ < 14m Ω @ V_{GS} =4. 5V

 $R_{DS(ON)}$ < 17m Ω @ V_{GS} =2. 5V



Application

Battery protection

Load switch

Uninterruptible power supply

Dual N-Channel MOSFET

Package Marking and Ordering Information

Product ID	Pack	Brand	Qty(PCS)
AOTE32136C	TSSOP-8	HXY MOSFET	5000

Absolute Maximum Ratings (TA=25 ℃ unless otherwise noted)

Symbol	Parameter	Limit	Unit	
V _{DS}	Drain-Source Voltage	20	V	
Vgs	Gate-Source Voltage	±10	V	
I _D	Drain Current-Continuous	7	А	
Ірм	Drain Current-Pulsed (Note 1)	23	А	
P _D	Maximum Power Dissipation	1.25	W	
T _J ,T _{STG}	Operating Junction and Storage Temperature Range	-55 To 150	$^{\circ}$	
RөJA	Thermal Resistance,Junction-to-Ambient (Note 2)	111	°C/W	



Electrical Characteristics (T_A=25 ℃ unless otherwise noted)

Parameter	Symbol	Condition	Min	Тур	Max	Unit
Drain-Source Breakdown Voltage	BV _{DSS}	V _{GS} =0V I _D =250µA	20	-	-	V
Zero Gate Voltage Drain Current	Ipss	V _{DS} =16V,V _{GS} =0V	-	-	1	μΑ
Gate-Body Leakage Current	Igss	V _{GS} =±8V,V _{DS} =0V	V _{GS} =±8V,V _{DS} =0V -		±100	nA
Gate Threshold Voltage	VGS(th)	V _{DS} =V _{GS} ,I _D =250μA	0.5	0.7	1.2	V
		V _{GS} =4.5V, I _D =4.5A	-	12	14	mΩ
Drain-Source On-State Resistance	RDS(ON)	V _{GS} =2.5V, I _D =3.5A	-	15	17	mΩ
Forward Transconductance	grs	V _{DS} =5V,I _D =3.5A	-	20	-	S
Input Capacitance	C _{lss}		-	955	-	PF
Output Capacitance	Coss	V _{DS} =8V,V _{GS} =0V,	-	200	-	PF
Reverse Transfer Capacitance	Crss	F=1.0MHz	-	150	-	PF
Turn-on Delay Time	td(on)		-	8		nS
Turn-on Rise Time	tr	V _{DD} =10V,I _D =3.5A	-	17	-	nS
Turn-Off Delay Time	td(off)	$V_{GS}=10V, I_{GEN}=3.5A$ $V_{GS}=4.5V, R_{GEN}=6\Omega$	-	27	-	nS
Turn-Off Fall Time	t _f		-	8.8	-	nS
Total Gate Charge	Qg		-	11.3	-	nC
Gate-Source Charge	Qgs	V _{DS} =10V,I _D =7A,	-	1.89	-	nC
Gate-Drain Charge	Q _{gd}	V _{GS} =4.5V	-	3.56	-	nC
Diode Forward Voltage (Note 3)	VsD	V _{GS} =0V,I _S =1.7A	-	0.75	1.2	V
Diode Forward Current (Note 2)	Is		-	-	7	А

Notes:

- 1. Repetitive Rating: Pulse width limited by maximum junction temperature.
- 2. Surface Mounted on FR4 Board, t ≤ 10 sec.
- **3.** Pulse Test: Pulse Width ≤ 300μ s, Duty Cycle ≤ 2%.
- 4. Guaranteed by design, not subject to production



Typical Characteristics

Figure1: Output Characteristics

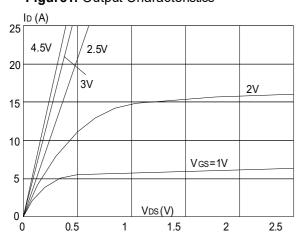


Figure 2: Typical Transfer Characteristics

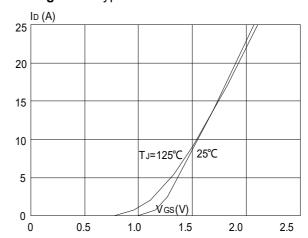


Figure 3:On-resistance vs. Drain Current

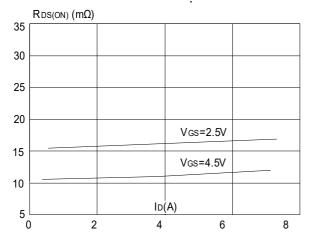


Figure 4: Body Diode Characteristics

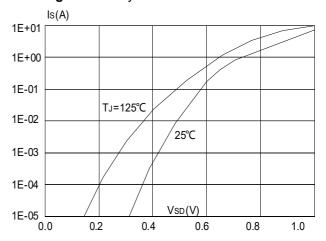


Figure 5: Gate Charge Characteristics

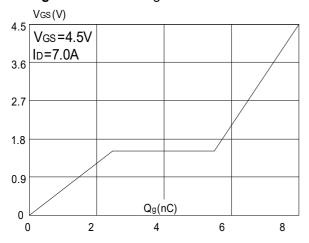


Figure 6: Capacitance Characteristics

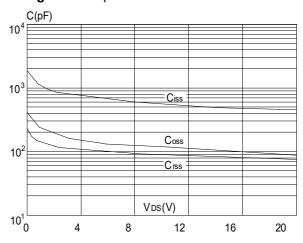




Figure 7: Normalized Breakdown Voltage vs. Junction Temperature

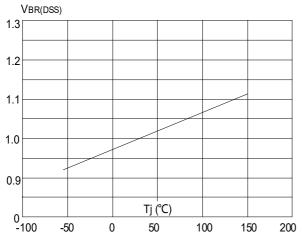


Figure 8: Normalized on Resistance vs. Junction Temperature

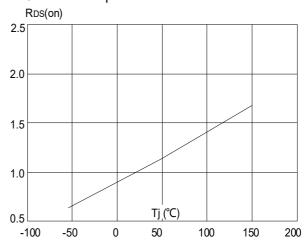


Figure 9: Maximum Safe Operating Area

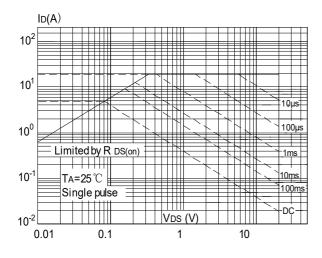


Figure 10: Maximum Continuous Drain Current vs. Ambient Temperature

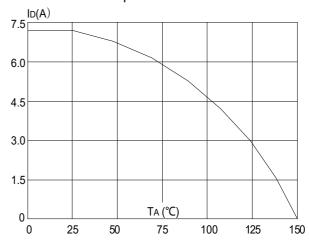
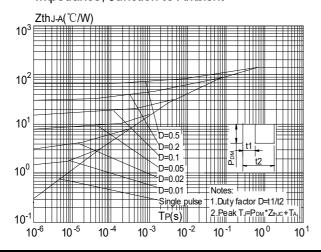
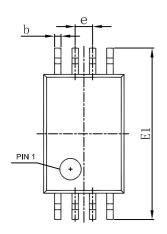


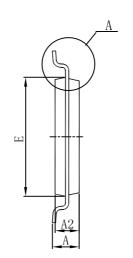
Figure.11: Maximum Effective Transient Thermal Impedance, Junction-to-Ambient

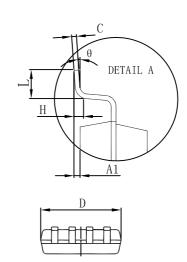




TSSOP-8 Package Outline Dimensions







Symbol	Dimensions In Millimeters		Dimensions In Inches		
	Min	Max	Min	Max	
D	2.900	3. 100	0. 114	0.122	
Е	4.300	4. 500	0. 169	0. 177	
b	0.190	0.300	0.007	0.012	
c	0.090	0.200	0.004	0.008	
E1	6.250	6. 550	0. 246	0.258	
A		1. 200		0.047	
A2	0.800	1. 000	0.031	0.039	
A1	0.050	0. 150	0.002	0.006	
e	0.65 (BSC)		0. 026 (BSC)		
L	0.500	0.700	0. 020	0.028	
Н	0.25(TYP)		0.01(TYP)		
θ	1°	7°	1 °	7°	



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