

Description

The SI7414DN-T1-E3 uses advanced trench technology

to provide excellent R_{DS(ON)}, low gate charge and

operation with gate voltages as low as 4.5V. This

device is suitable for use as a

Battery protection or in other Switching application.

General Features

V_{DS} = 60V I_D =30 A

 $R_{DS(ON)} < 30 m\Omega$ @ $V_{GS}=10V$

Application

Battery protection

Load switch

Uninterruptible power supply

Package Marking and Ordering Information

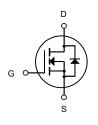
Product ID	Pack	Brand	Qty(PCS)
SI7414DN-T1-E3	DFN3X3-8L	HXY MOSFET	5000

Absolute Maximum Ratings (T_c=25 ℃ unless otherwise noted)

Symbol	Parameter	Rating	Units
VDS	Drain-Source Voltage	60	V
VGS	Gate-Source Voltage	±20	V
I _D @T _C =25°C	Continuous Drain Current, V _{GS} @ 10V ¹	30	А
I _D @T _C =100°C	Continuous Drain Current, V _{GS} @ 10V ¹	16	А
IDM	Pulsed Drain Current ²	90	А
EAS	Single Pulse Avalanche Energy ³	42	mJ
P _D @T _C =25°C	Total Power Dissipation ⁴	33	W
TSTG	Storage Temperature Range	-55 to 150	°C
TJ	Operating Junction Temperature Range	-55 to 150	°C
R₀JA	Thermal Resistance Junction-ambient ¹	62	°C/W
R₀JC	Thermal Resistance Junction-Case ¹	3.79	°C/W



DFN3X3-8L



N-Channel MOSFET



Electrical Characteristics (T_J=25 °C, unless otherwise noted)

Symbol	Parameter	Conditions	Min	Тур	Max	Units
BV _{DSS}	Drain-Sourtce Breakdown Voltage	V _{GS} =0V,I _D =250 μ A	60			V
I _{DSS}	Drain-Source Leakage Current	V _{GS} =0V, V _{DS} =60V			1	μA
I _{GSS}	Gate-Source Leakage Current	$V_{GS}=\pm 20V$, $V_{DS}=0A$			±100	nA
V _{GS(th)}	GATE-Source Threshold Voltage	V _{GS} =V _{DS} , I _D =250 μ A	1.2	1.8	2.5	V
		V _{GS} =10V,I _D =20A		24	30	m Ω
R _{DS(ON)}	Drain-Source On Resistance ³	V _{GS} =4.5V,I _D =20A		31	40	
C _{iss}	Input Capacitance			1060		
C _{oss}	Output Capacitance	V _{DS} =30V, V _{GS} =0V, f=1MHz		64		pF
C _{rss}	Reverse Transfer Capacitance			54		
t _{d(on)}	Turn-On Delay Time	V_{DD} =30V , V_{GS} =10V , I_{D} =20A, R_{G} =3 Ω		8.4		ns
t _r	Rise Time			8.5		ns
t _{d(off)}	Turn-Off Delay Time			36		ns
t _f	Fall Time			5		ns
$\mathbf{Q}_{\mathbf{g}}$	Total Gate Charge	V_{DS} =30V , V_{GS} =10V , I_D =20A		26		nC
\mathbf{Q}_{gs}	Gate-Source Charge			5.7		nC
$Q_{\rm gd}$	Gate-Drain "Miller" Charge			5.2		nC
Is	Continuous Source Current	VG=VD=0V			20	А
I _{SM}	Pulsed Source Current	VG=VD=0V			90	А
V _{SD}	Forward on voltage	I _S =20A,V _{GS} =0V			1.2	V
Trr	Body Diode Reverse Recovery Time	- IF=20A, dI/dt=100A/μs		18		nS
Qrr	Body Diode Reverse Recovery Charge			13		nC

Notes:

- 1) L=0.5mH, VDD=30V, Start TJ=25°C.
- 2) Limited by maximum junction temperature.
- 3) Repetitive Rating: Pulse width limited by maximum junction temperature



Typical Characteristics

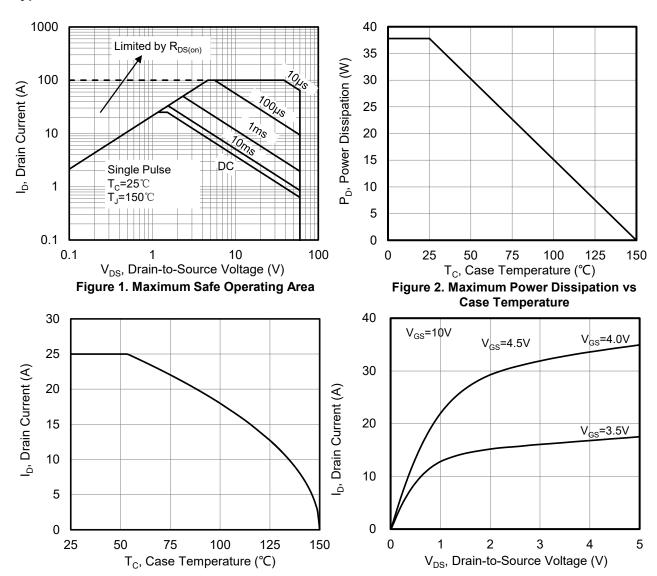


Figure 3. Maximum Continuous Drain Current vs **Case Temperature**

Figure 4. Typical output Characteristics

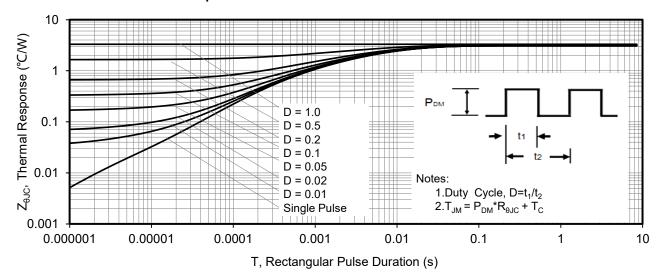
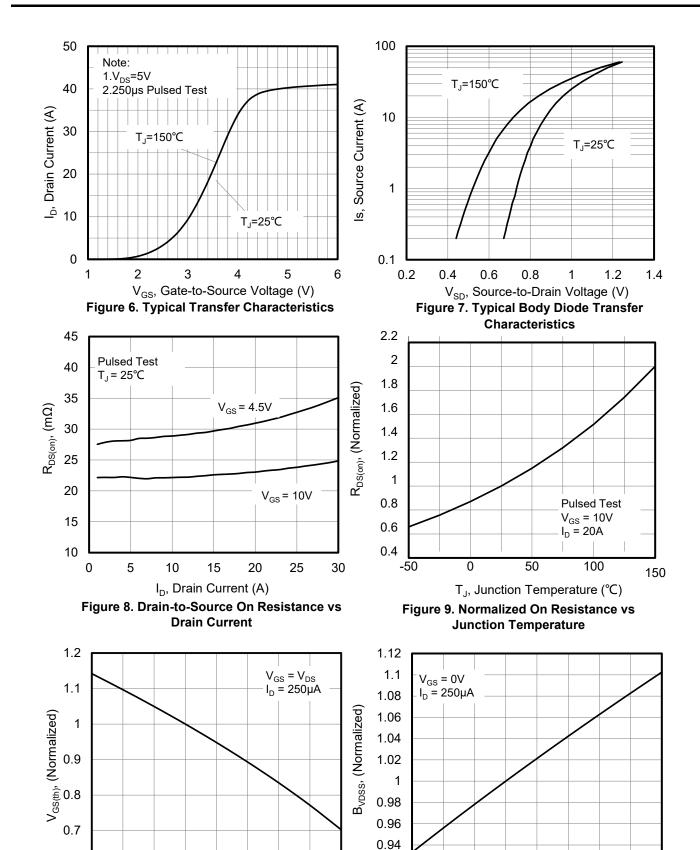


Figure 5. Maximum Effective Thermal Impedance, Junction to Case



0.92

-50

50

Figure 11. Normalized Breakdown Voltage vs

Junction Temperature

T_J, Junction Temperature (°C)

100

150

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50

T_J, Junction Temperature (°C)

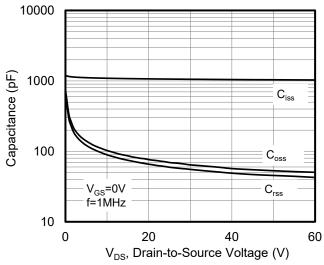
Figure 10. Normalized Threshold Voltage vs Junction Temperature

100

0.6

-50

150





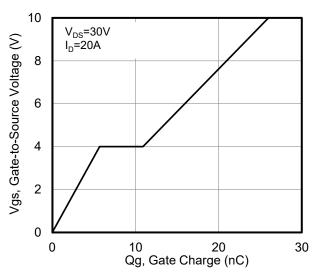
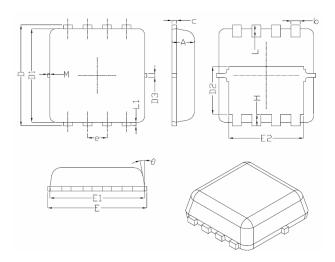


Figure 13. Typical Gate Charge vs Gate to Source Voltage

DFN3X3-8L Package Information



Symbol	Dimensions In Millimeters			
	Min.	Nom.	Max.	
A	0.70	0.75	0.80	
b	0.25	0.30	0.35	
С	0.10	0.15	0.25	
D	3.25	3.35	3.45	
D1	3.00	3.10	3.20	
D2	1.48	1.58	1.68	
D3	-	0.13	-	
E	3.20	3.30	3.40	
E1	3.00	3.15	3.20	
E2	2.39	2.49	2.59	
е	0.65	5BSC		
Н	0.30	0.39	0.50	
L	0.30	0.40	0.50	
L1	-	0.13	-	
M	*	*	0.15	
θ		10 [°]	12 [°]	



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