

Description

The DMN3009SK3 uses advanced trench technology

to provide excellent $R_{\text{DS}(\text{ON})},$ low gate charge and

operation with gate voltages as low as 4.5V. This

device is suitable for use as a

Battery protection or in other Switching application.

General Features

V_{DS} = 30V I_D =150A

 $R_{DS(ON)} < 2.9 \, m\Omega @ V_{GS} = 10 V$

Application

Battery protection

Load switch

Uninterruptible power supply

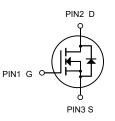
Package Marking and Ordering Information

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Product ID	Pack	Brand	Qty(PCS)
DMN3009SK3	TO-252-2L	HXY MOSFET	2500

Absolute Maximum Ratings (Tc=25°Cunless otherwise noted)

Symbol	Parameter	Rating	Units	
VDS	Drain-Source Voltage 30		V	
Vgs	Gate-Source Voltage	±20	V	
I⊳@Tc=25°C	Continuous Drain Current, V _{GS} @ 10V ¹	Continuous Drain Current, V _{GS} @ 10V ¹ 150		
I _D @T _C =100°C	Continuous Drain Current, V _{GS} @ 10V ¹	80	A	
Ідм	Pulsed Drain Current ²	450	A	
EAS	Single Pulse Avalanche Energy ³	580	mJ	
las	Avalanche Current	60	A	
P₀@Tc=25°C	Total Power Dissipation ⁴	87	W	
Тѕтс	Storage Temperature Range	-55 to 150	°C	
TJ	T _J Operating Junction Temperature Range		°C	
RθJA	RθJA Thermal Resistance Junction-Ambient 1		°C/W	
RθJC	R0JC Thermal Resistance Junction-Case1		°C/W	





N-Channel MOSFET



Electrical characteristic	(T ₁ = 25°C unless	otherwise specified)
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Symbol	Parameter	Test conditions	Min.	Тур.	Max.	Unit
BV _{DSS}	Drain to source breakdown voltage	V _{GS} =0V, I _D =250uA	30			V
ΔΒV _{DSS} / ΔΤ _J	Breakdown voltage temperature coefficient	I _D =250uA, referenced to 25°C		0.02		V/ºC
	Drain to source leakage current	V _{DS} =30V, V _{GS} =0V			1	uA
I _{DSS}		V _{DS} =24V, T _J =125°C			50	uA
	Gate to source leakage current, forward	V _{GS} =20V, V _{DS} =0V			100	nA
I _{GSS}	Gate to source leakage current, reverse	V _{GS} =-20V, V _{DS} =0V			-100	nA
V _{GS(TH)}	Gate threshold voltage	V _{DS} =V _{GS} , I _D =250uA	1.2		2.4	V
	Drain to source on state resistance	V _{GS} =4.5V, I _D =30A,T _J =25°C		2.2	4.8	mΩ
R _{DS(ON)}		V _{GS} =10V, I _D =30A,T _J =25°C		1.5	2.9	mΩ
		V _{GS} =10V, I _D =30A,T _J =125°C		2.5		mΩ
G _{fs}	Forward transconductance	V _{DS} =5V, I _D =30A		73		S
C _{iss}	Input capacitance			6272		pF
C _{oss}	Output capacitance	V _{GS} =0V, V _{DS} =15V, f=1MHz		1022		
C _{rss}	Reverse transfer capacitance			718		
t _{d(on)}	Turn on delay time			20		- ns
t _r	Rising time	V _{DS} =15V, I _D =30A, R _G =4.7Ω, V _{GS} =10V (note 4.5)		58		
t _{d(off)}	Turn off delay time			158		
t _f	Fall time			77		
Q _g	Total gate charge	V _{DS} =24V, V _{GS} =10V, I _D =30A ,		143		
Q _{gs}	Gate-source charge	I _G =5mA		17		nC
Q _{gd}	Gate-drain charge	(note 4,5)		43		
R_{g}	Gate resistance	V _{DS} =0V, Scan F mode		4.2		Ω
I _s	Continuous source current	Integral reverse p-n Junction			110	A
I _{SM}	Pulsed source current	diode in the MOSFET			440	A
V _{SD}	Diode forward voltage drop.	I _s =45A, V _{GS} =0V			1.4	V
t _{rr}	Reverse recovery time	I _S =30A, V _{GS} =0V,		26		ns
Q _{rr}	Reverse recovery charge	dl _F /dt=100A/us		10		nC

X. Notes

Repeatitive rating : pulse width limited by junction temperature. L =0.5mH, I_{AS} =48A, V_{DD} =30V, R_{G} =25 Ω , Starting T_{J} = 25°C I_{SD} ≤30A, di/dt = 100A/us, V_{DD} ≤ BV_{DSS}, Staring T_{J} =25°C Pulse Test : Pulse Width ≤ 300us, duty cycle ≤ 2%. 1.

2.

3.

4.



Typical Electrical and Thermal Characteristics

Fig. 1. On-state characteristics

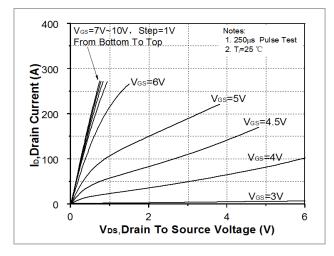


Fig. 3. On-resistance variation vs. drain current and gate voltage

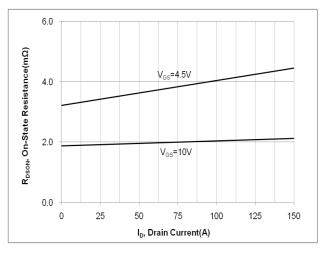


Fig 5. Breakdown voltage variation vs. junction temperature

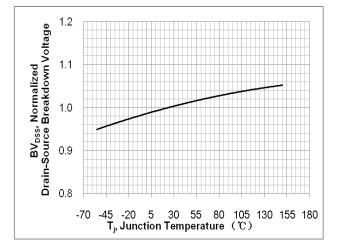


Fig. 2. Transfer Characteristics

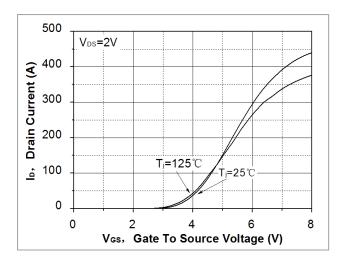


Fig. 4. On-state current vs. diode forward voltage

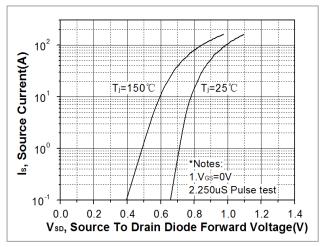


Fig. 6. On-resistance variation vs. junction temperature

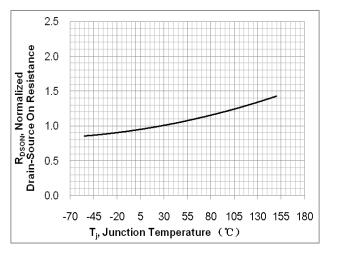




Fig. 7. Gate charge characteristics

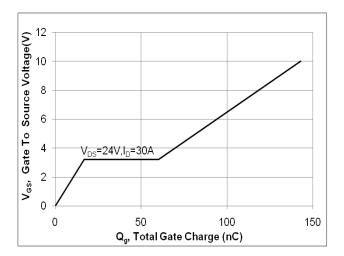


Fig. 9. Maximum safe operating area

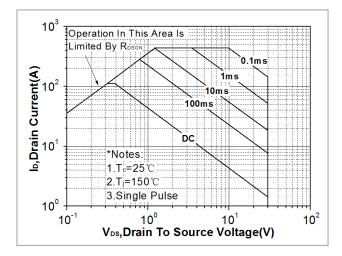


Fig. 11. Transient thermal response curve

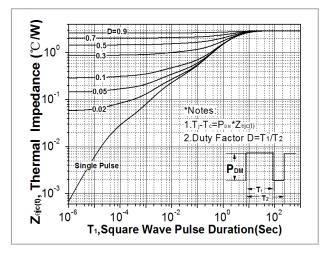


Fig. 8. Capacitance Characteristics

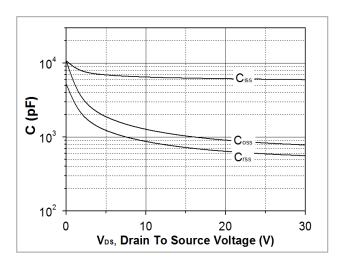
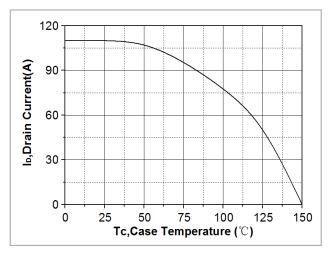
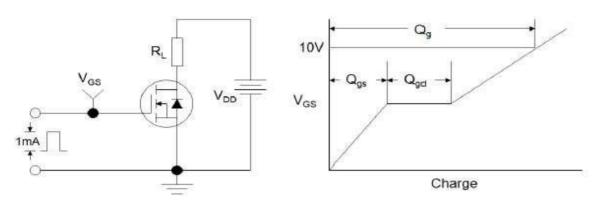


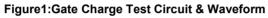
Fig. 10. Maximum drain current vs. case temperature





Test Circuit





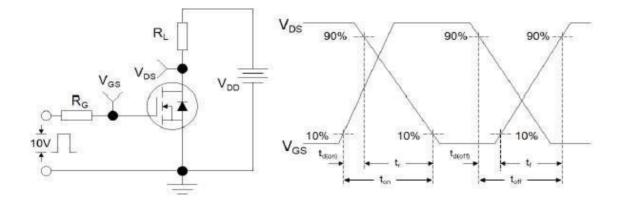


Figure 2: Resistive Switching Test Circuit & Waveforms

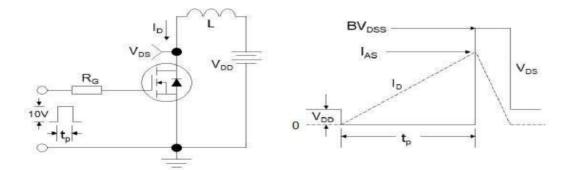
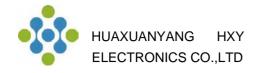
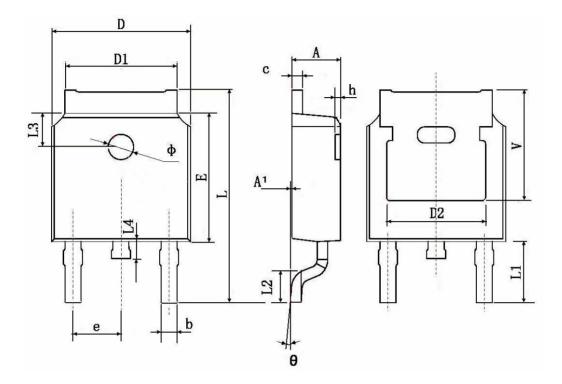


Figure 3:Unclamped Inductive Switching Test Circuit & Waveforms



TO-252-2L Package Information



Querra ha d	Dimensions In Millimeters		Dimensions In Inches		
Symbol	Min.	Max.	Min.	Max.	
А	2.200	2.400	0.087	0.094	
A1	0.000	0.127	0.000	0.005	
b	0.660	0.860	0.026	0.034	
С	0.460	0.580	0.018	0.023	
D	6.500	6.700	0.256	0.264	
D1	5.100	5.460	0.201	0.215	
D2	0.483 TYP.		0.190 TYP.		
E	6.000	6.200	0.236	0.244	
е	2.186	2.386	0.086	0.094	
L	9.800	10.400	0.386	0.409	
L1	2.900	D TYP.	0.114 TYP.		
L2	1.400	1.700	0.055	0.067	
L3	1.600	1.600 TYP.		3 TYP.	
L4	0.600	1.000	0.024	0.039	
Φ	1.100	1.300	0.043	0.051	
θ	0°	8°	0°	8°	
h	0.000	0.300	0.000	0.012	
V	5.350 TYP. 0.211 TYP.			1 TYP.	



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