General Description

The IRF7351PbF use advanced SGT MOSFET technology to provide low RDS(ON), low gate charge, fast switching and excellent avalanche characteristics.

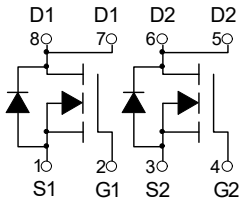
This device is specially designed to get better ruggedness and suitable to use in

SOP-8

General Features

V_{DS} =60V I_D =15A

$R_{DS(ON)}$ < 15mΩ @ V_{GS}=10V



Dual N-Channel MOSFET

Applications

Consumer electronic power supply Motor control

Synchronous-rectification Isolated DC

Synchronous-rectification applications

Package Marking and Ordering Information

Product ID	Pack	Brand	Qty(PCS)
IRF7351PbF	SOP-8	HXY MOSFET	3000

Absolute Maximum Ratings at T_j=25℃ unless otherwise noted

Parameter	Symbol	Value	Unit
Drain source voltage	VDS	60	V
Gate source voltage	VGS	±20	V
Continuous drain current[1)]	ID	15	A
Pulsed drain current[2)]	ID, pulse	180	A
Power dissipation[3)]	P_D	60	W
Single pulsed avalanche energy[5)]	EAS	36	mJ
Operation and storage temperature	Tstg，Tj	-55 to 150	℃
Thermal resistance, junction-case	RθJC	2.5	°C/W

Electrical Characteristics (T_J=25℃ unless otherwise specified)

Symbol	Parameter	Test Condition	Min.	Typ.	Max.	Units
$V_{(BR)DSS}$	Drain-Source Breakdown Voltage	V_{GS}=0V, I_D=250μA	60	-	-	V
I_{DSS}	Zero Gate Voltage Drain Current	V_{DS}=60V, V_{GS}=0V,	-	-	1.0	μA
I_{GSS}	Gate to Body Leakage Current	V_{DS}=0V, V_{GS}= ±20V	-	-	±100	nA
$V_{GS(th)}$	Gate Threshold Voltage	V_{DS}=V_{GS}, I_D=250μA	1.0	1.6	2.5	V
$R_{DS(on)}$	Static Drain-Source on-Resistance note3	V_{GS}=10V, I_D=20A	-	12	15	mΩ
		V_{GS}=4.5V, I_D=10A	-	15	20	
C_{iss}	Input Capacitance	V_{DS}=25V, V_{GS}=0V, f=1.0MHz	-	930	-	pF
C_{oss}	Output Capacitance		-	230	-	pF
C_{rss}	Reverse Transfer Capacitance		-	8	-	pF
Q_g	Total Gate Charge	V_{DS}=30V, I_D=20A, V_{GS}=10V	-	22	-	nC
Q_{gs}	Gate-Source Charge		-	4.5	-	nC
Q_{gd}	Gate-Drain(“Miller”) Charge		-	3.5	-	nC
$t_{d(on)}$	Turn-on Delay Time	V_{DD}=30V, I_D=20A, R_G=1.6Ω, V_{GS}=10V	-	4.5	-	ns
t_r	Turn-on Rise Time		-	2.7	-	ns
$t_{d(off)}$	Turn-off Delay Time		-	13.8	-	ns
t_f	Turn-off Fall Time		-	2.7	-	ns
I_S	Maximum Continuous Drain to Source Diode Forward Current		-	-	15	A
I_{SM}	Maximum Pulsed Drain to Source Diode Forward Current		-	-	180	A
V_{SD}	Drain to Source Diode Forward Voltage	V_{GS}=0V, I_S=30A	-	-	1.2	V
t_{rr}	Body Diode Reverse Recovery Time	T_J=25℃, I_F=20A,dI/dt=100A/μs	-	18	-	ns
Q_{rr}	Body Diode Reverse Recovery Charge		-	12	-	nC

Notes:1. Repetitive Rating: Pulse Width Limited by Maximum Junction Temperature

2. EAS condition: T_J=25℃, V_{DD}=30V, V_G=10V, R_G=25Ω, L=0.5mH, I_{AS}=12A

3. Pulse Test: Pulse Width≤300μs, Duty Cycle≤0.5%

Typical Performance Characteristics

Figure1: Output Characteristics

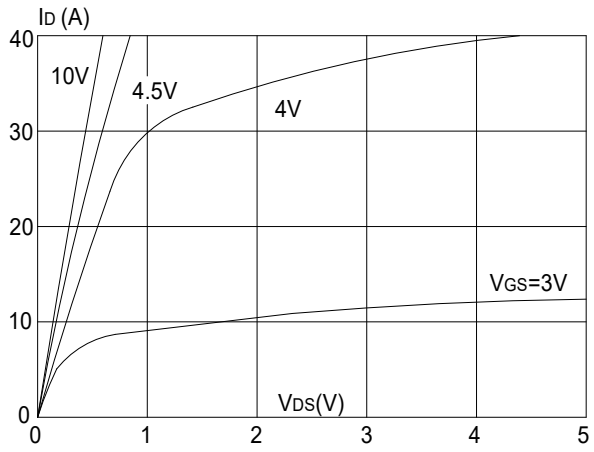


Figure 2: Typical Transfer Characteristics

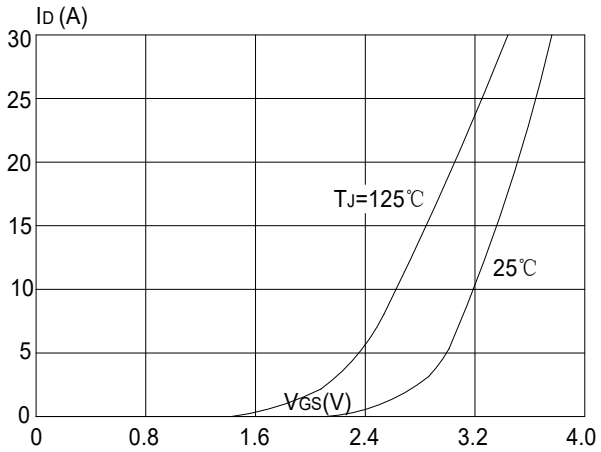


Figure 3: On-resistance vs. Drain Current

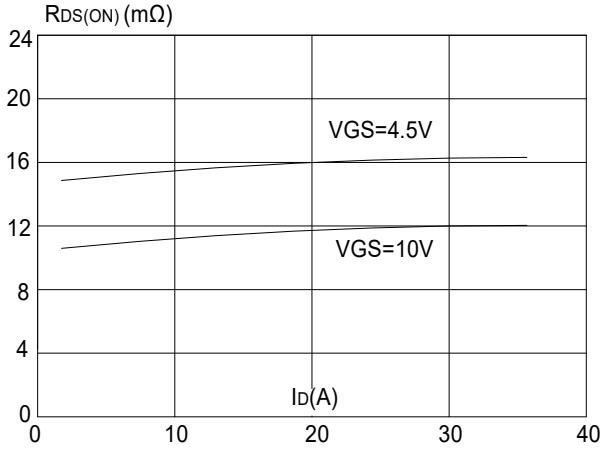


Figure 4: Body Diode Characteristics

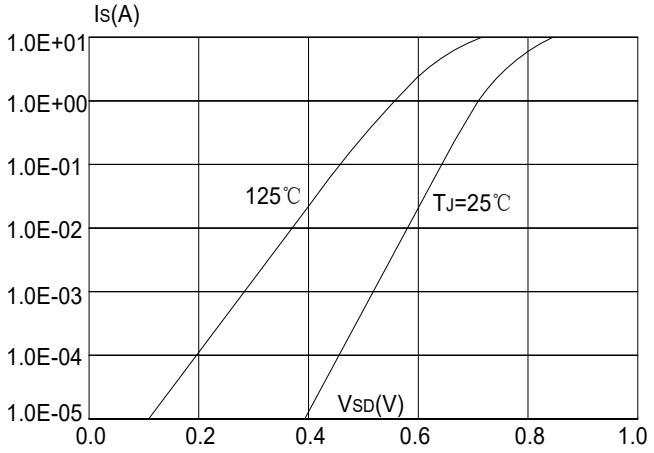


Figure 5: Gate Charge Characteristics

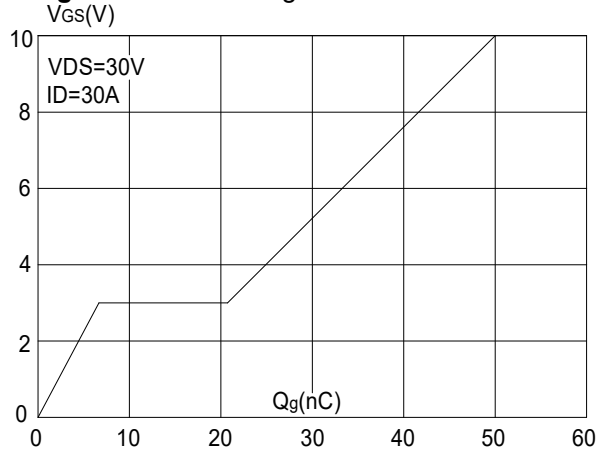


Figure 6: Capacitance Characteristics

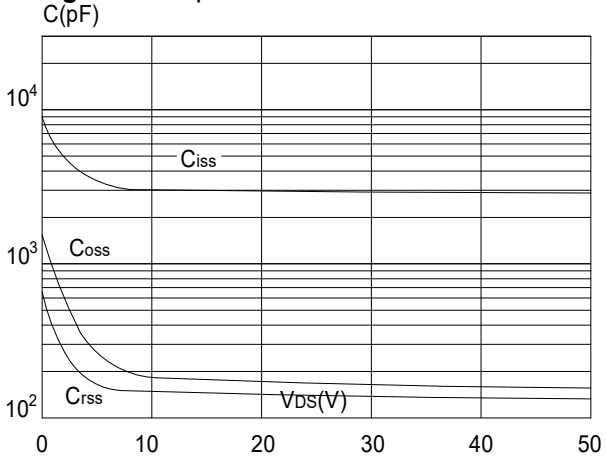


Figure 7: Normalized Breakdown Voltage vs. Junction Temperature

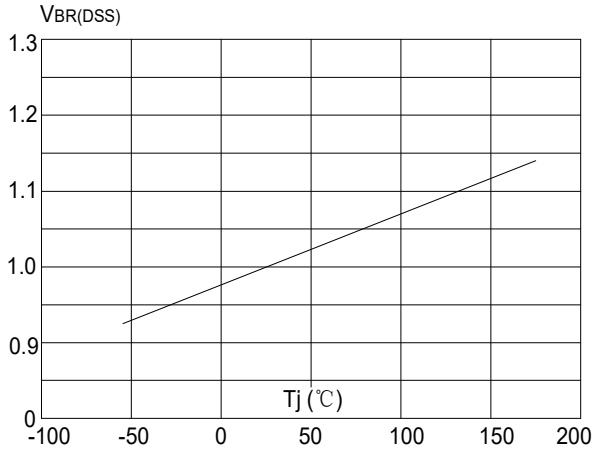


Figure 8: Normalized on Resistance vs. Junction Temperature

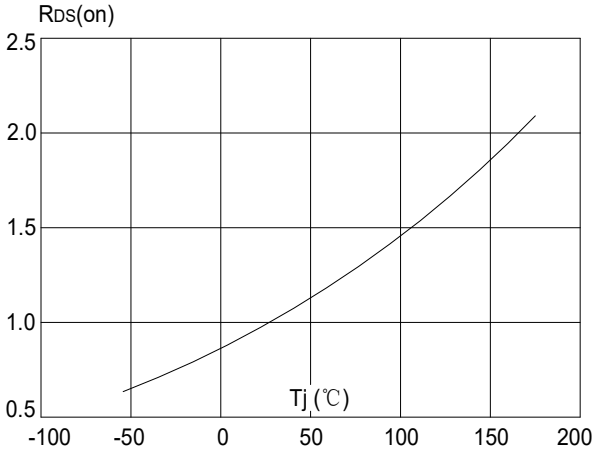


Figure 9: Maximum Safe Operating Area

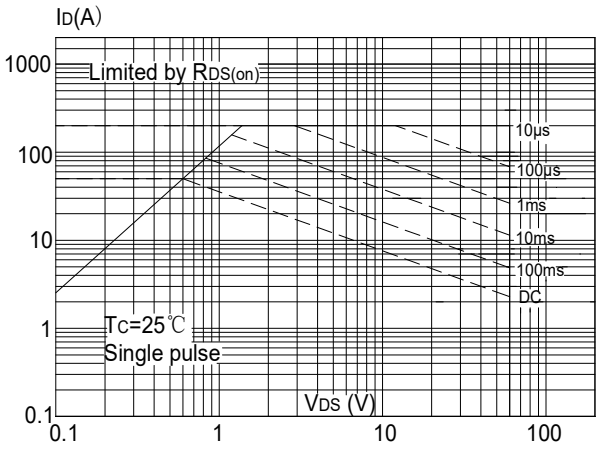


Figure 10: Maximum Continuous Drain Current vs. Case Temperature

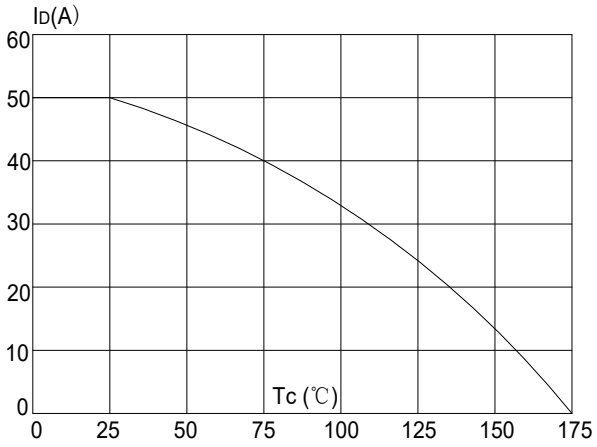
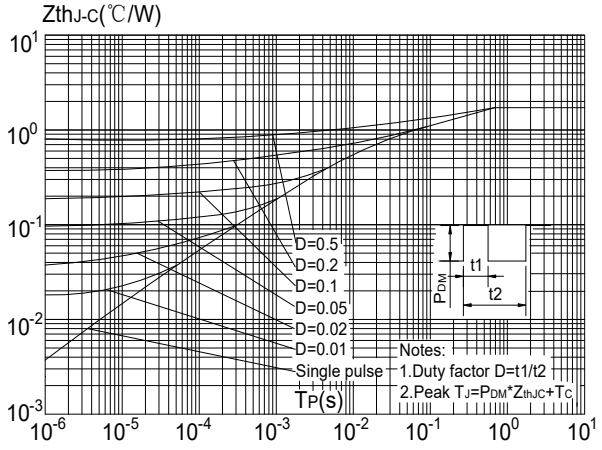
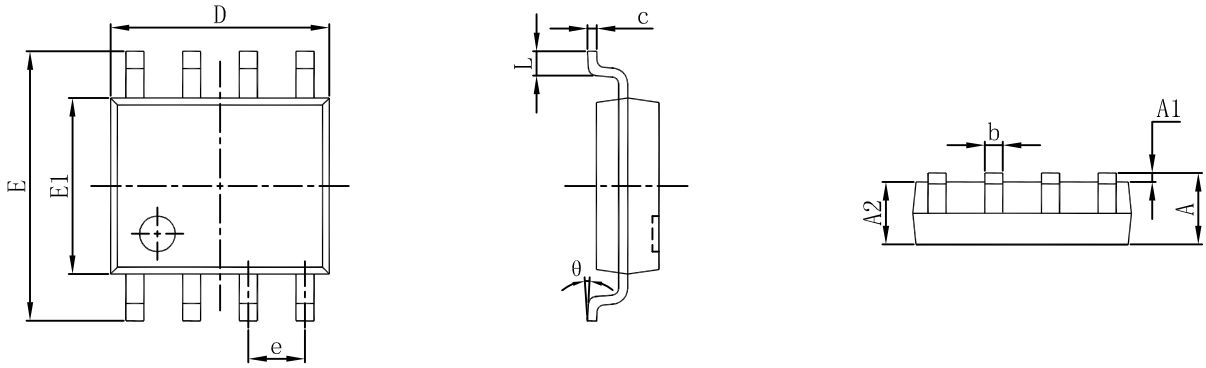


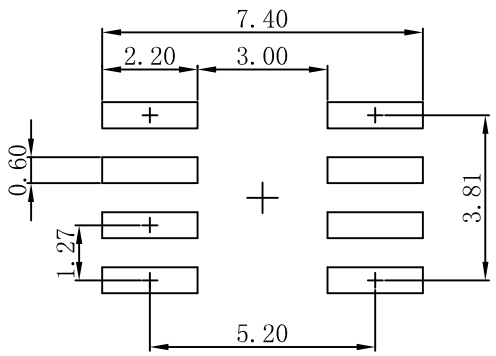
Figure.11: Maximum Effective Transient Thermal Impedance, Junction-to-Case



SOP-8 Package Outline Dimensions



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min	**Max**	**Min**	**Max**
A	1.350	1.750	0.053	0.069
A1	0.100	0.250	0.004	0.010
A2	1.350	1.550	0.053	0.061
b	0.330	0.510	0.013	0.020
c	0.170	0.250	0.007	0.010
D	4.800	5.000	0.189	0.197
e	1.270 (BSC)		0.050 (BSC)	
E	5.800	6.200	0.228	0.244
E1	3.800	4.000	0.150	0.157
L	0.400	1.270	0.016	0.050
θ	0°	8°	0°	8°



Note:
1.Controlling dimension:in millimeters.
2.General tolerance:± 0.05mm.
3.The pad layout is for reference purposes only.

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