## **Description**

The FDS6910 uses advanced trench technology

to provide excellent RDS(ON), low gate charge and

operation with gate voltages as low as 2.5V. This

device is suitable for use as a

Battery protection or in other Switching application.



SOP-8

**General Features** 

 $V_{DS} = 30V I_{D} = 11.5A$ 

 $R_{DS(ON)}$  < 30m $\Omega$  @  $V_{GS}$ =10 V

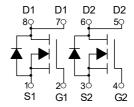
 $R_{DS(ON)} < 42m\Omega$  @  $V_{GS}=4.5V$ 



Battery protection

Load switch

Uninterruptible power supply



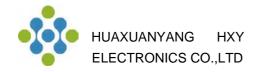
**Dual N-Channel MOSFET** 

**Package Marking and Ordering Information** 

Product ID	Pack	Brand	Qty(PCS)
FDS6910	SOP-8	HXY MOSFET	3000

Absolute Maximum Ratings@T<sub>i</sub>=25°C(unless otherwise specified)

Symbol	Parameter	Rating	Units
V <sub>DS</sub>	Drain-Source Voltage	30	V
V <sub>GS</sub>	Gate-Source Voltage	<u>+</u> 20	V
I <sub>D</sub> @T <sub>A</sub> =25°C	Drain Current, V <sub>GS</sub> @ 4.5V <sup>3</sup>	11.5	А
I <sub>D</sub> @T <sub>A</sub> =70°C	Drain Current, V <sub>GS</sub> @ 4.5V <sup>3</sup>	7.8	А
Ірм	Pulsed Drain Current <sup>1</sup>	42	А
PD@TA=25°C	Total Power Dissipation	3.2	W
Тѕтс	Storage Temperature Range	-55 to 150	°C
TJ	Operating Junction Temperature Range	-55 to 150	°C
Rthj-a	Maximum Thermal Resistance, Junction- ambient <sup>3</sup>	62.5	°C/W



# Electrical Characteristics (T<sub>J</sub>=25°C unless otherwise noted)

Symbol	Parameter	Test Condition	Min.	Тур.	Max.	Units
V <sub>(BR)DSS</sub>	Drain-Source Breakdown Voltage	V <sub>GS</sub> =0V, I <sub>D</sub> =250μA	30	-	-	V
I <sub>DSS</sub>	Zero Gate Voltage Drain Current	V <sub>DS</sub> =30V, V <sub>GS</sub> =0V,	-	-	1.0	μΑ
I <sub>GSS</sub>	Gate to Body Leakage Current	V <sub>DS</sub> =0V, V <sub>GS</sub> =±20V	-	-	±100	nA
V <sub>GS(th)</sub>	Gate Threshold Voltage	V <sub>DS</sub> =V <sub>GS</sub> , I <sub>D</sub> =250µA	1.0	1.5	2.5	V
В	Static Drain-Source on-Resistance	V <sub>GS</sub> =10V, I <sub>D</sub> =10A	-	10	13	mΩ
$R_{DS(on)}$	note3	V <sub>GS</sub> =4.5V, I <sub>D</sub> =5A	-	16	22.5	
C <sub>iss</sub>	Input Capacitance	\\ 45\\\\\ 0\\\	-	633	-	pF
Coss	Output Capacitance	V <sub>DS</sub> =15V, V <sub>GS</sub> =0V, f=1.0MHz	-	120	-	pF
C <sub>rss</sub>	Reverse Transfer Capacitance	1-1.UIVITZ	-	99	-	pF
Qg	Total Gate Charge	\/ -45\/   -400	-	15	-	nC
Q <sub>gs</sub>	Gate-Source Charge	$V_{DS}$ =15V, $I_{D}$ =10A, $V_{GS}$ =10V	-	4.7	-	nC
$Q_{gd}$	Gate-Drain("Miller") Charge	VGS-10V	-	3.6	-	nC
t <sub>d(on)</sub>	Turn-on Delay Time		-	5	-	ns
t <sub>r</sub>	Turn-on Rise Time	$V_{DS}$ =30V, $I_{D}$ =18A, $R_{GEN}$ =3 $\Omega$ , $V_{GS}$ =10V	-	8	-	ns
t <sub>d(off)</sub>	Turn-off Delay Time		-	21	-	ns
t <sub>f</sub>	Turn-off Fall Time		-	7	-	ns
Is	Maximum Continuous Drain to Source Diode Forward Current		-	-	11.5	Α
Ism	Maximum Pulsed Drain to Source Diode Forward Current		-	-	72	Α
\/	Drain to Source Diode Forward				4.0	V
V <sub>SD</sub>	Voltage	V <sub>GS</sub> =0V, I <sub>S</sub> =18A	-	-	1.2	V
trr	Body Diode Reverse Recovery Time		-	7	-	ns
Qrr	Body Diode Reverse Recovery	I <sub>F</sub> =18A,dI/dt=100A/μs	-	5.9	-	nC
QII	Charge					

### Note:

<sup>1.</sup> The data tested by surface mounted on a 1 inch $^2$  FR-4 board with 2OZ copper. 2. The data tested by pulsed , pulse width  $\leq$  300us , duty cycle  $\leq$  2%

<sup>3.</sup> The EAS data shows Max. rating . The test condition is  $V_{DD}$ =25V, $V_{GS}$ =10V,L=0.1mH,I<sub>AS</sub>=20A

<sup>4.</sup> The power dissipation is limited by 150  $^{\circ}$ C junction temperature

<sup>5.</sup> The data is theoretically the same as  $I_D$  and  $I_{DM}$ , in real applications, should be limited by total power dissipation.

# **Typical Electrical And Thermal Characteristics**

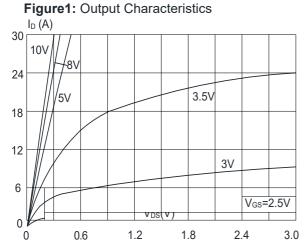
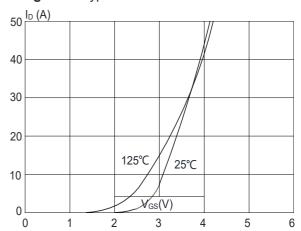


Figure 2: Typical Transfer Characteristics



**Figure 3:**On-resistance vs. Drain Current  $Ros(on) (m\Omega)$ 

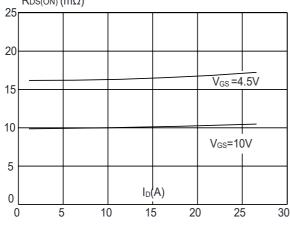


Figure 4: Body Diode Characteristics

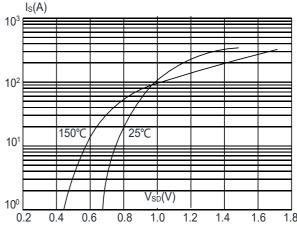
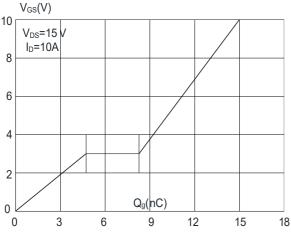
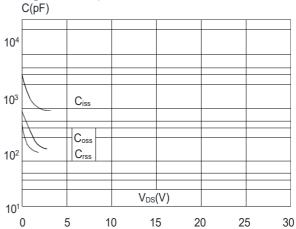


Figure 5: Gate Charge Characteristics



**Figure 6:** Capacitance Characteristics





**Figure 7:** Normalized Breakdown Voltage vs. Junction Temperature

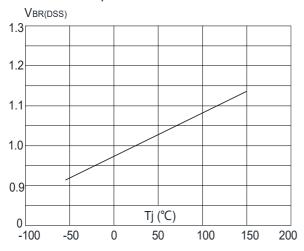
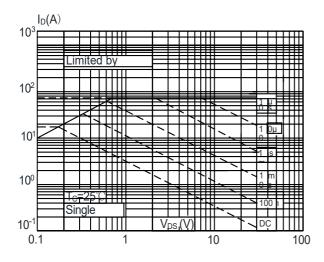
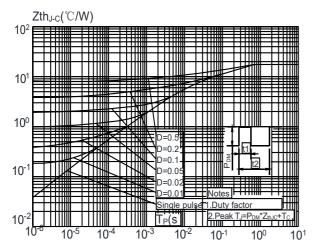


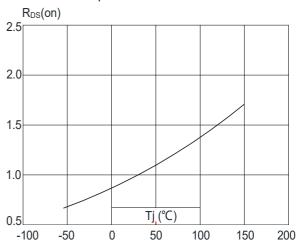
Figure 9: Maximum Safe Operating Area



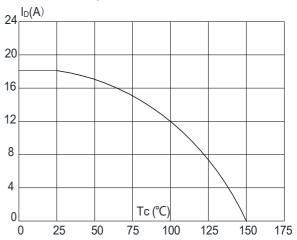
**Figure.11:** Maximum Effective Transient Thermal Impedance, Junction-to-Case

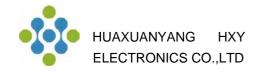


**Figure 8:** Normalized on Resistance vs. Junction Temperature

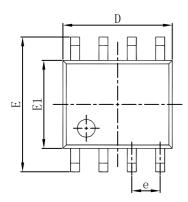


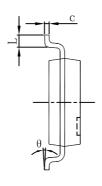
**Figure 10:** Maximum Continuous Drain Current vs. Case Temperature

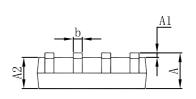




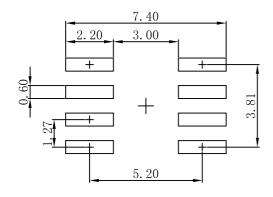
# **SOP-8 Package Outline Dimensions**







Symbol	Dimensions In Millimeters		Dimensions In Inches		
Symbol	Min	Max	Min	Max	
A	1.350	1.750	0.053	0.069	
A1	0.100	0.250	0.004	0.010	
A2	1.350	1.550	0.053	0.061	
b	0.330	0.510	0.013	0.020	
c	0.170	0.250	0.007	0.010	
D	4.800	5.000	0.189	0.197	
e	1. 270 (	1.270 (BSC)		0.050 (BSC)	
E	5.800	6. 200	0. 228	0.244	
E1	3.800	4.000	0.150	0. 157	
L	0.400	1.270	0.016	0.050	
θ	0°	8°	0°	8°	



- Note: 1.Controlling dimension: in millimeters.
- 2.General tolerance:± 0.05mm.
  3.The pad layout is for reference purposes only.

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