

## **Description**

The BSC030P03NS3G uses advanced trench technology to provide excellent R<sub>DS(ON)</sub>, low gate charge and operation with gate voltages as low as 4.5V. This device is suitable for use as a Battery protection or in other Switching application.

### **General Features**

 $V_{DS} = -30V I_{D} = -100A$ 

 $R_{DS(ON)} < 4 \text{ m}\Omega \text{ V}_{GS} = -10 \text{V}$ 

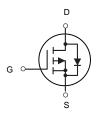
### **Application**

Battery protection

Load switch

Uninterruptible power supply

DFN5X6-8L



P-Channel MOSFET

## **Package Marking and Ordering Information**

Product ID	Pack	Brand	Qty(PCS)
BSC030P03NS3G	DFN5X6-8L	HXY MOSFET	5000

## Absolute Maximum Ratings (Tc=25 ℃ unless otherwise noted)

Symbol	Parameter	Rating	Units	
VDS	Drain-Source Voltage	-30	V	
Vgs	Gate-Source Voltage	V		
I <sub>D</sub> @T <sub>C</sub> =25°C	Continuous Drain Current, V <sub>GS</sub> @ 10V <sup>1</sup>	Α		
Io@Tc=100°C	Continuous Drain Current, V <sub>GS</sub> @ 10V <sup>1</sup>	-70	Α	
Ірм	Pulsed Drain Current <sup>2</sup> -250		А	
EAS	Single Pulse Avalanche Energy <sup>3</sup> 80		mJ	
las	Avalanche Current	Avalanche Current -70		
P <sub>D</sub> @T <sub>C</sub> =25°C	Total Power Dissipation <sup>4</sup>	120	W	
Тѕтс	Storage Temperature Range -55 to 150		°C	
TJ	Operating Junction Temperature Range -55 to 150		°C	
Reja	Thermal Resistance Junction-Ambient <sup>1</sup>	ermal Resistance Junction-Ambient <sup>1</sup> 50		
Rejc	Thermal Resistance Junction-Case <sup>1</sup>		°C/W	

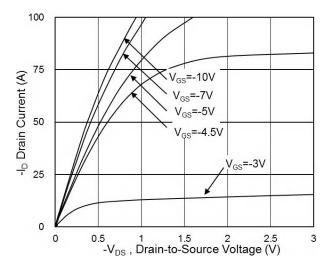
## Electrical Characteristics (T<sub>J</sub>=25 °C, unless otherwise noted)

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
$BV_DSS$	Drain-Source Breakdown Voltage	$V_{GS}$ =0V , $I_D$ =-250uA	-30			V
В	Static Drain-Source On-Resistance <sup>2</sup>	V <sub>GS</sub> =-10V , I <sub>D</sub> =-20A		3	4.0	mΩ
$R_{DS(ON)}$	Static Drain-Source On-Resistance-	V <sub>GS</sub> =-4.5V , I <sub>D</sub> =-15A		4.2	6.0	mΩ
V <sub>GS(th)</sub>	Gate Threshold Voltage	$V_{GS}=V_{DS}$ , $I_D=-250uA$	-1.2		-2.5	V
	Drain-Source Leakage Current	V <sub>DS</sub> =-24V , V <sub>GS</sub> =0V , T <sub>J</sub> =25°C			-1	uA
I <sub>DSS</sub>		V <sub>DS</sub> =-24V , V <sub>GS</sub> =0V , T <sub>J</sub> =55°C			-5	
I <sub>GSS</sub>	Gate-Source Leakage Current	V <sub>GS</sub> =±20V , V <sub>DS</sub> =0V			±100	nA
Rg	Gate Resistance	V <sub>DS</sub> =0V , V <sub>GS</sub> =0V , f=1MHz		1.2		Ω
Qg	Total Gate Charge (-10V)			60		
$Q_{gs}$	Gate-Source Charge	V <sub>DS</sub> =-15V , V <sub>GS</sub> =-10V , I <sub>D</sub> =-18A		9		nC
$Q_{gd}$	Gate-Drain Charge			15		
$T_{d(on)}$	Turn-On Delay Time			17		
T <sub>r</sub>	Rise Time	$V_{DD}$ =-15V , $V_{GS}$ =-10V , $R_{G}$ =3.3 $\Omega$ ,		40		ns
T <sub>d(off)</sub>	Turn-Off Delay Time	I <sub>D</sub> =-20A		55		
T <sub>f</sub>	Fall Time			13		
C <sub>iss</sub>	Input Capacitance			3450		
C <sub>oss</sub>	Output Capacitance	V <sub>DS</sub> =-25V , V <sub>GS</sub> =0V , f=1MHz		255		pF
$C_{rss}$	Reverse Transfer Capacitance			140		
Is	Continuous Source Current <sup>1,5</sup>	V <sub>G</sub> =V <sub>D</sub> =0V , Force Current			-100	Α
$V_{\text{SD}}$	Diode Forward Voltage <sup>2</sup>	$V_{GS}$ =0 $V$ , $I_{S}$ =-1 $A$ , $T_{J}$ =25 $^{\circ}$ C			-1.2	V
t <sub>rr</sub>	Reverse Recovery Time	IF=-20A , di/dt=100A/μs ,		22		nS
Q <sub>rr</sub>	Reverse Recovery Charge	T <sub>J</sub> =25℃		72		nC

### Note:

- 1.The data tested by surface mounted on a 1 inch<sup>2</sup> FR-4 board with 2OZ copper.
- 2.The data tested by pulsed , pulse width  $\leqq 300 us$  , duty cycle  $\leqq 2\%$
- 3.The EAS data shows Max. rating . The test condition is  $V_{DD}$ =-50V, $V_{GS}$ =-10V,L=0.1mH,I<sub>AS</sub>=-40A 4.The power dissipation is limited by 150°C junction temperature
- 5. The data is theoretically the same as  $I_D$  and  $I_{DM}$ , in real applications, should be limited by total power dissipation
- 6. The maximum current rating is package limited.

## **Typical Characteristics**



**Fig.1 Typical Output Characteristics** 

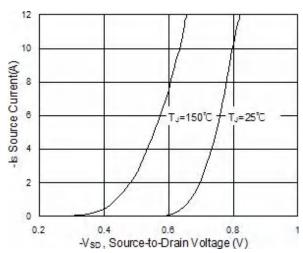


Fig.3 Source Drain Forward Characteristics

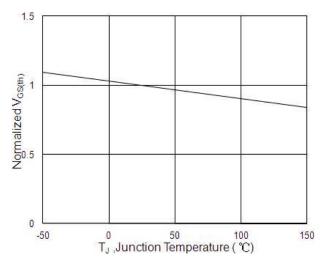


Fig.5 Normalized - $V_{GS(th)}$  vs  $T_J$ 

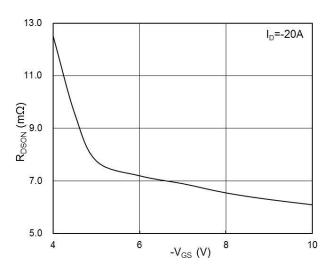


Fig.2 On-Resistance vs G-S Voltage

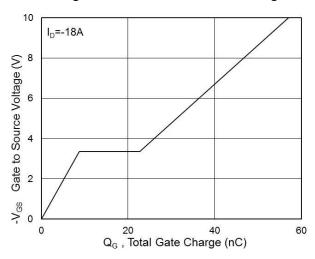


Fig.4 Gate-Charge Characteristics

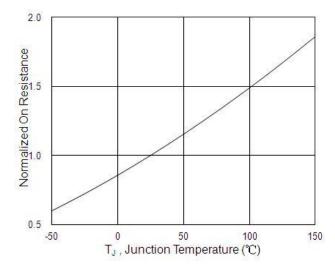
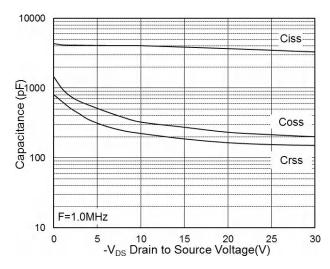


Fig.6 Normalized R<sub>DSON</sub> vs T<sub>J</sub>



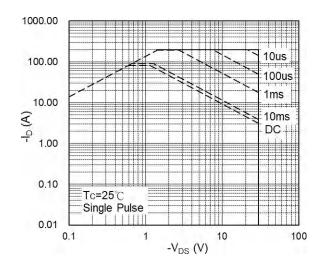


Fig.7 Capacitance

Fig.8 Safe Operating Area

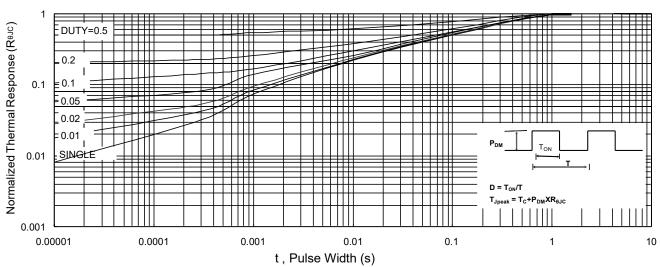


Fig.9 Normalized Maximum Transient Thermal Impedance

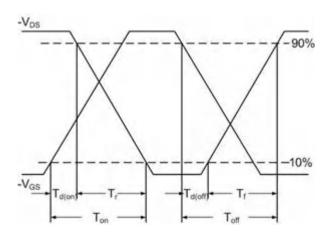


Fig.10 Switching Time Waveform

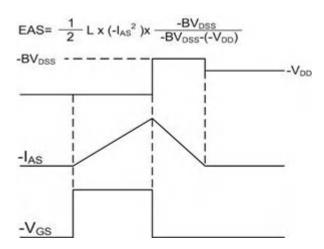
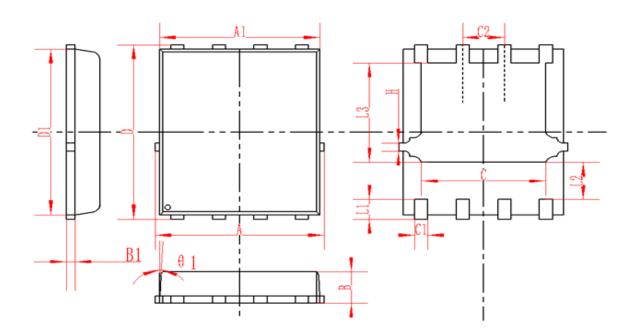
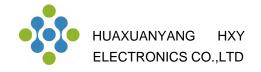


Fig.11 Unclamped Inductive Switching Waveform

# **DFN5X6-8L Package Information**



SYMBOL	MM		INCH			
	MIN	NOM	MAX	MIN	NOM	MAX
Α	4.95	5	5.05	0.195	0.197	0.199
A1	4.82	4.9	4.98	0.190	0.193	0.196
D	5.98	6	6.02	0.235	0.236	0.237
D1	5.67	5.75	5.83	0.223	0.226	0.230
В	0.9	0.95	1	0.035	0.037	0.039
B1	0.254REF		0.010REF			
С	3.95	4	4.05	0.156	0.157	0.159
C1	0.35	0.4	0.45	0.014	0.016	0.018
C2		1.27TYP			0.5TYP	
θ1	8°	10°	12°	8°	10°	12°
L1	0.63	0.64	0.65	0.025	0.025	0.026
L2	1.2	1.3	1.4	0.047	0.051	0.055
L3	3.415	3.42	3.425	0.134	0.135	0.135
Н	0.24	0.25	0.26	0.009	0.010	0.010



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